

HP9816

| A           | B       | C            |
|-------------|---------|--------------|
| -12V → 1    | → Gnd   | → +12V       |
| BD14 → 2    | → BD15  | → +5V        |
| BD12 → 3    | → BD13  | → +5V        |
| BD10 → 4    | → BD11  | → +5V        |
| BD8 → 5     | → BD9   | → +5V        |
| BD6 → 6     | → BD7   | → +5V        |
| BD4 → 7     | → BD5   | → +5V        |
| BD2 → 8     | → BD3   | → +5V        |
| BD0 → 9     | → BA23  | → BD1        |
| BA22 → 10   | → BA21  | → SpA        |
| BA20 → 11   | → BA19  | → SpB        |
| BA18 → 12   | → BA17  | → Gnd        |
| BA16 → 13   | → BA15  | → Gnd        |
| BA14 → 14   | → BA13  | → Gnd        |
| BA12 → 15   | → BA11  | → Gnd        |
| BA10 → 16   | → BA9   | → Gnd        |
| BA8 → 17    | → BA7   | → Gnd        |
| BA6 → 18    | → BA5   | → Gnd        |
| BA4 → 19    | → BA3   | → Gnd        |
| BA2 → 20    | → BA1   | → Gnd        |
| HaIt → 21   | → Gnd   | → Gnd        |
| Reset → 22  | → BR/W  | → BLDS       |
| SpaCe1 → 23 | → BAS   | → Gnd        |
| B6 → 24     | → BUD5  | → BFC2       |
| Vector → 25 | → BErr  | → GraphicsCS |
| IR6 → 26    | → DTAck | → TextCS     |
| IR4 → 27    | → BFC1  | → Sound      |
| DMARdy → 28 | → EndT  | → BFC0       |
| IR2 → 29    | → IR5   | → BDRV       |
| SpaCe0 → 30 | → IR3   | → BGack      |
| DMACK0 → 31 | → IR1   | → BR         |
| DMAR0 → 32  | → IR7   | → Iack       |

Processor PCB → Backplane

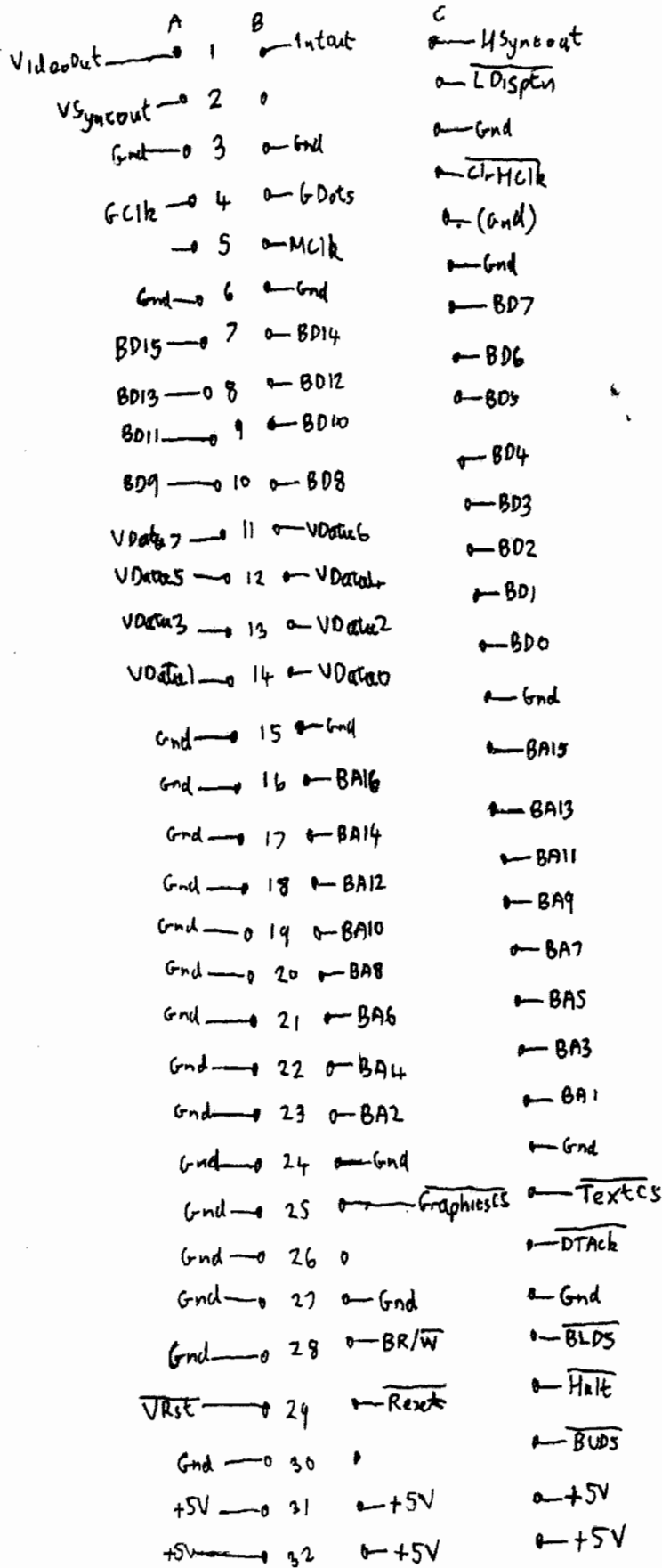
RMS  
↑  
→ Top

HP9816

| A          |    | B      |  |
|------------|----|--------|--|
| -12V       | 1  | +12V   |  |
| DGnd       | 2  | DGnd   |  |
| +5V        | 3  | +5V    |  |
| +5V        | 4  | +5V    |  |
| +5V        | 5  | +5V    |  |
| +5V        | 6  | +5V    |  |
| SpA        | 7  | SpB    |  |
| Gnd        | 8  | Gnd    |  |
| BD15       | 9  | BD14   |  |
| BD13       | 10 | BD12   |  |
| BD11       | 11 | BD10   |  |
| BD9        | 12 | BD8    |  |
| BD7        | 13 | BD6    |  |
| BD5        | 14 | BD4    |  |
| BD3        | 15 | BD2    |  |
| BD1        | 16 | BD0    |  |
| Gnd        | 17 | Gnd    |  |
| BA16       | 18 | BA15   |  |
| BA14       | 19 | BA13   |  |
| BA12       | 20 | BA11   |  |
| BA10       | 21 | BA9    |  |
| BA8        | 22 | BA7    |  |
| BA6        | 23 | BA5    |  |
| BA4        | 24 | BA3    |  |
| BA2        | 25 | BA1    |  |
| BUDS       | 26 | Gnd    |  |
| GraphicsCS | 27 | TextCS |  |
| Sound      | 28 | DTAck  |  |
| Gnd        | 29 | Gnd    |  |
| BR/W       | 30 | BLDS   |  |
| Reset      | 31 | Halt   |  |
| Gnd        | 32 | Gnd    |  |

Monitor PCB → Backplane

RHS  
↑  
→ Top



Rear  
 ←  
 LHS

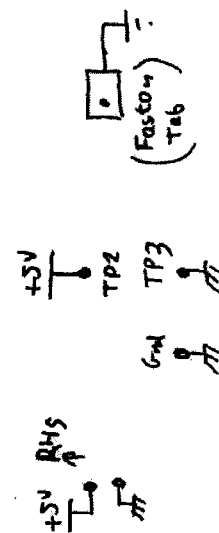
Text PCB → Monitor PCB

9816

| A             | B         |
|---------------|-----------|
| VSyncOut 1    | HSync Out |
| 2             | LDISP En  |
| Gnd 3         | Gnd       |
| 4             | CT-HCLK   |
| GCLK 5        | GDOTS     |
| Gnd 6         | Gnd       |
| BD15 7        | BD14      |
| BD13 8        | BD12      |
| BD11 9        | BD10      |
| BD9 10        | BD8       |
| VData7 11     | VData6    |
| VData5 12     | VData4    |
| VData3 13     | VData2    |
| VData1 14     | VData0    |
| Gnd 15        | Gnd       |
| BA16 16       | BA15      |
| BA14 17       | BA13      |
| BA12 18       | BA11      |
| BA10 19       | BA9       |
| BA8 20        | BA7       |
| BA6 21        | BA5       |
| BA4 22        | BA3       |
| BA2 23        | BA1       |
| Gnd 24        | Gnd       |
| GraphicsCS 25 | *         |
| 26            | DTACK     |
| Gnd 27        | Gnd       |
| BR/W 28       | BLDS      |
| BUDS 29       | VR5E      |
| Gnd 30        | Gnd       |
| +5V 31        | +5V       |
| +5V 32        | +5V       |

Pear  
 LH5

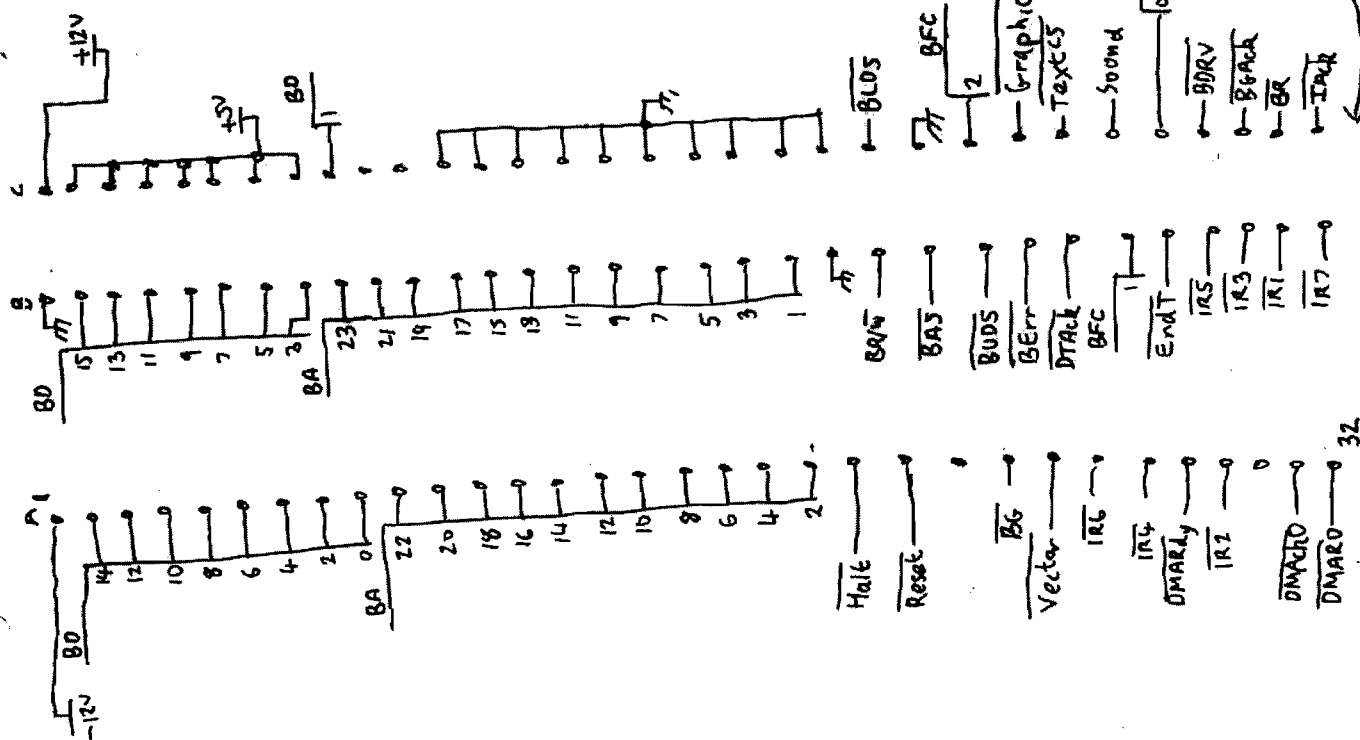
Graphics PCB → Monitor PCB

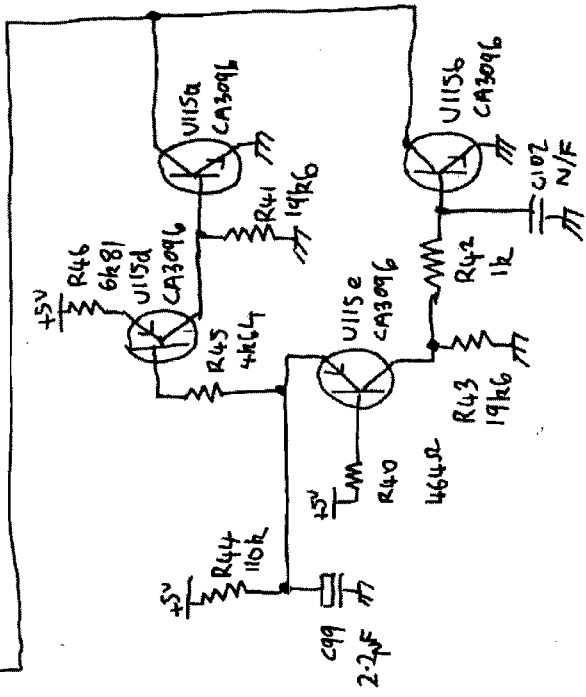
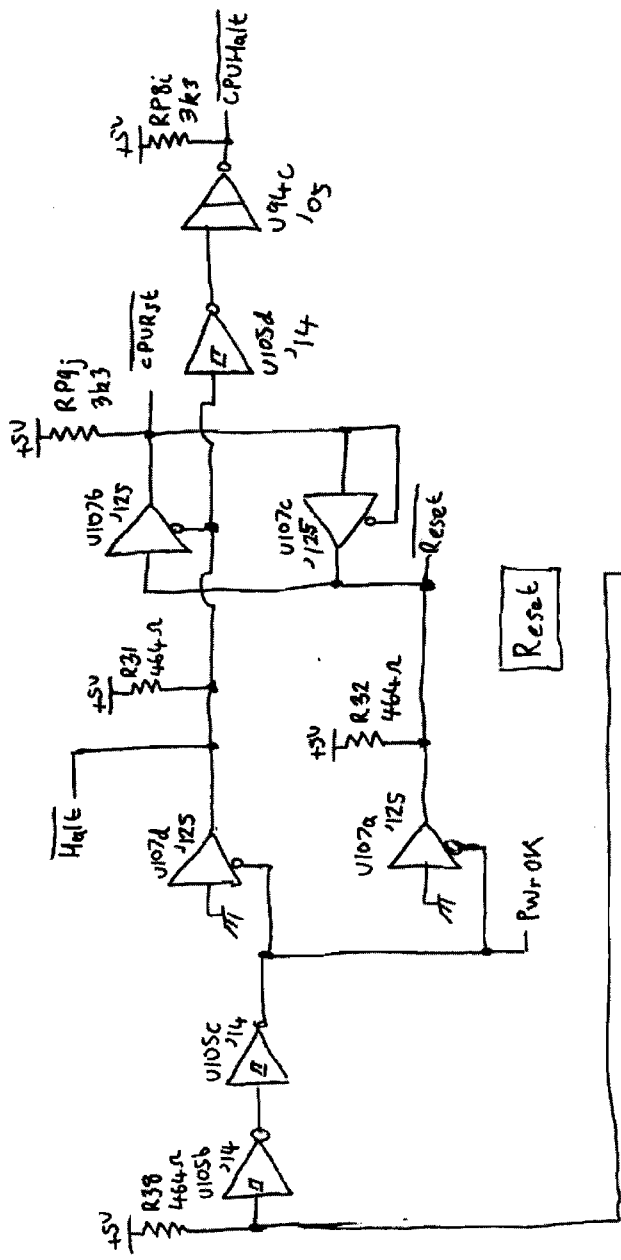


T1 (Backplane)

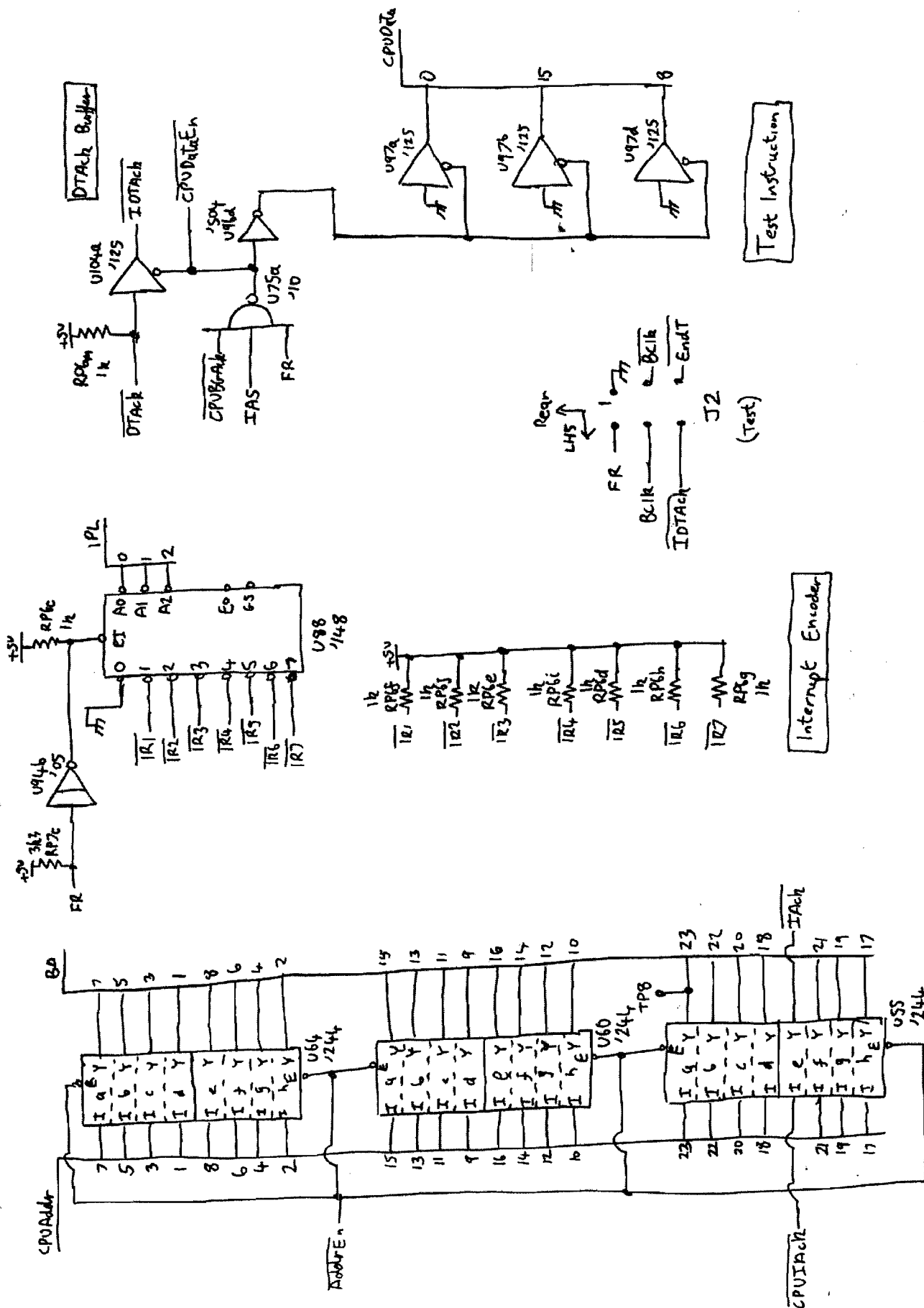
HP9816 Processor PCB Sheet ①

09816-66514





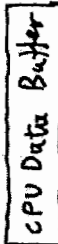


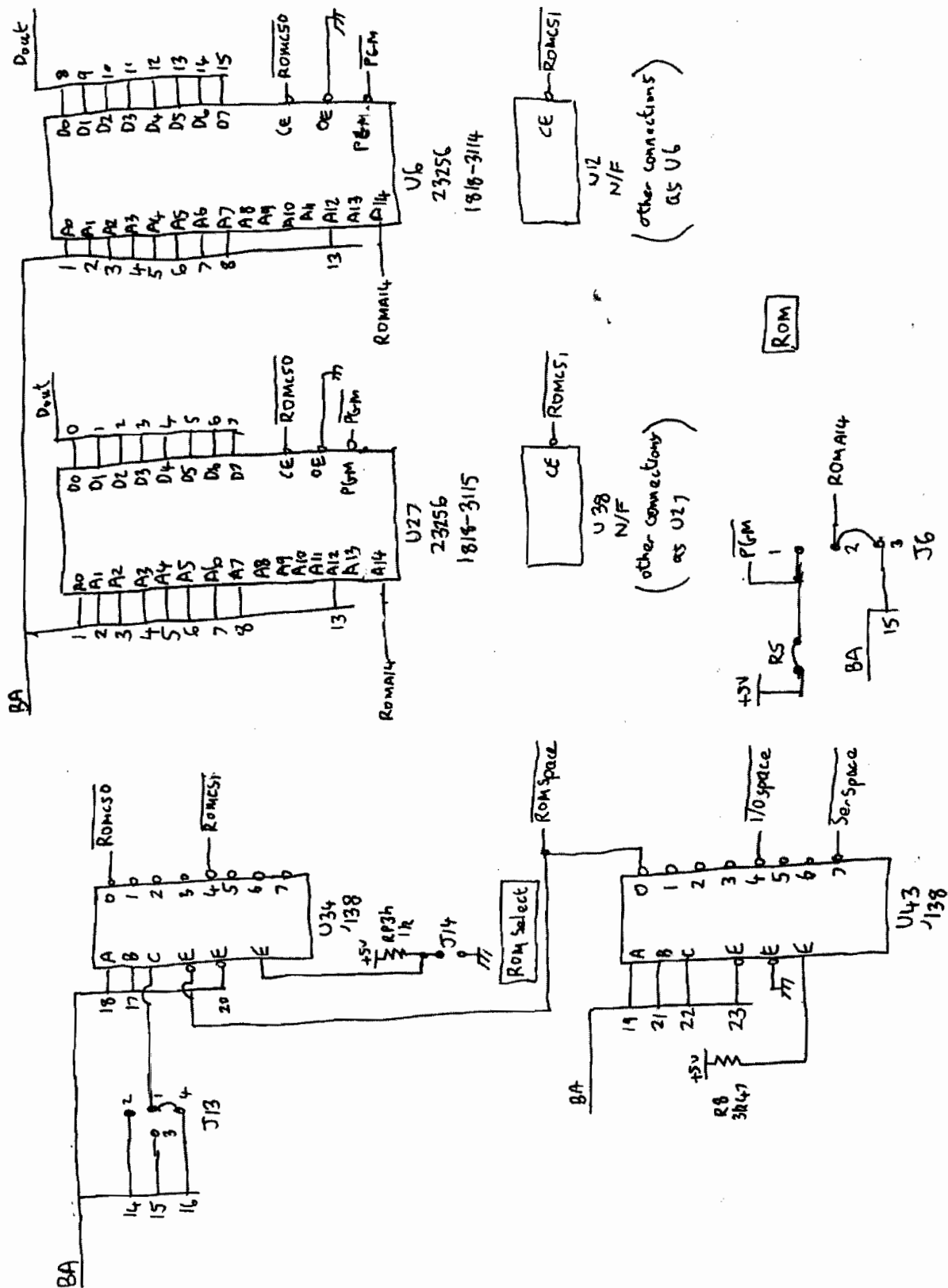


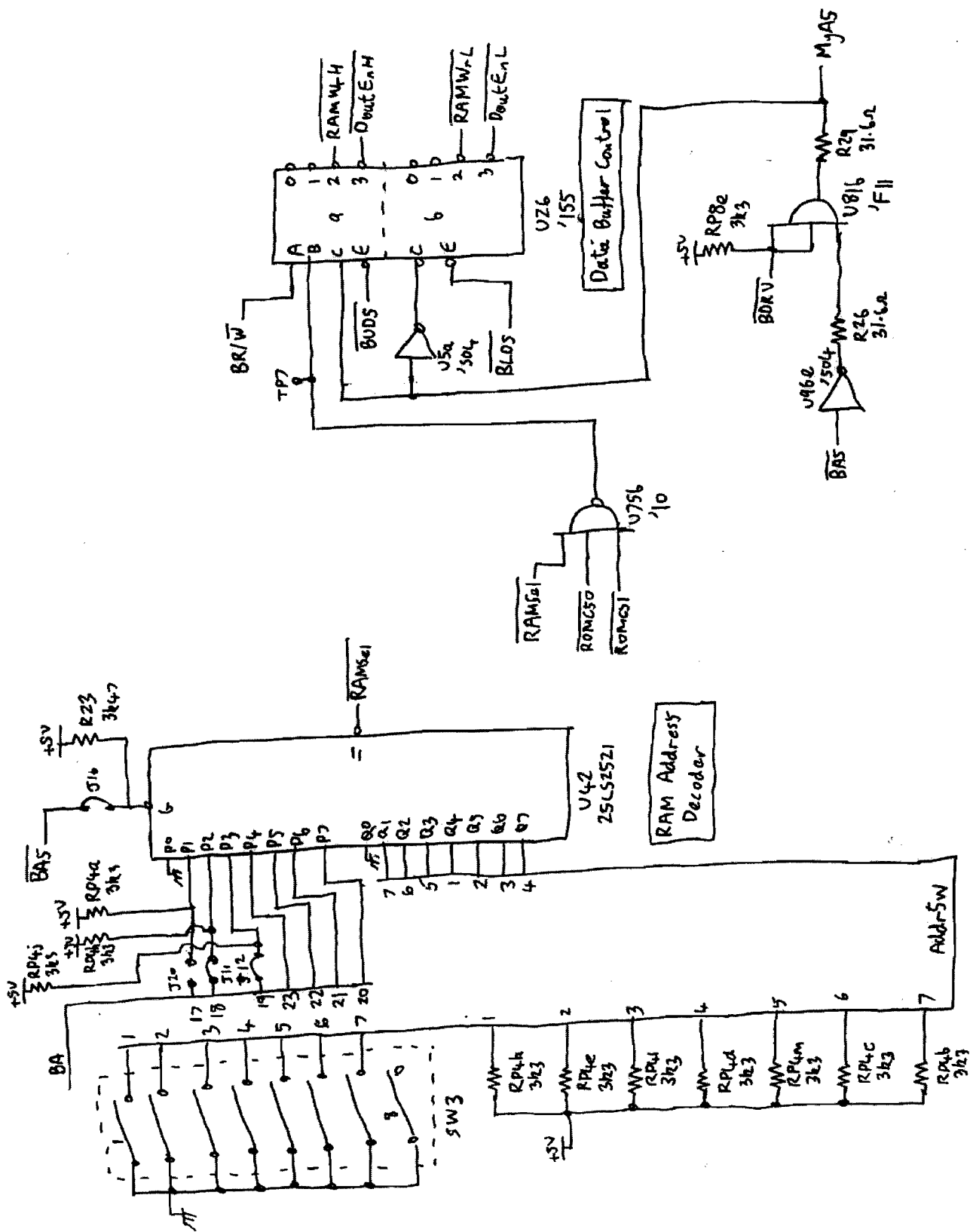
09816-66514

HP9816 Processor PCB sheet (4)

Address Buffers

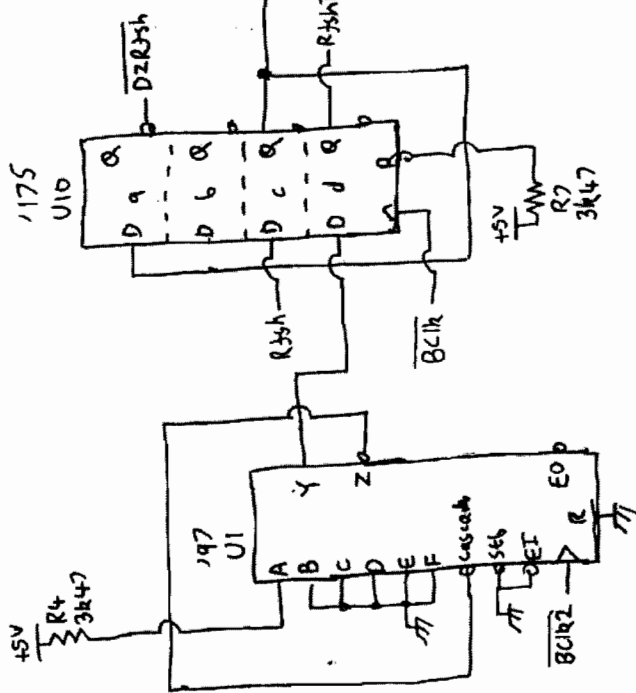
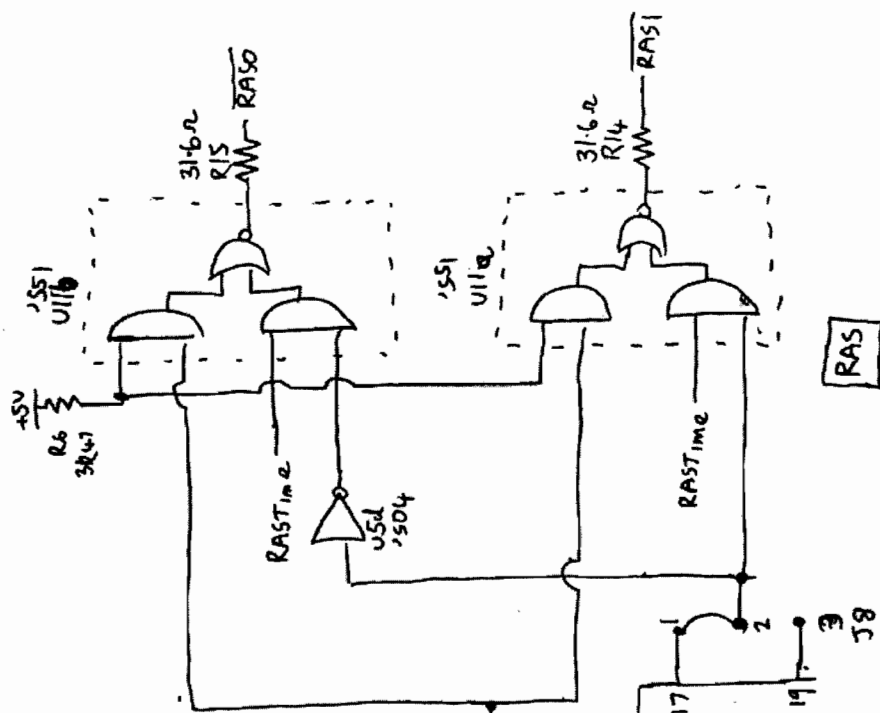




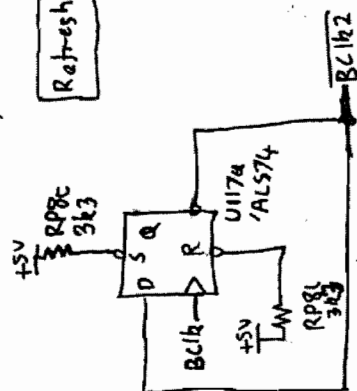


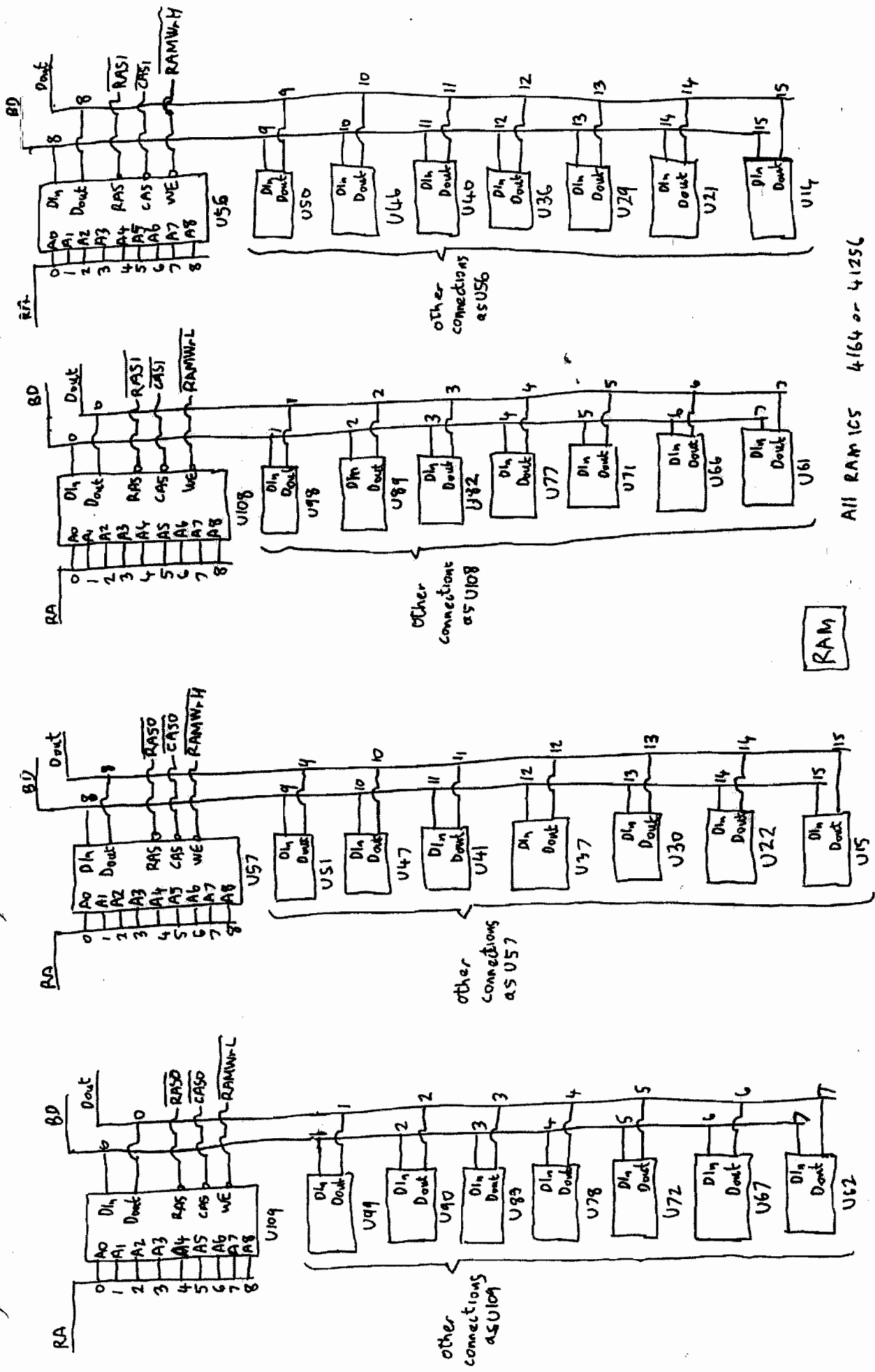




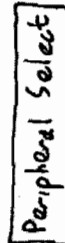


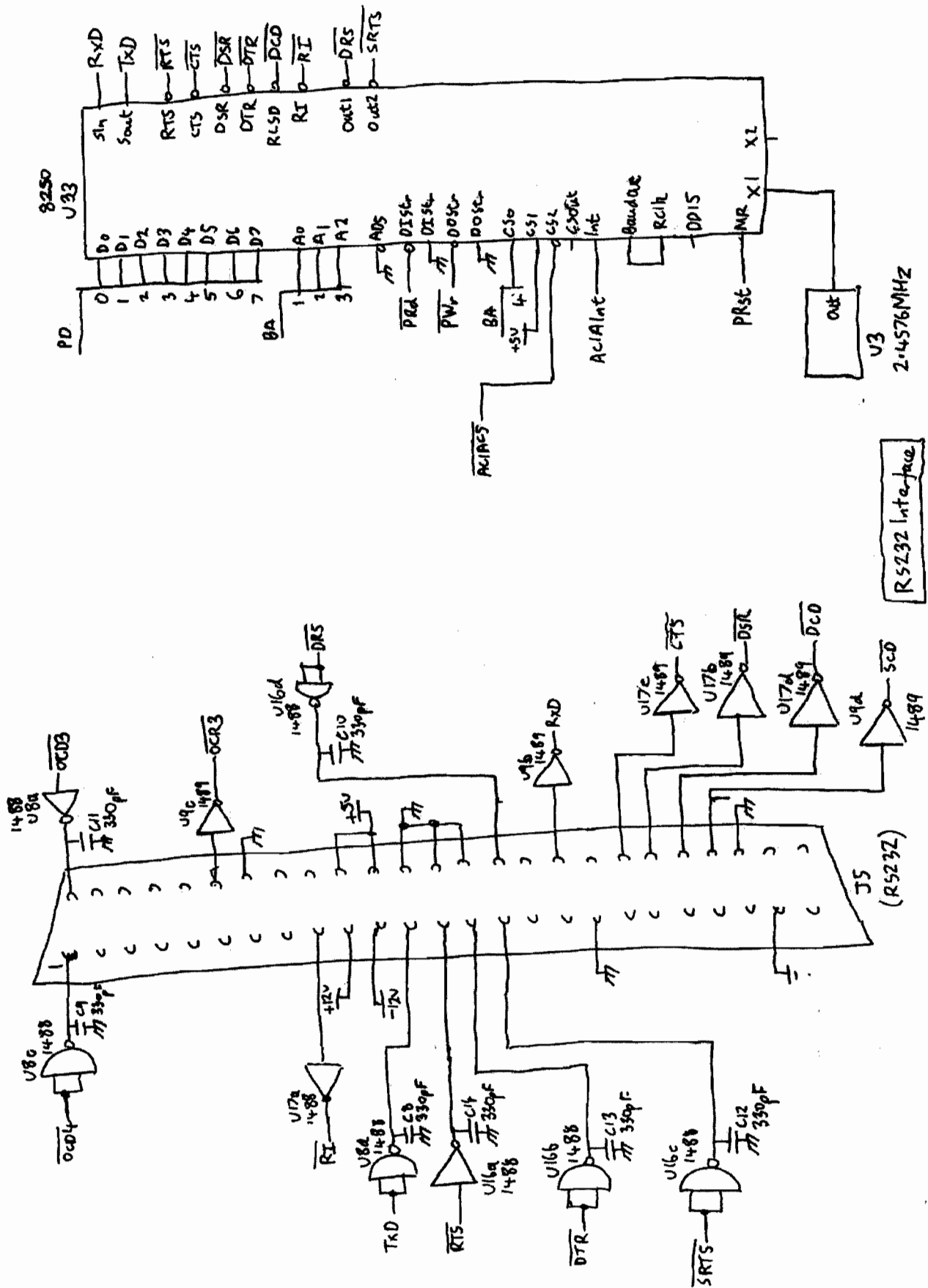
Refresh Timing

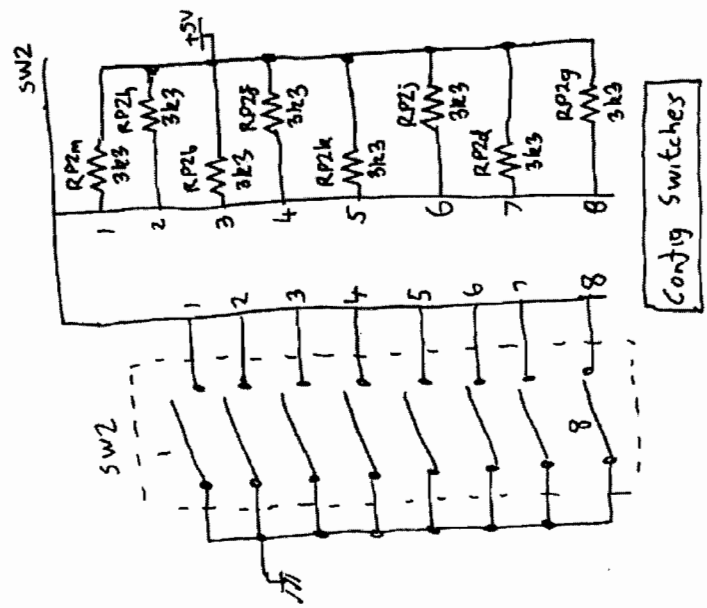
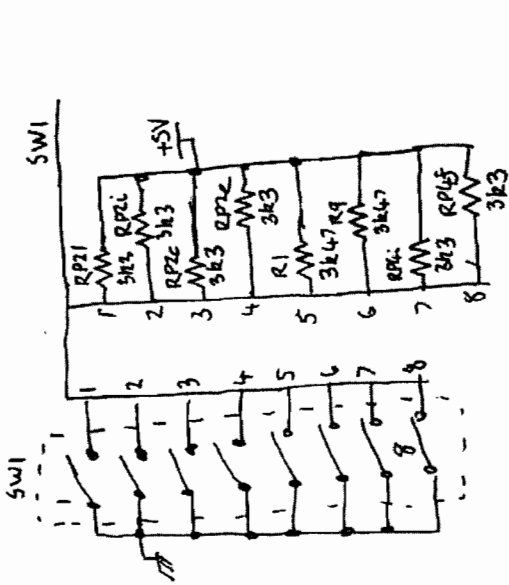




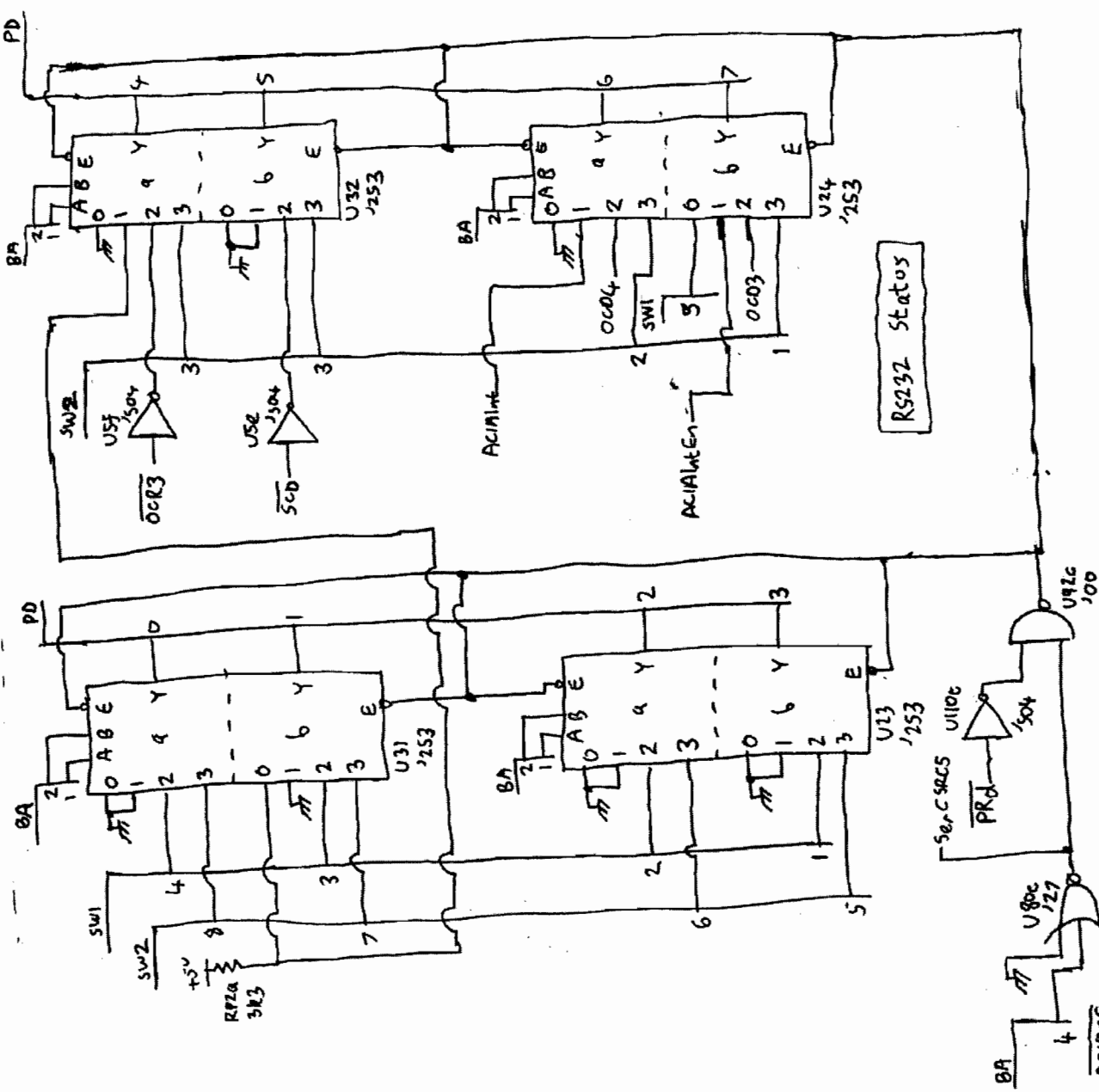
All RAM ICs 4164 or 4125C

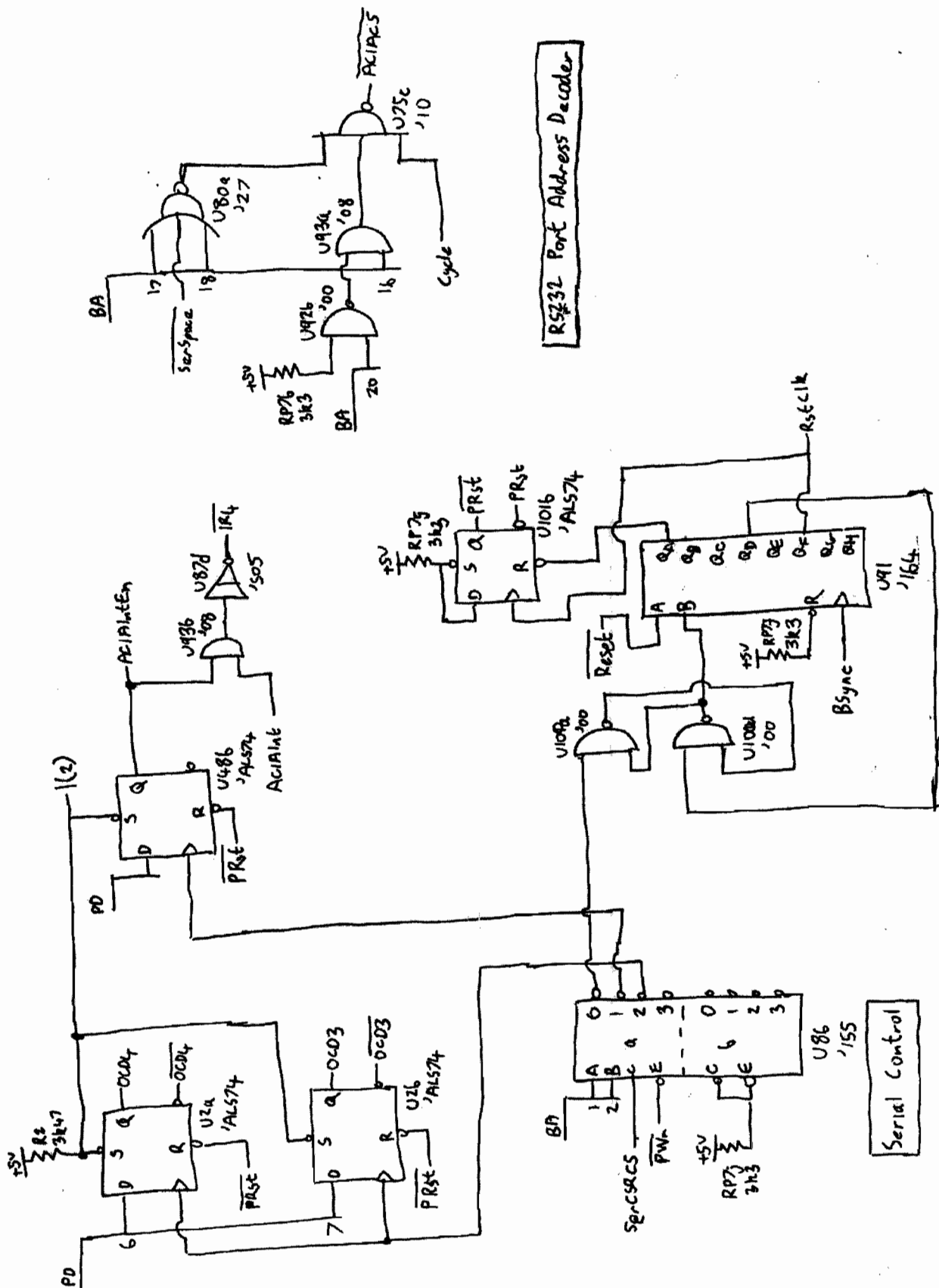






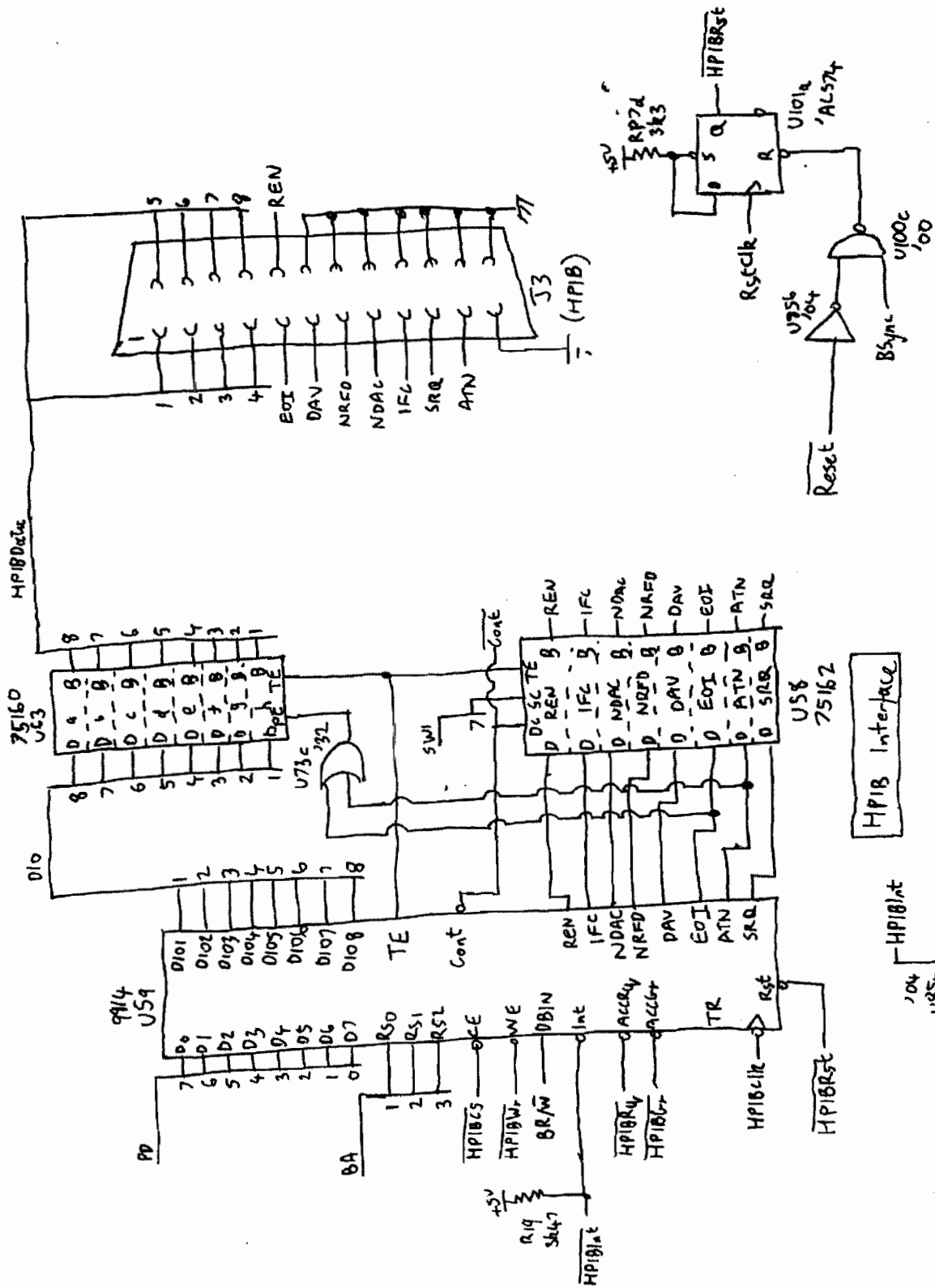
Config Switches

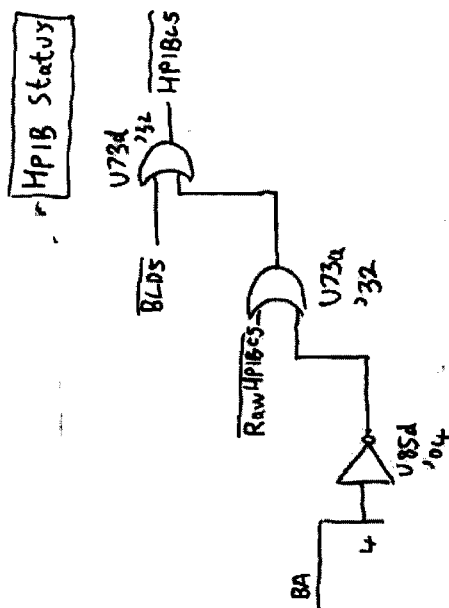
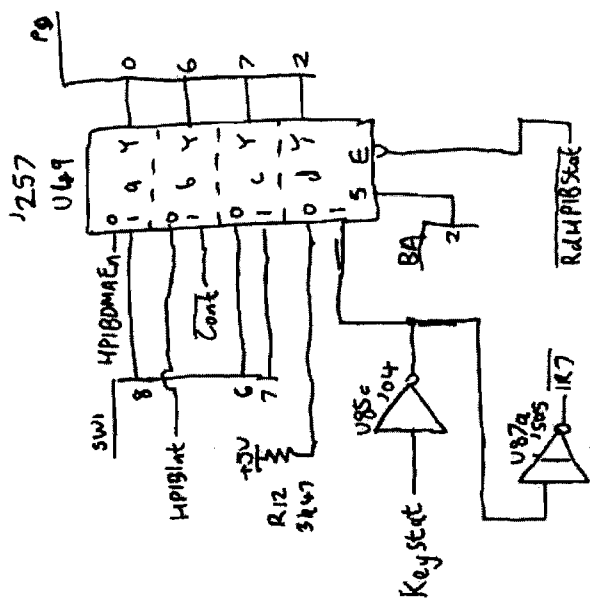
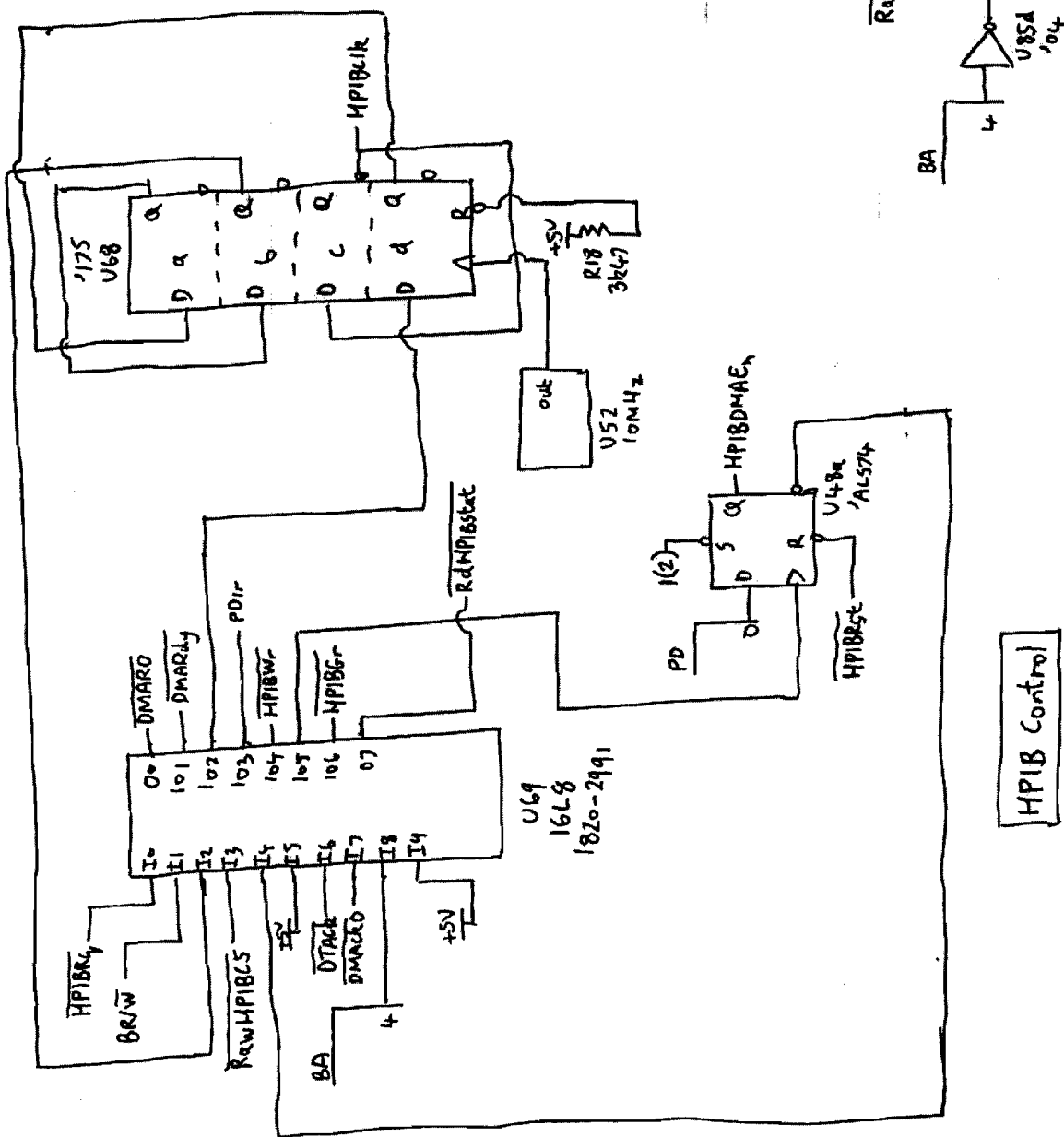


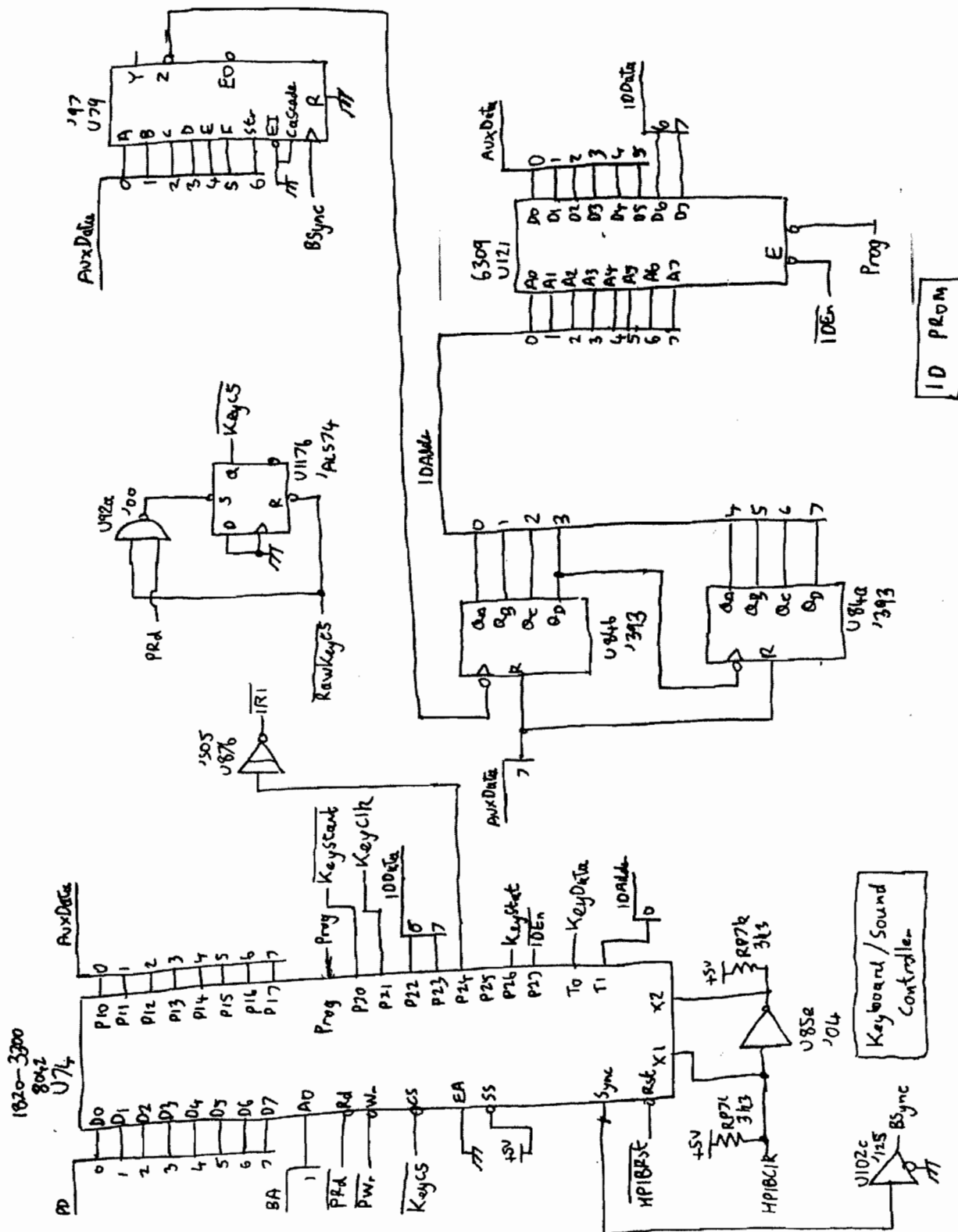


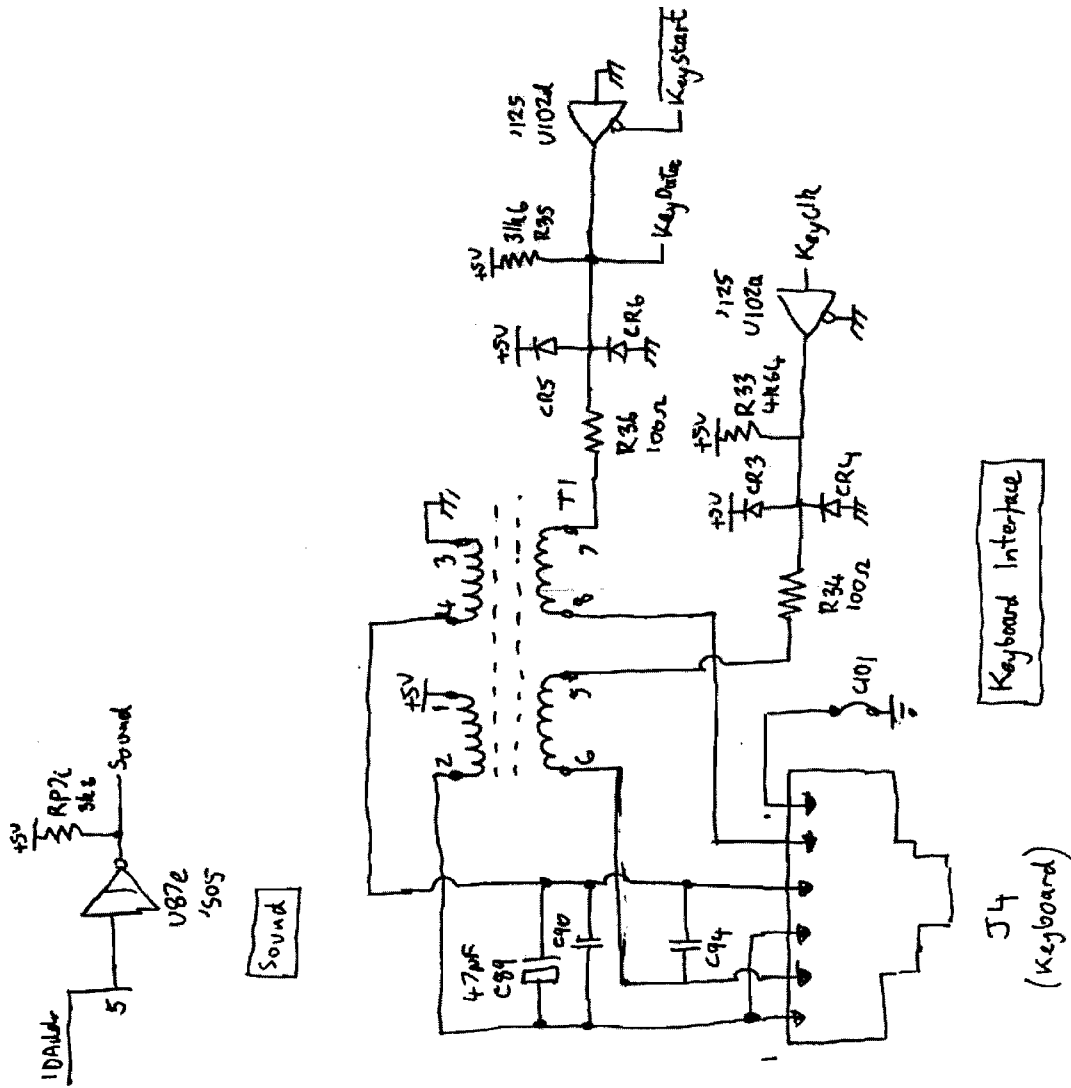
RS232 Port Address Decoder

Serial Control



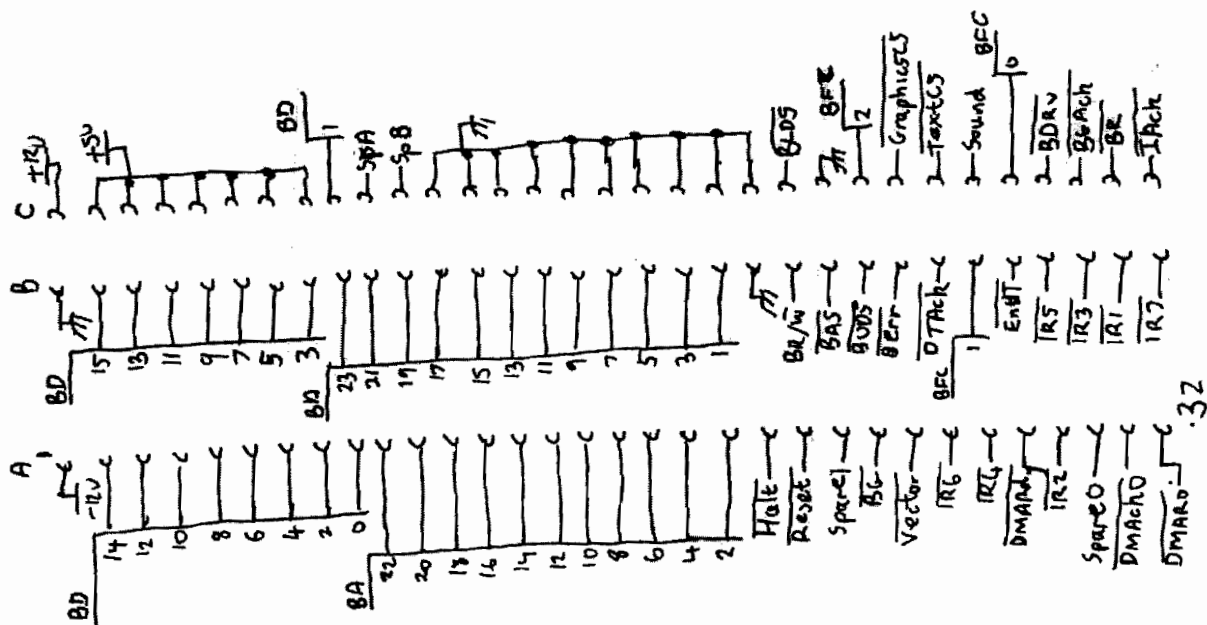








RMS  $\rightarrow$  Top

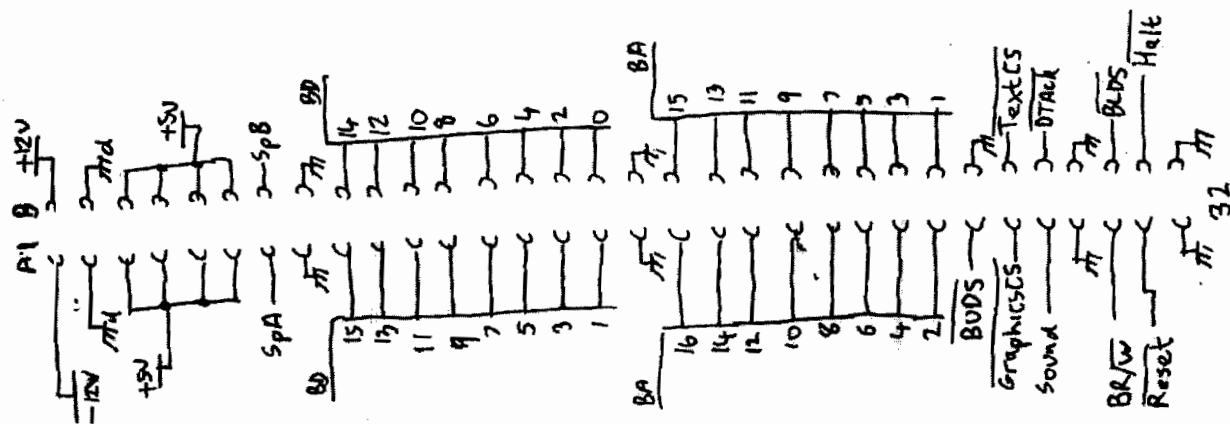


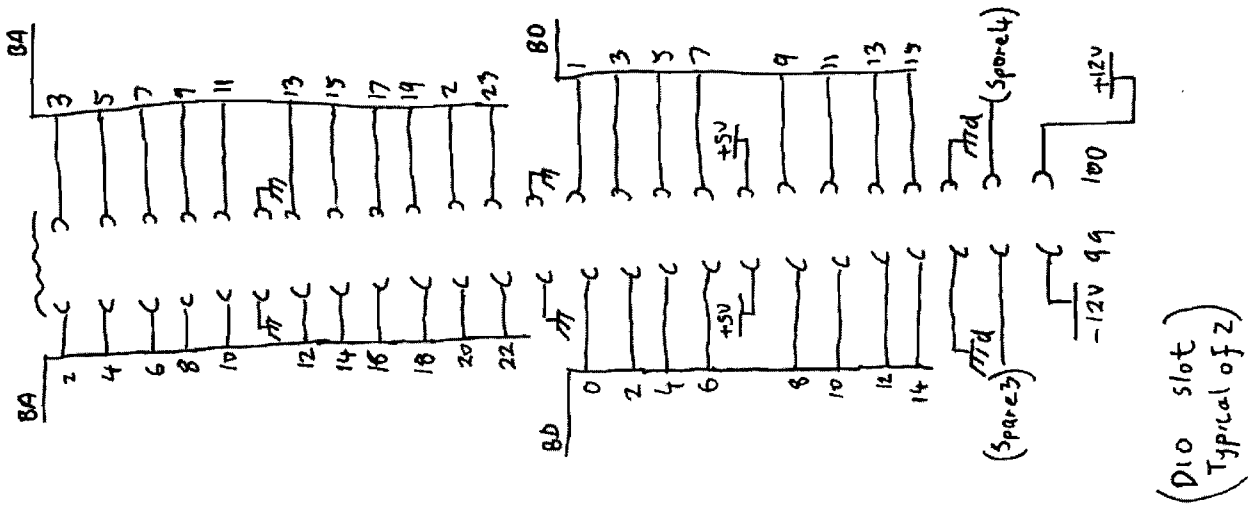
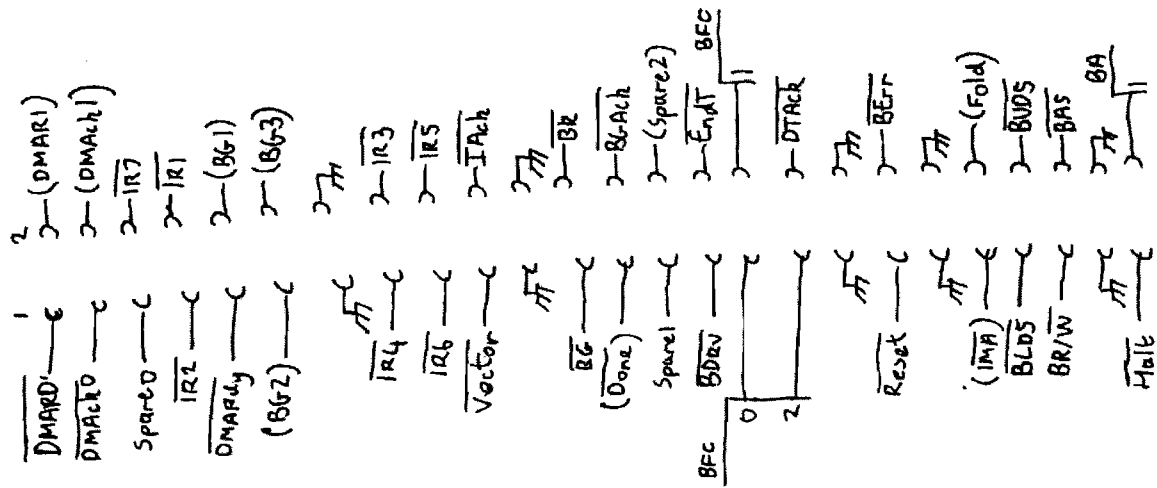
(Processor PCB)

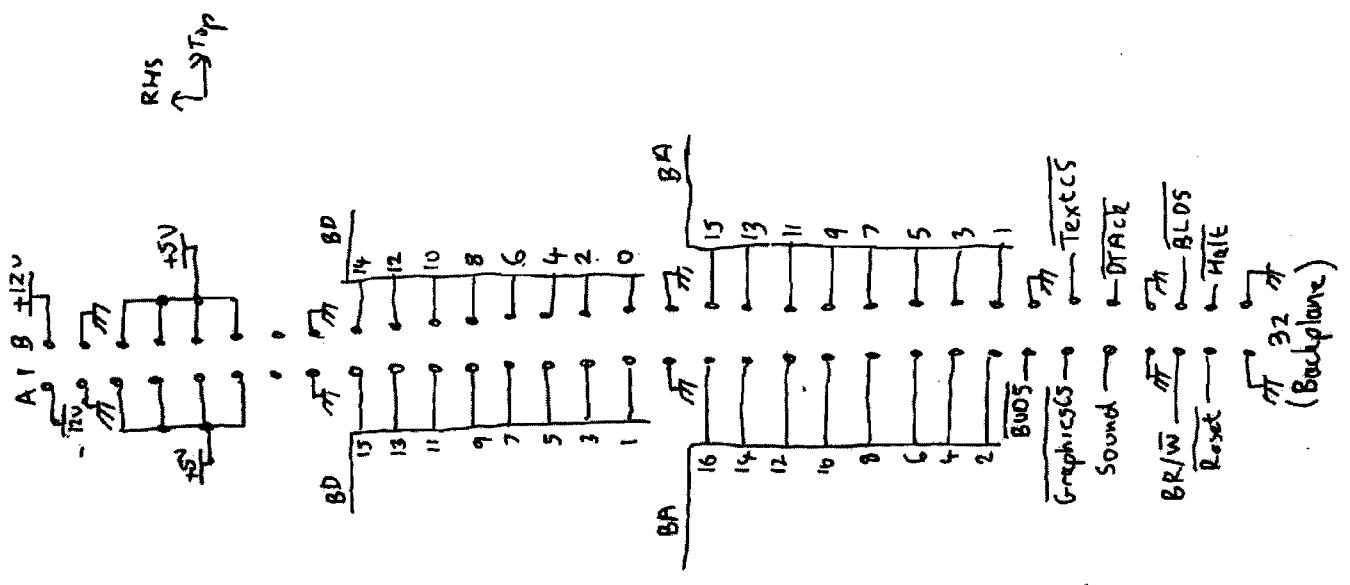
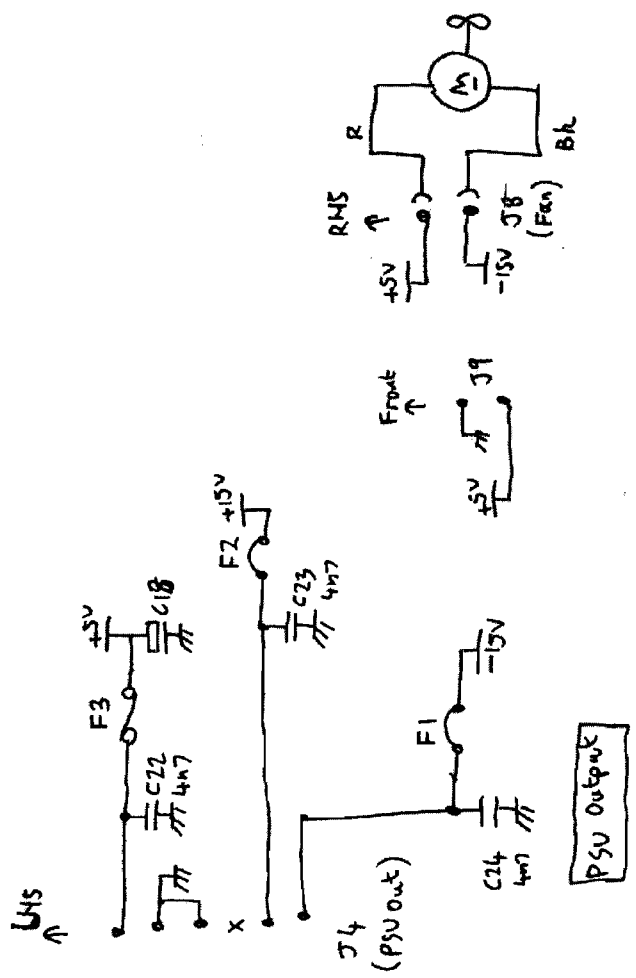
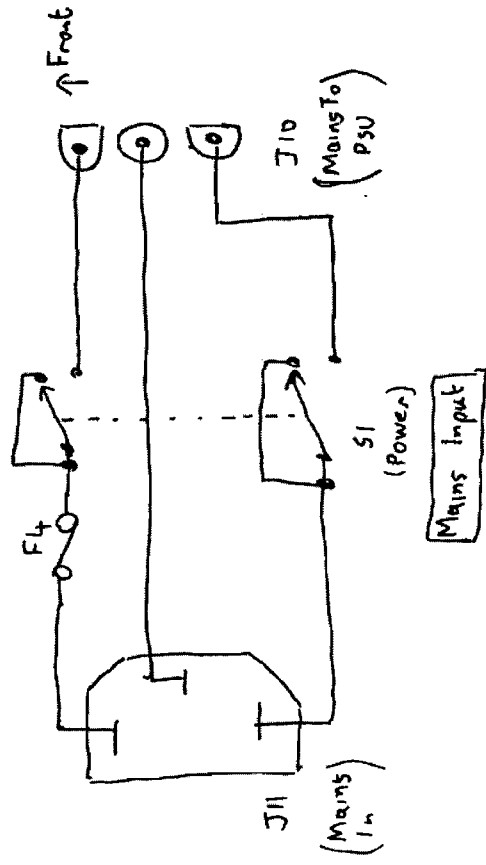
HP99816 Backplane sheet ①

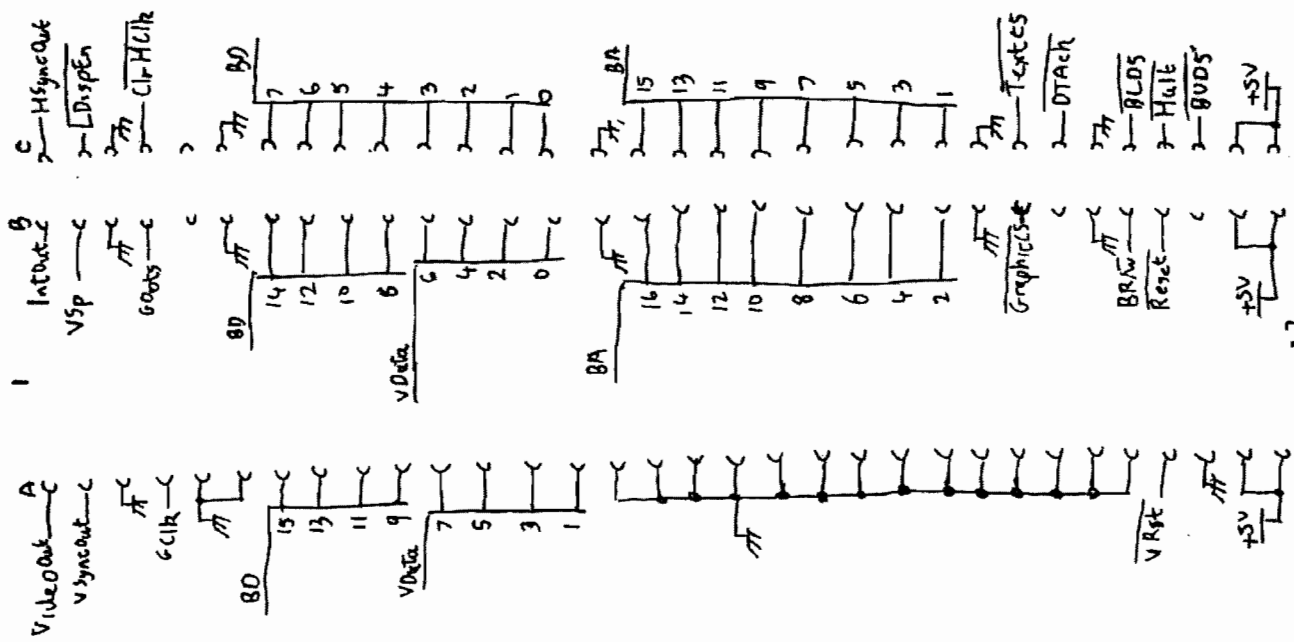
(Monitor PCB)

09816-66561



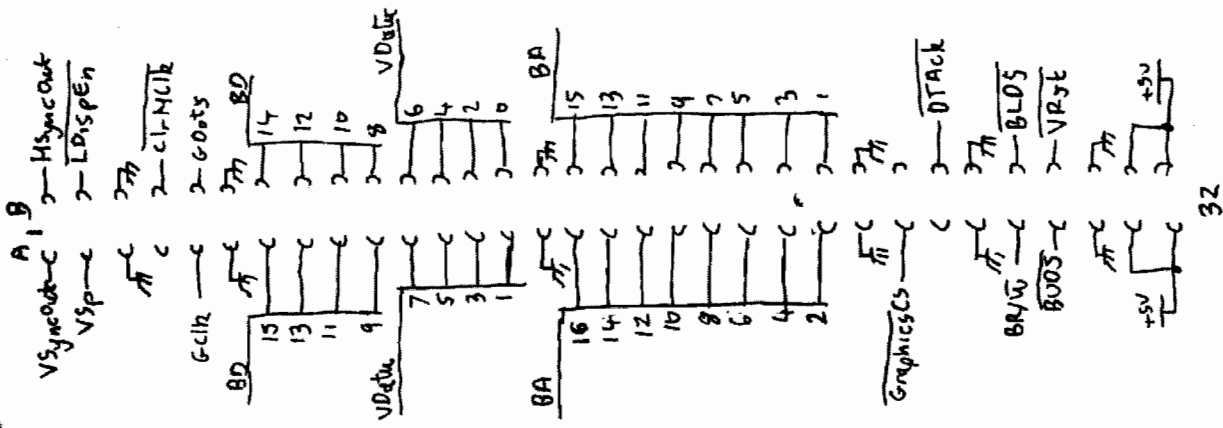




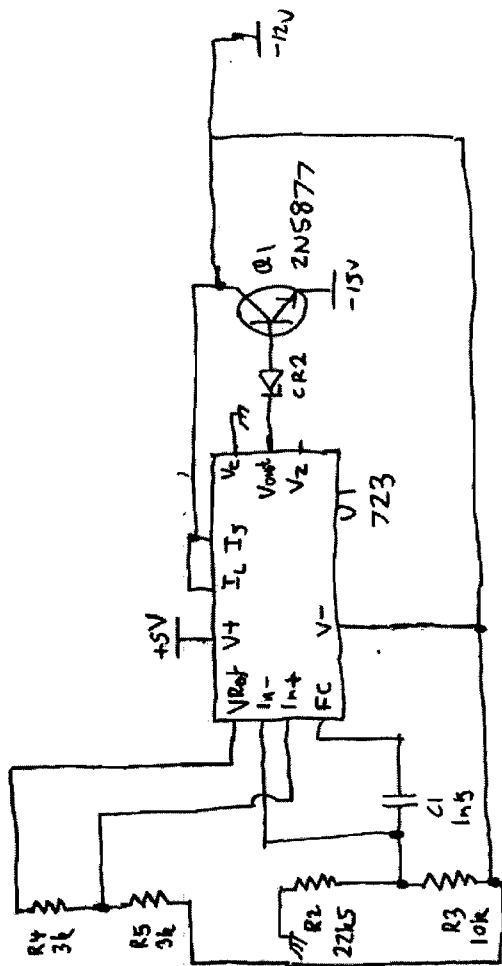


32 (Text PCB)

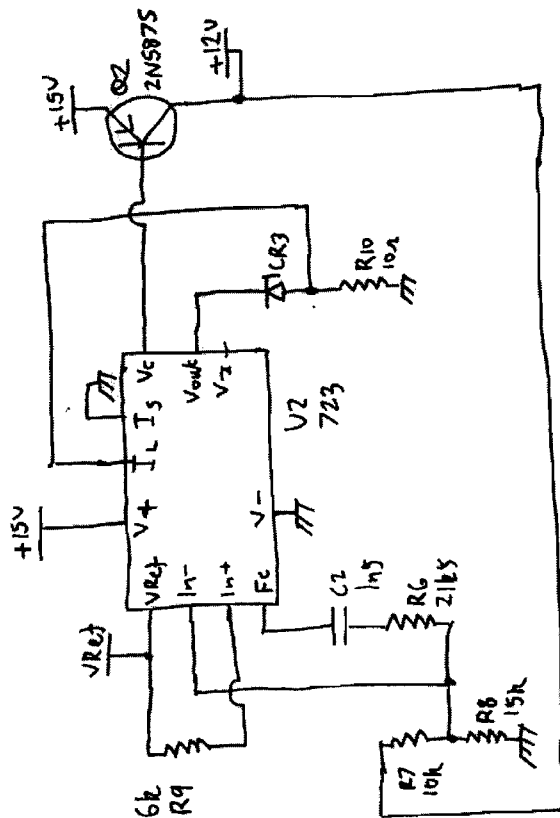
Rear  
 ↖  
 LHS



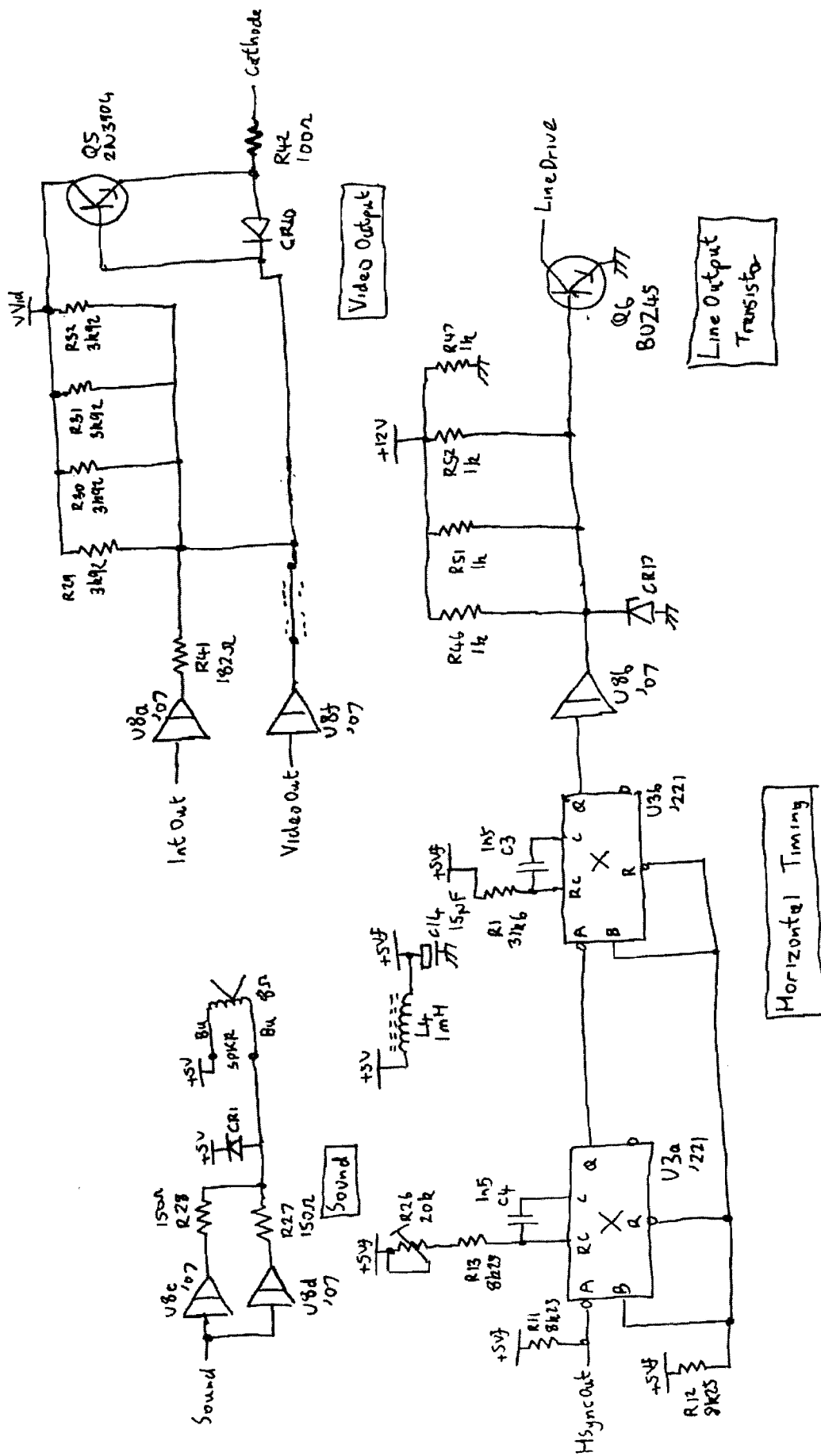
(Graphics PCB)

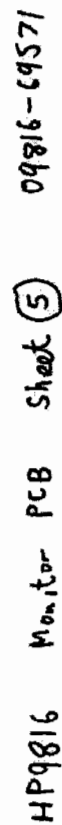


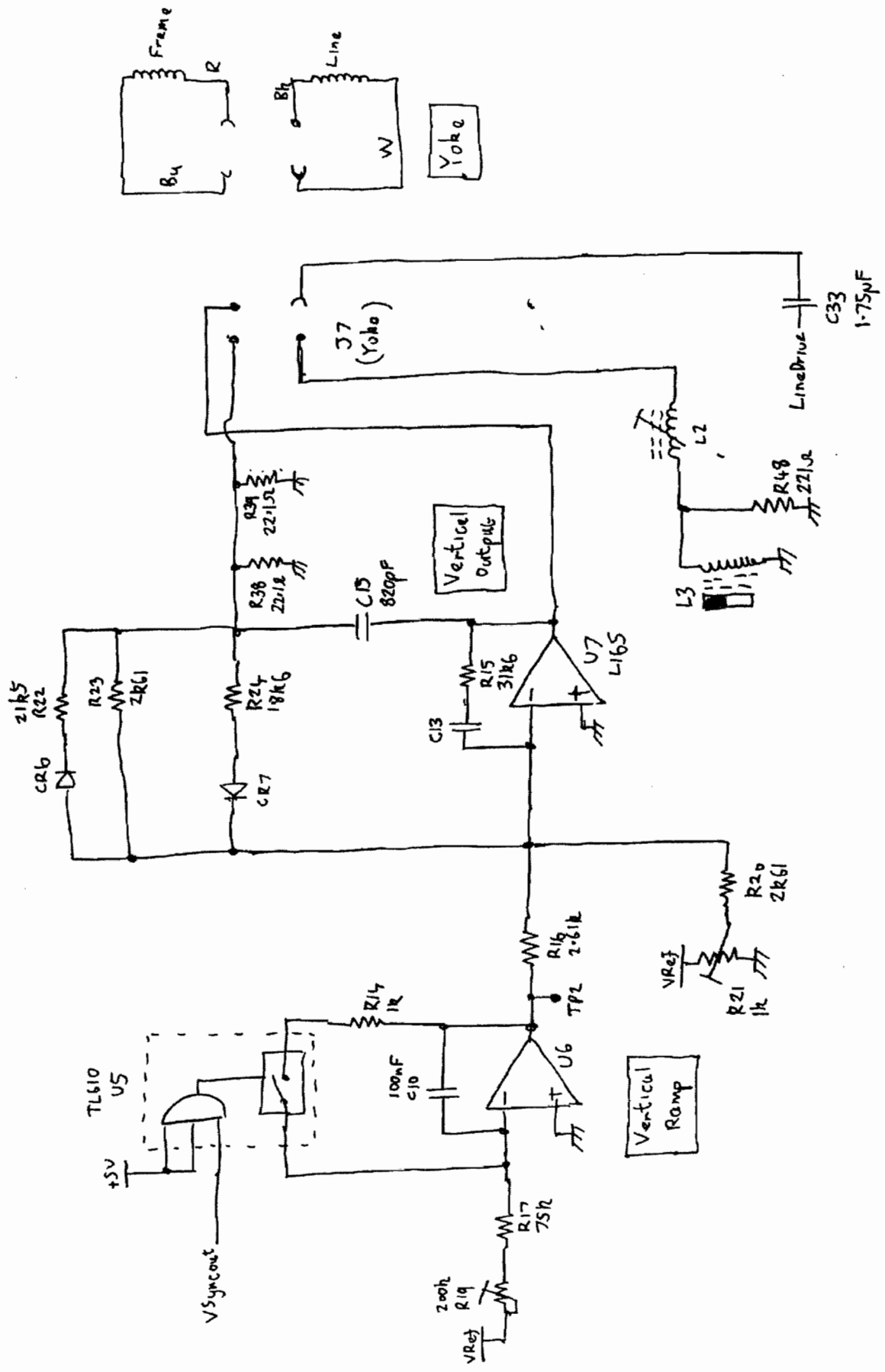
-12V Regulator



+12V Regulator

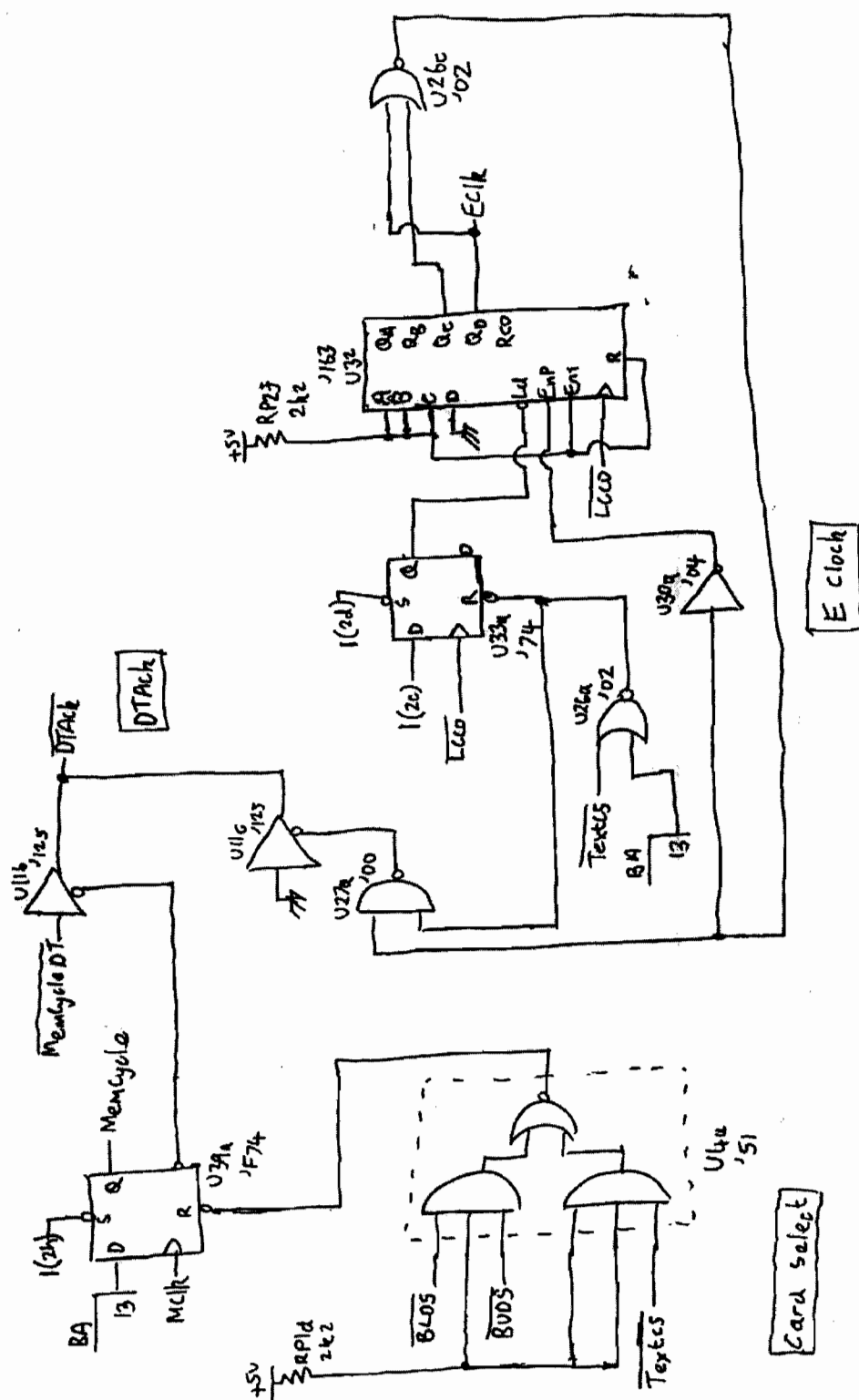


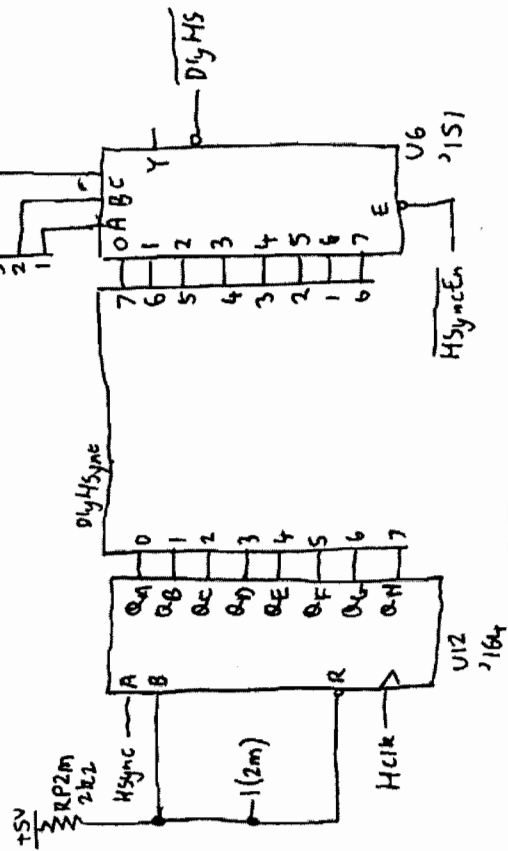
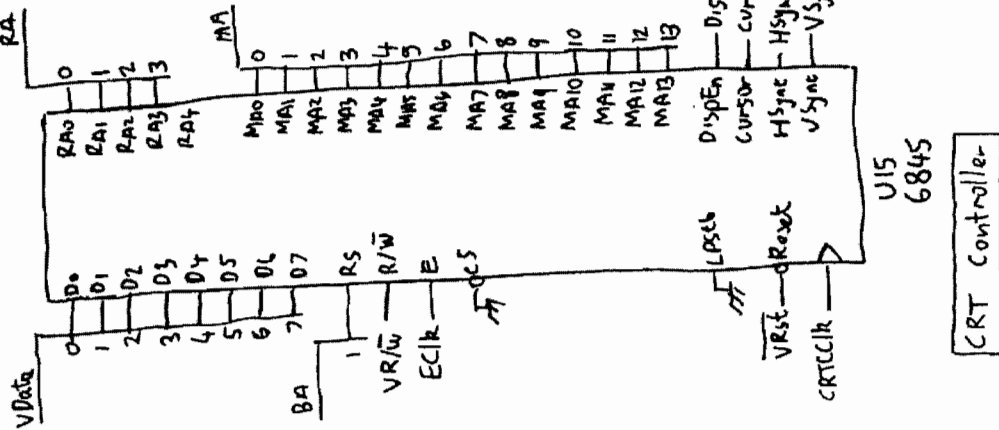
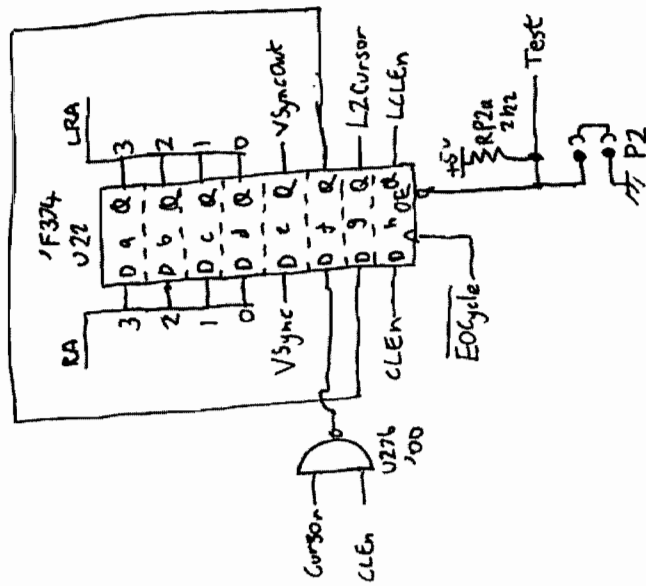
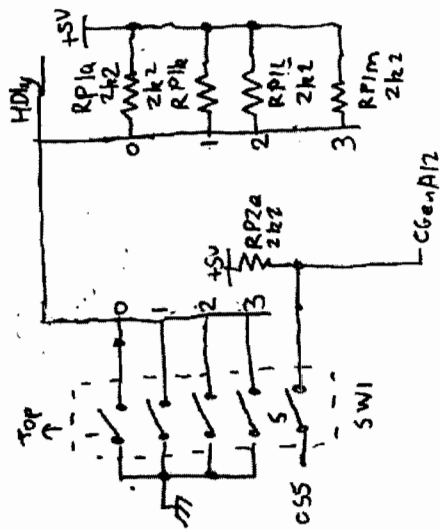




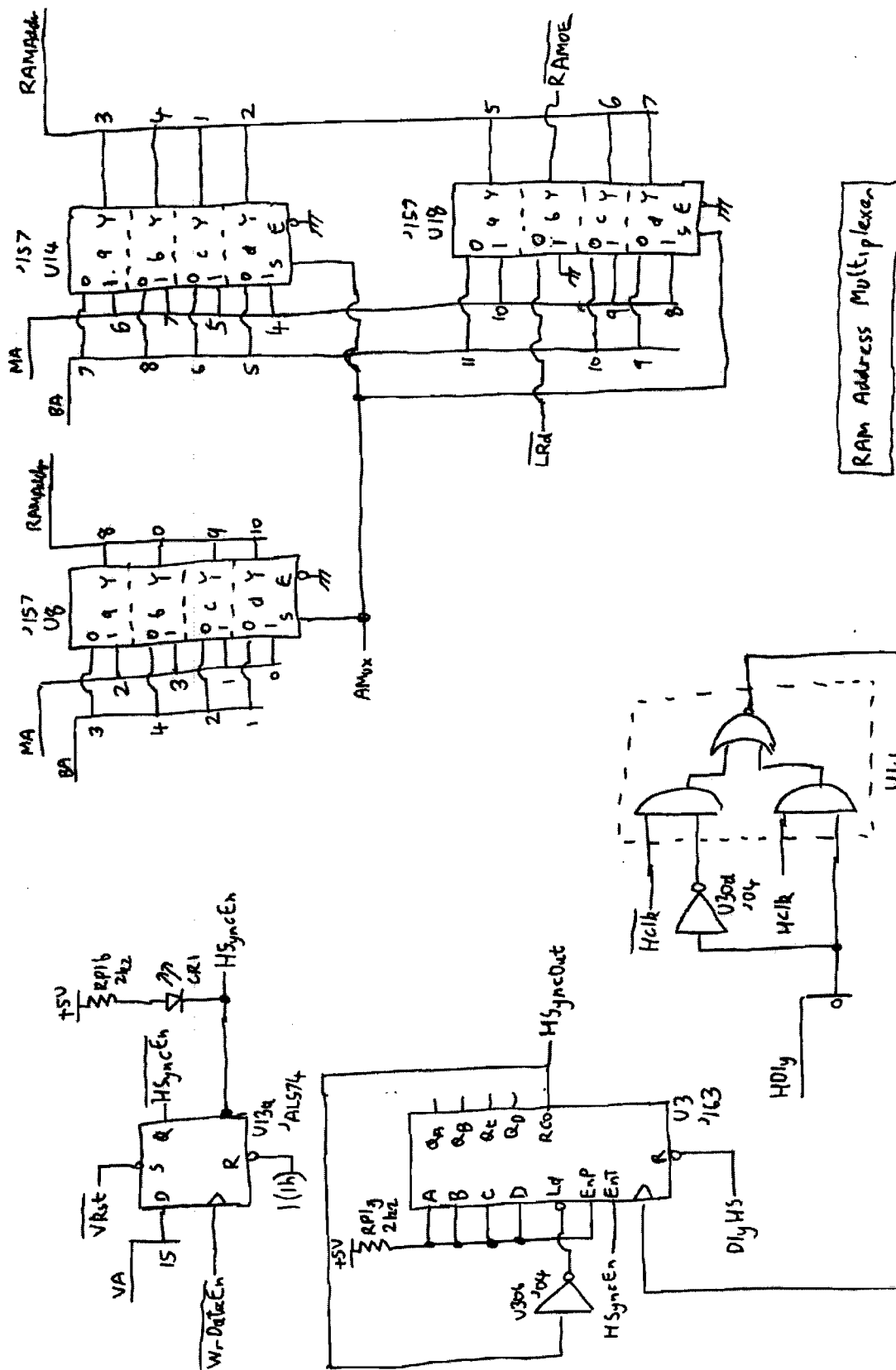


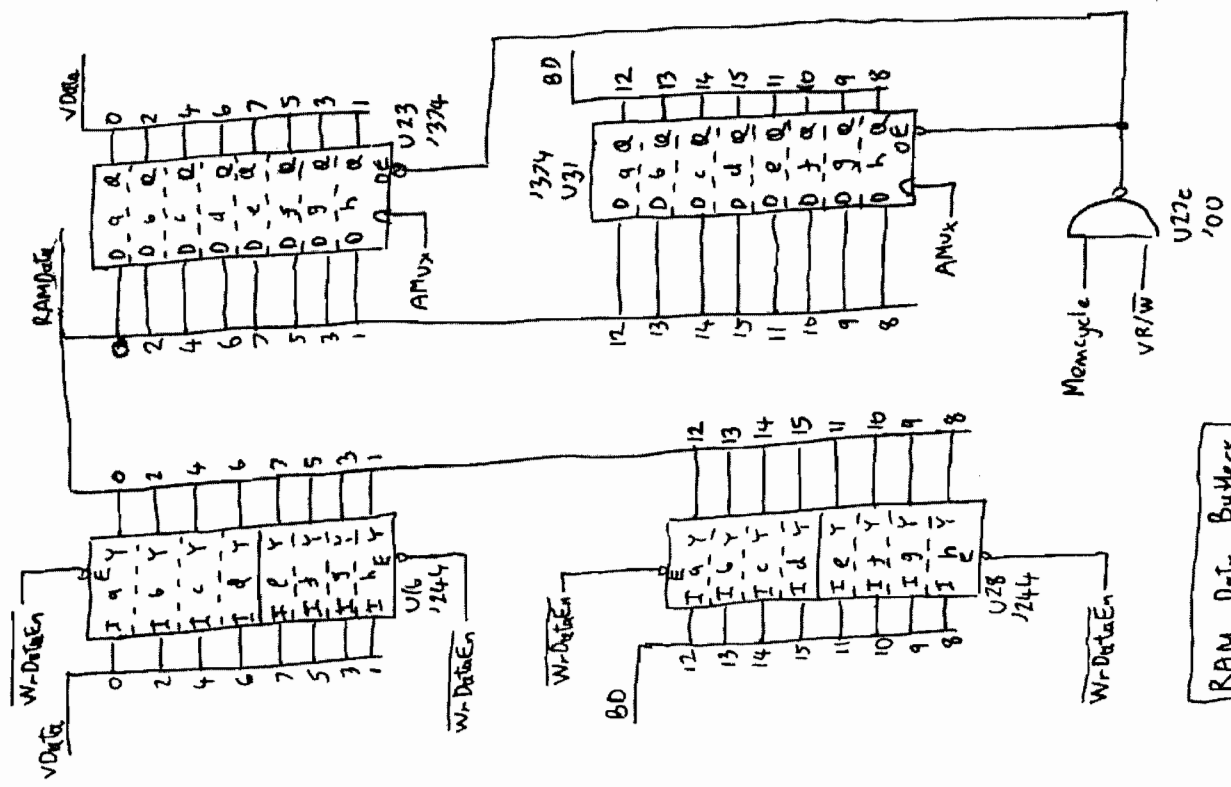
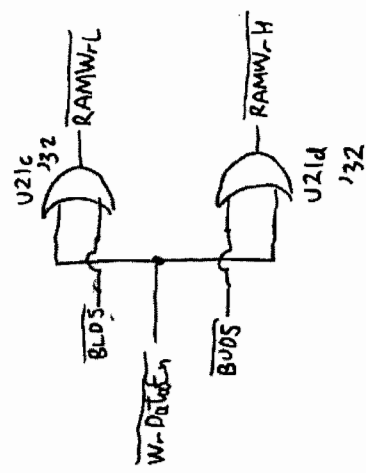
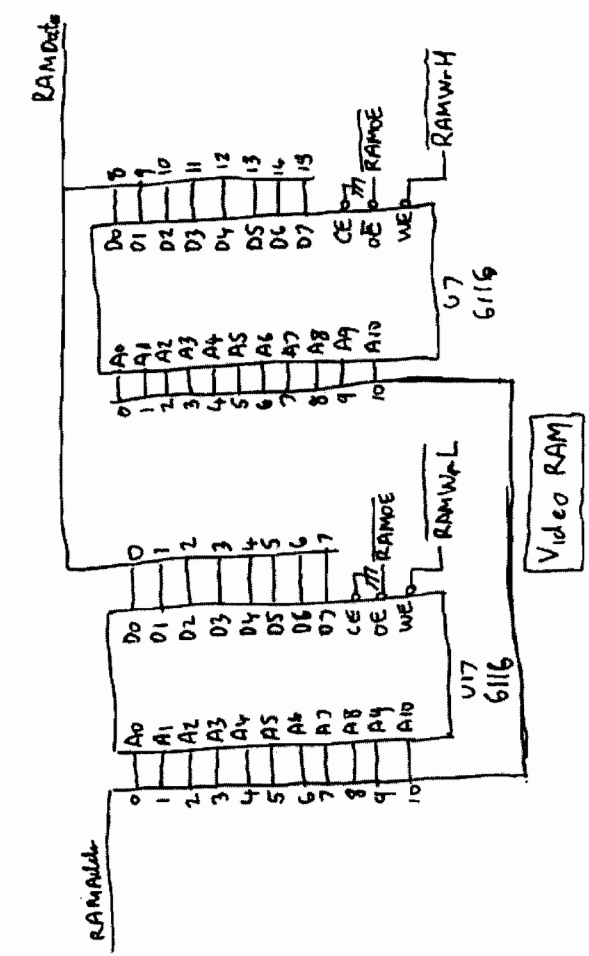


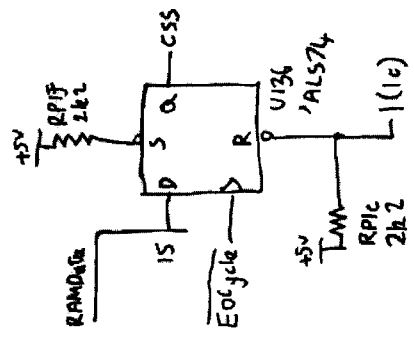
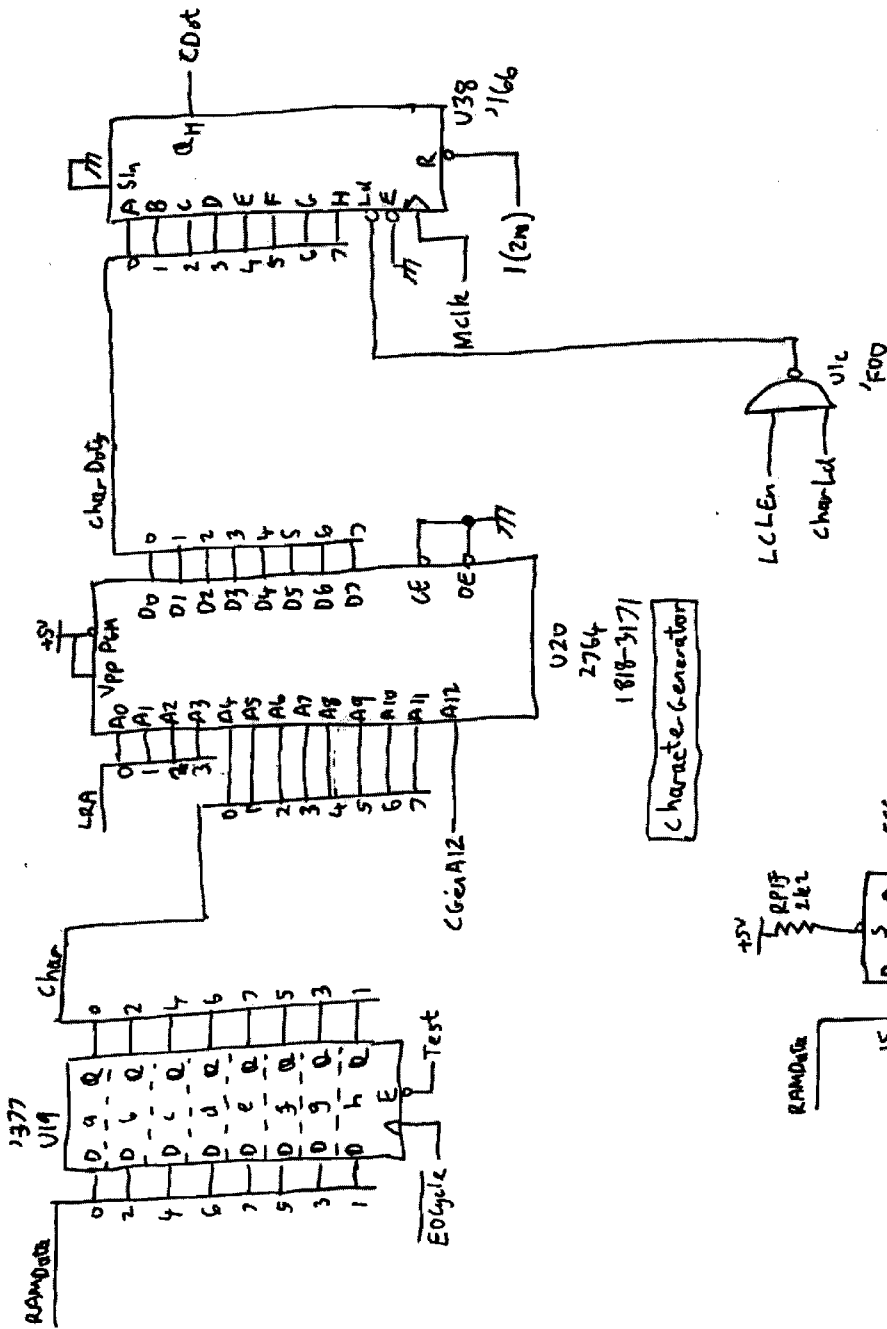


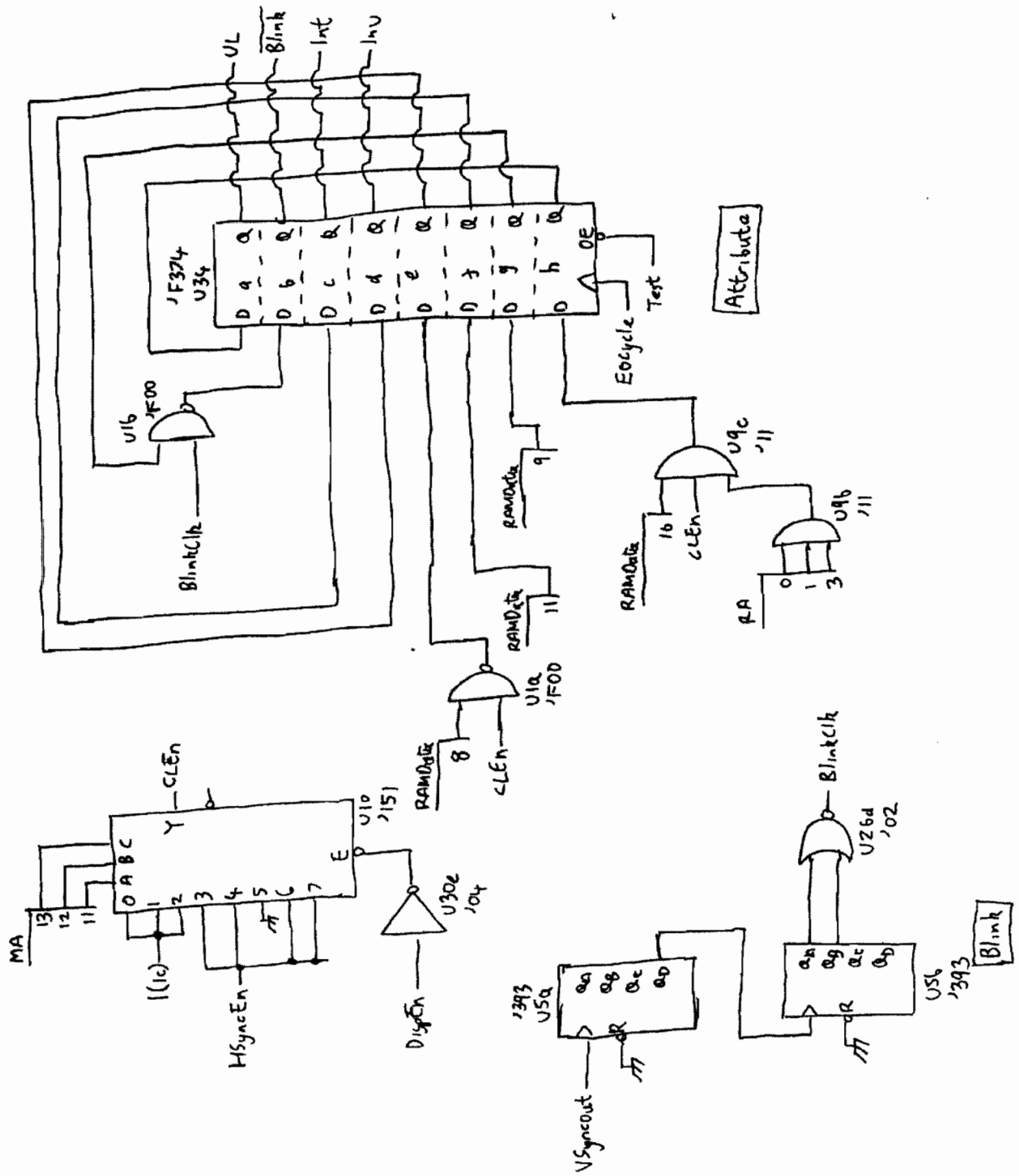


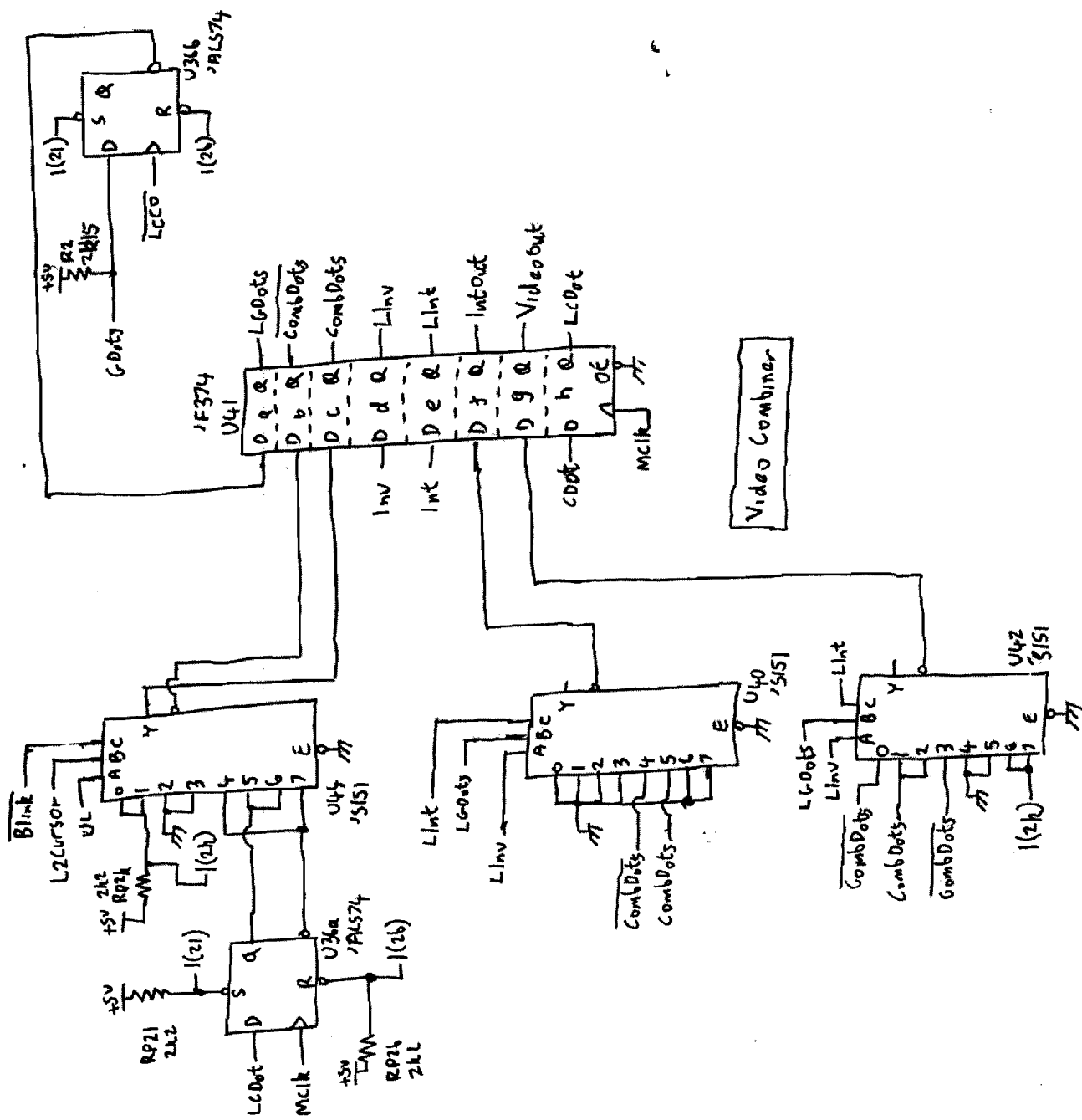
Horizontal Timing





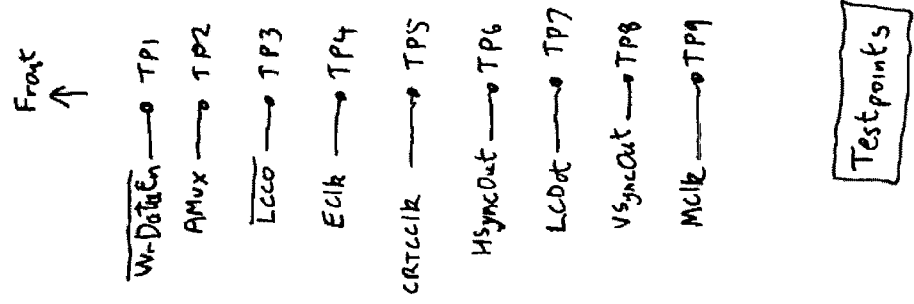
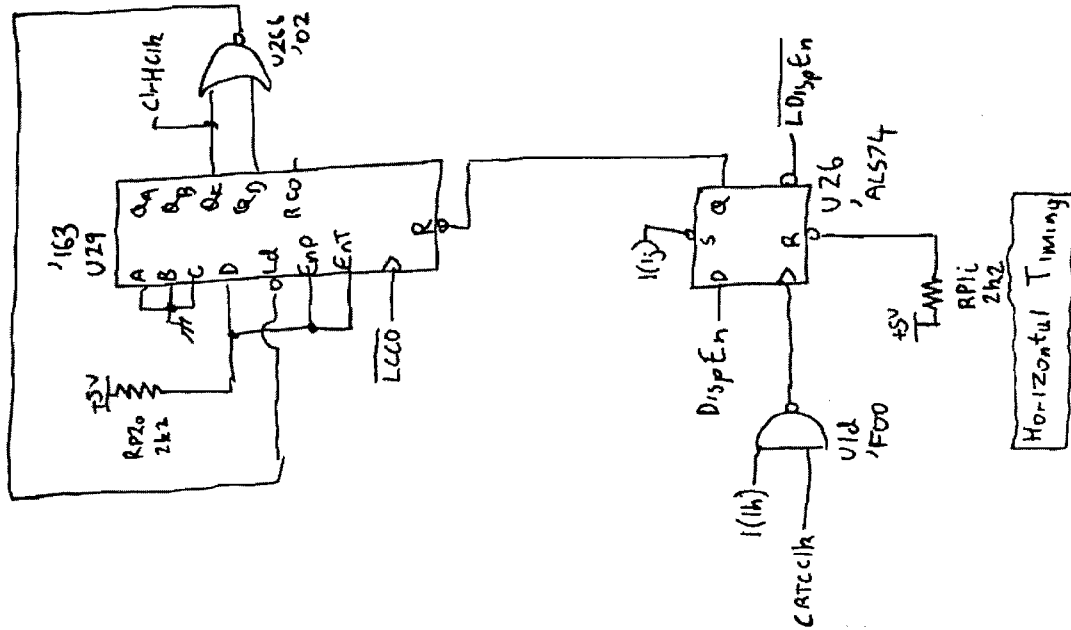


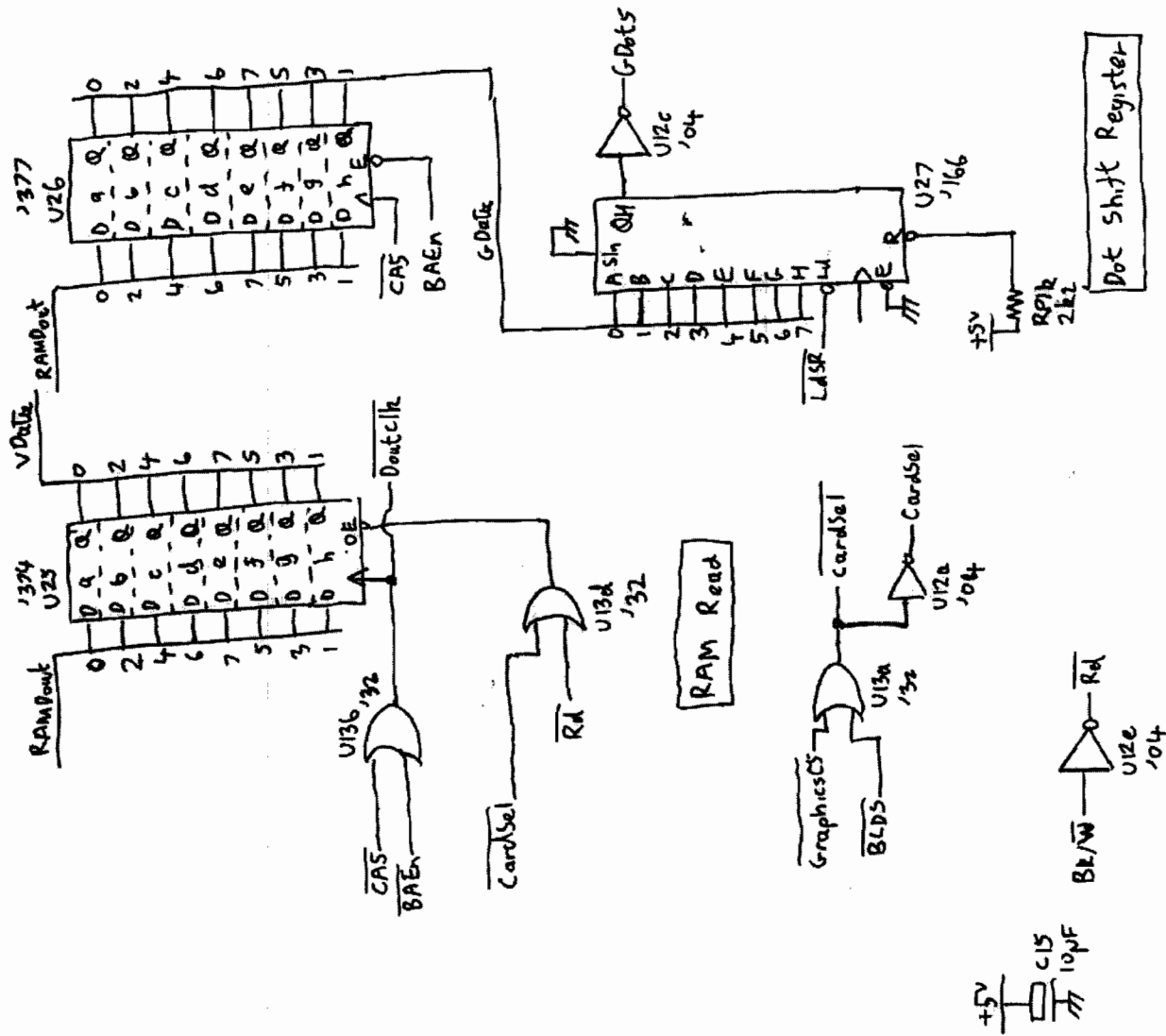
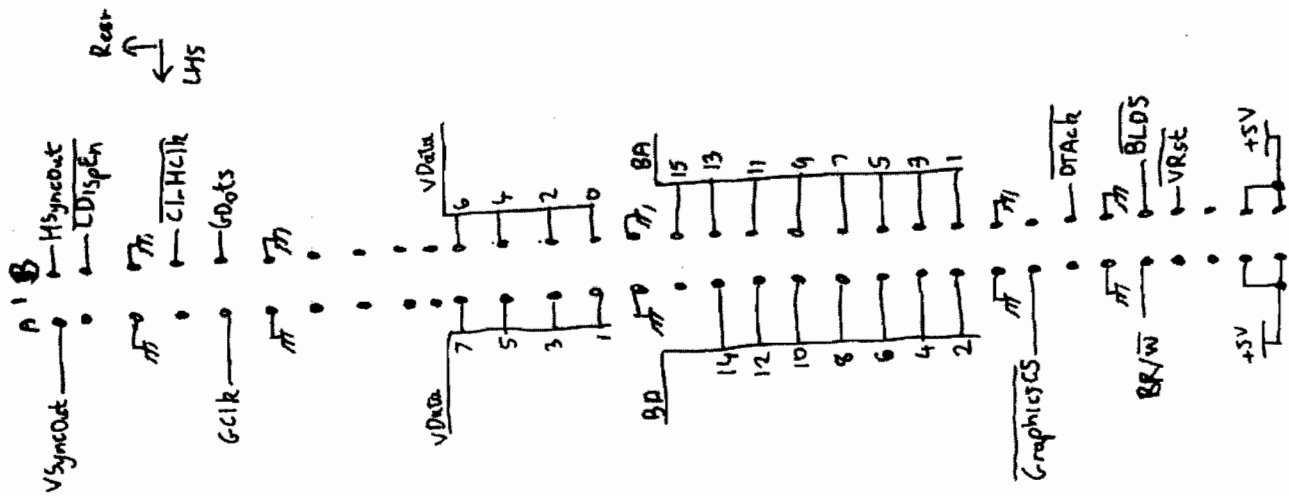




09816-66582

HP9816 Text PCB sheet 9

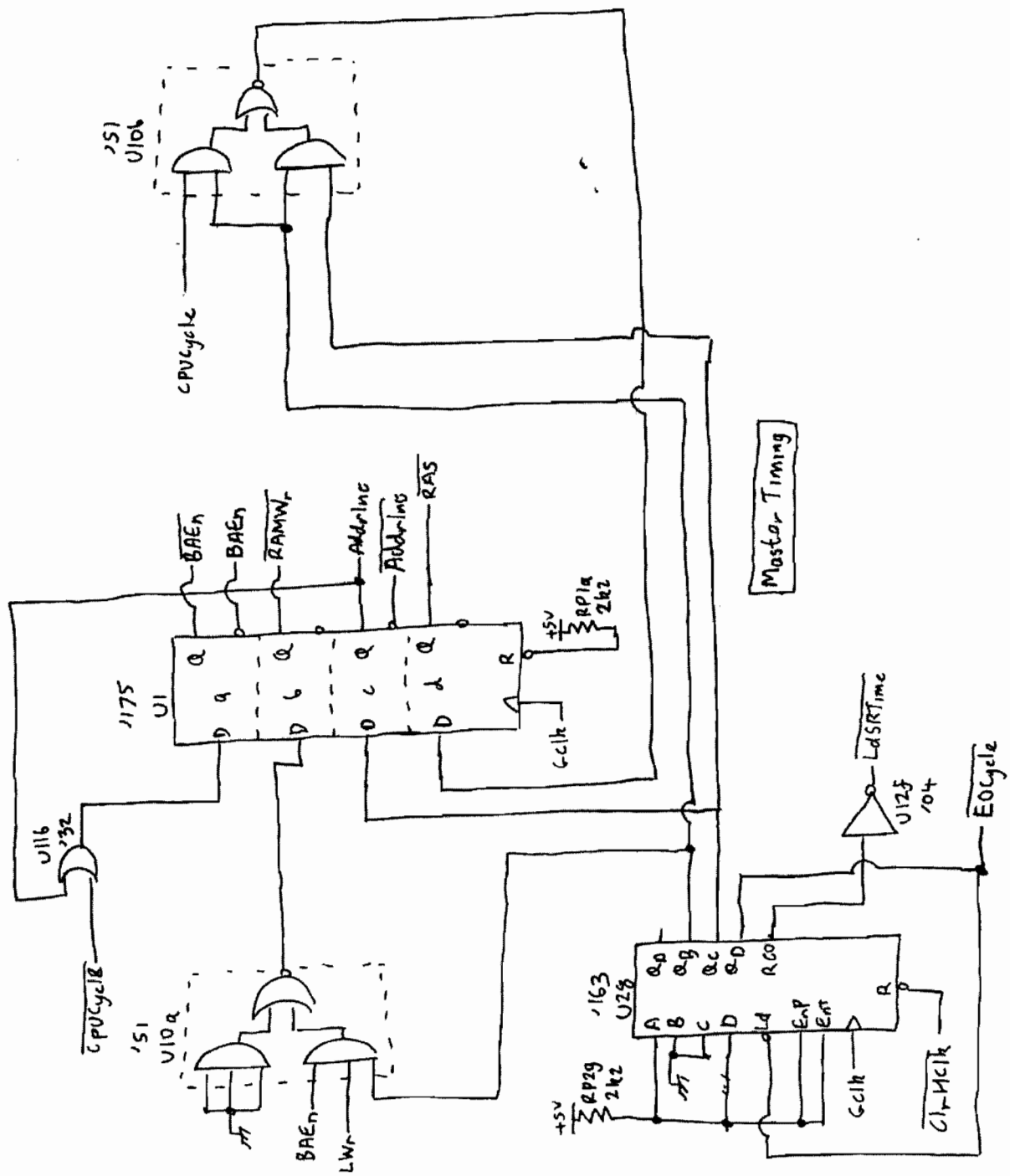


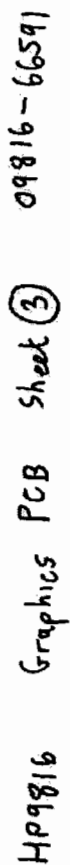


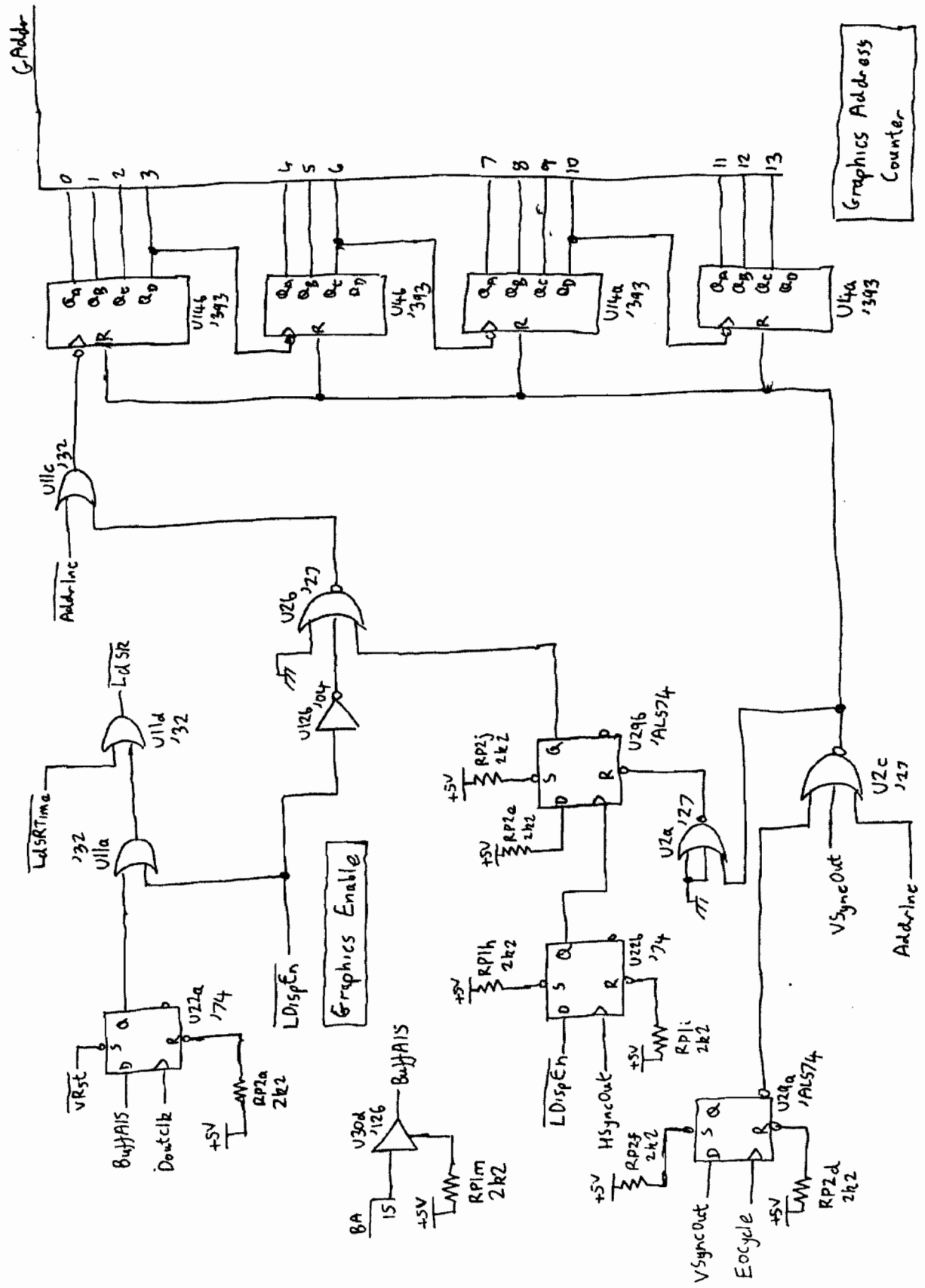
09816-66591

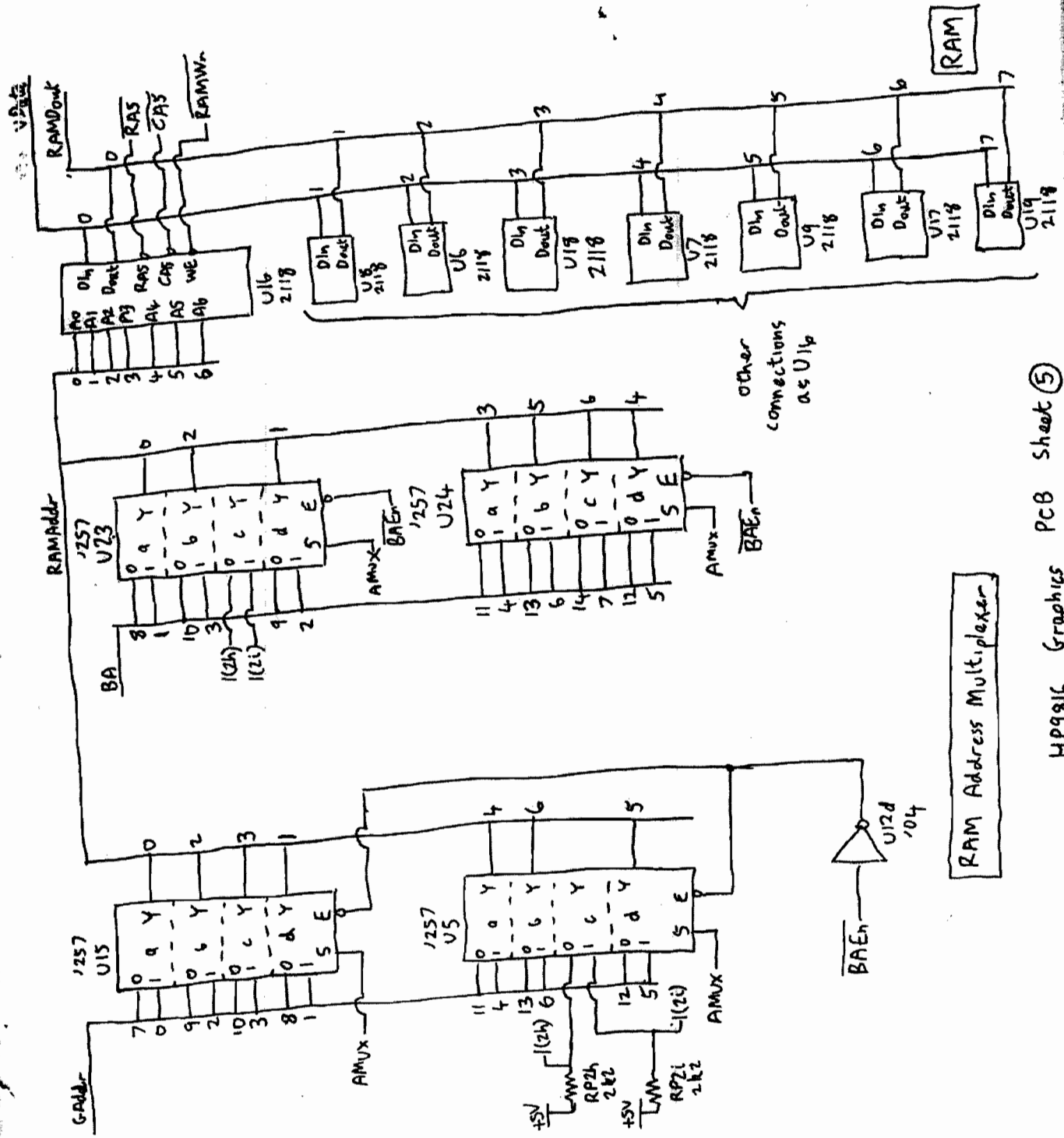
HP9816 Graphics PCB sheet (1)

(Monitor PCB)







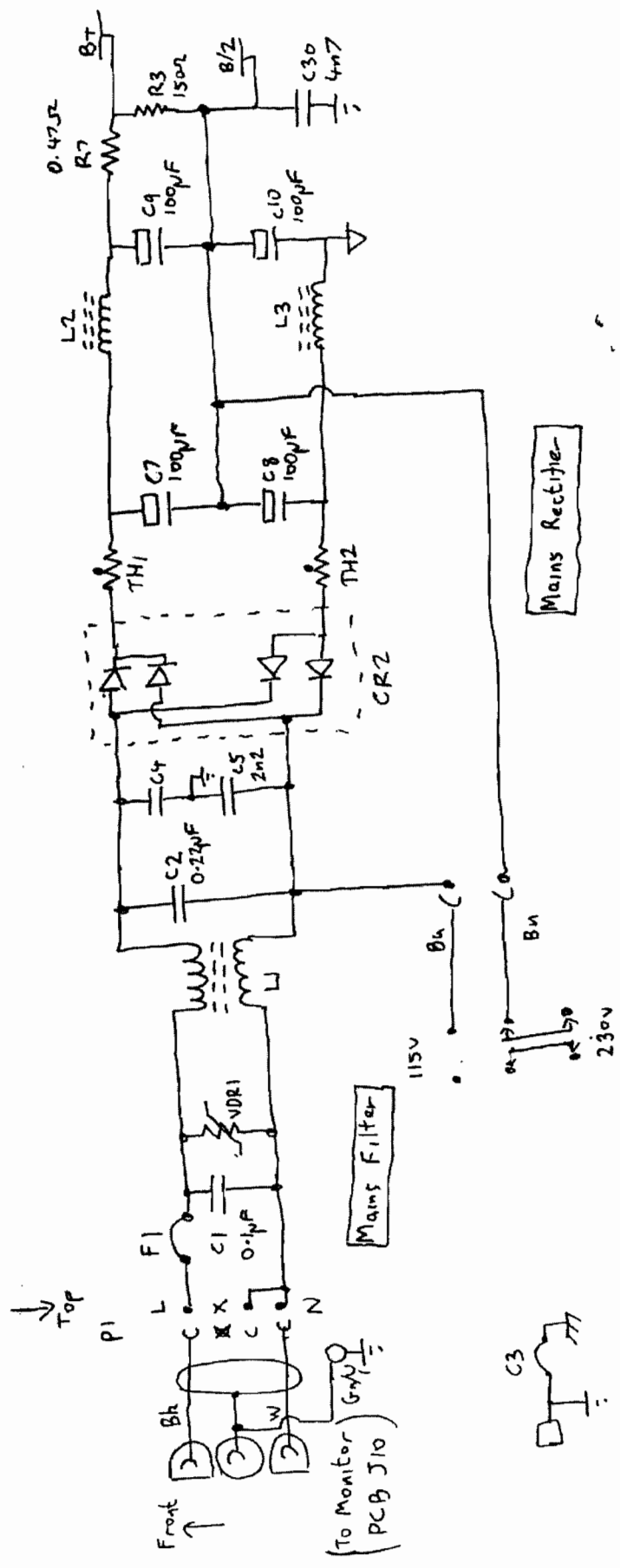


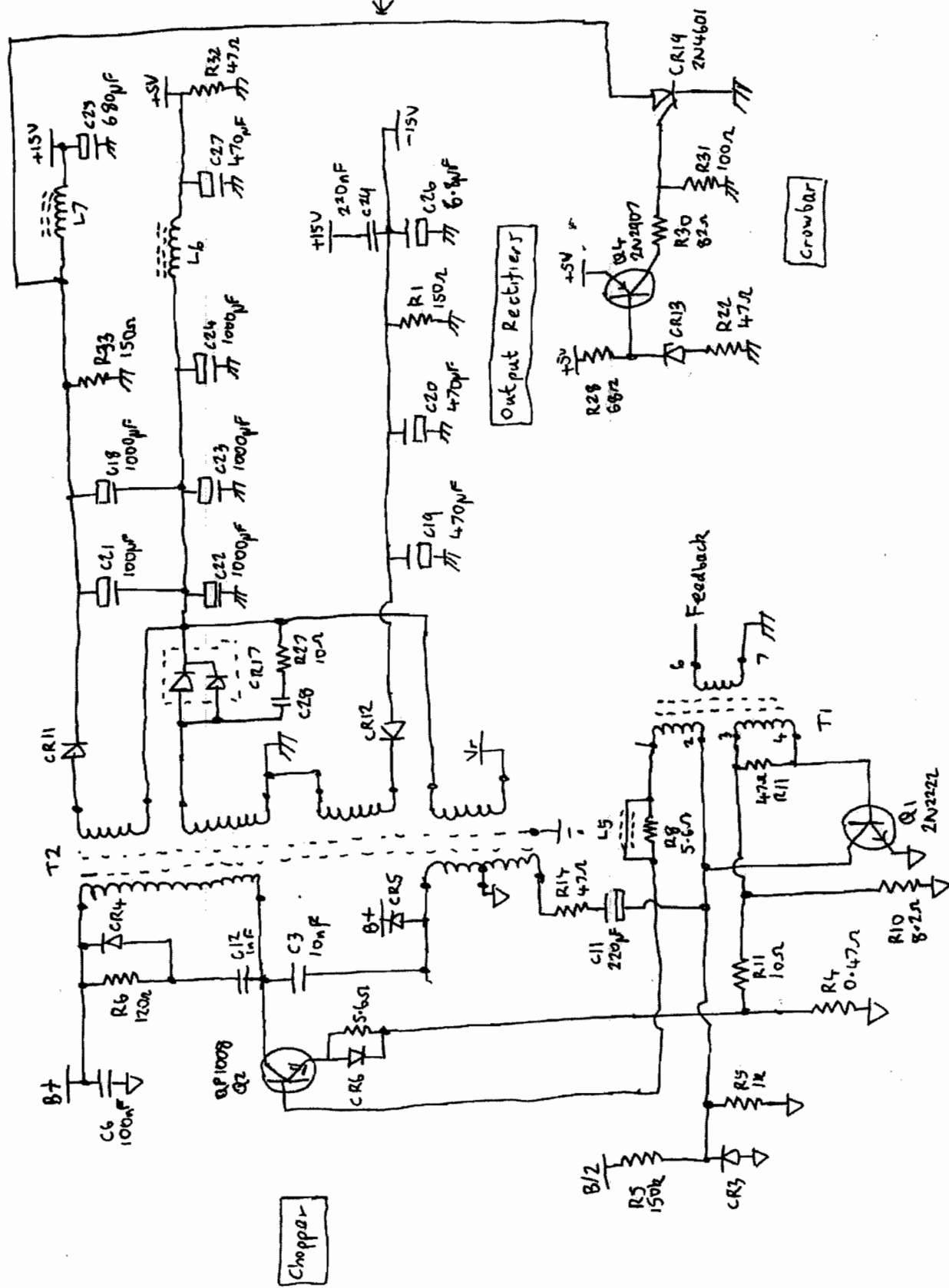
BA $\overline{\text{En}}$   $\rightarrow$  XA-  
 RAMW $\overline{\text{r}}$   $\rightarrow$  WE-  
 RAS  $\rightarrow$  RAS  
 BuffA15  $\rightarrow$  A15  
 DoutClk  $\rightarrow$  DC  
 CAS  $\rightarrow$  CAS  
 Testpoints

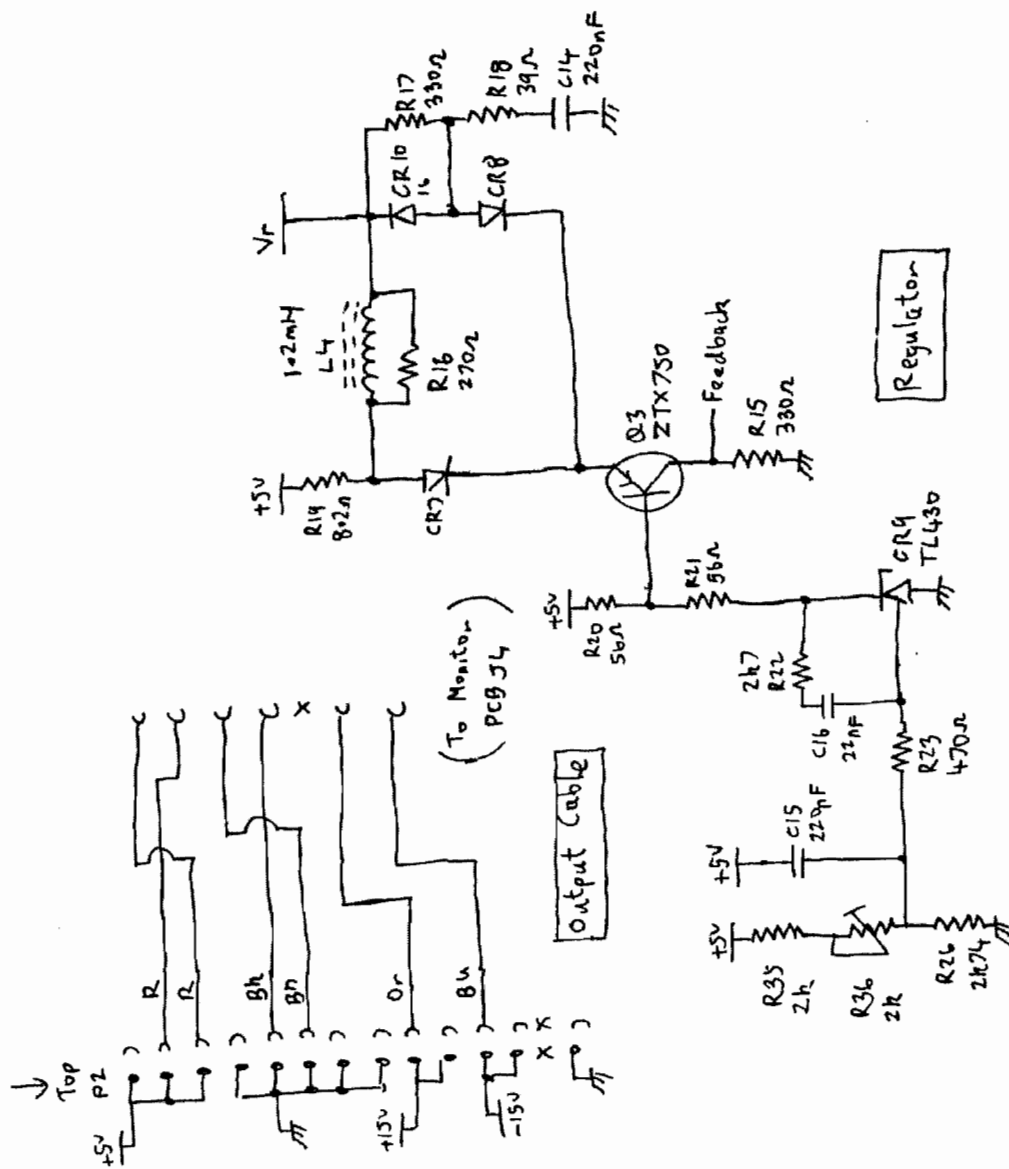
09816-66591

RAM Address Multiplexer

HP9816 Graphics PCB sheet ⑤









HP 9816 Fan

Papst Multiton 8314

[illegible]

