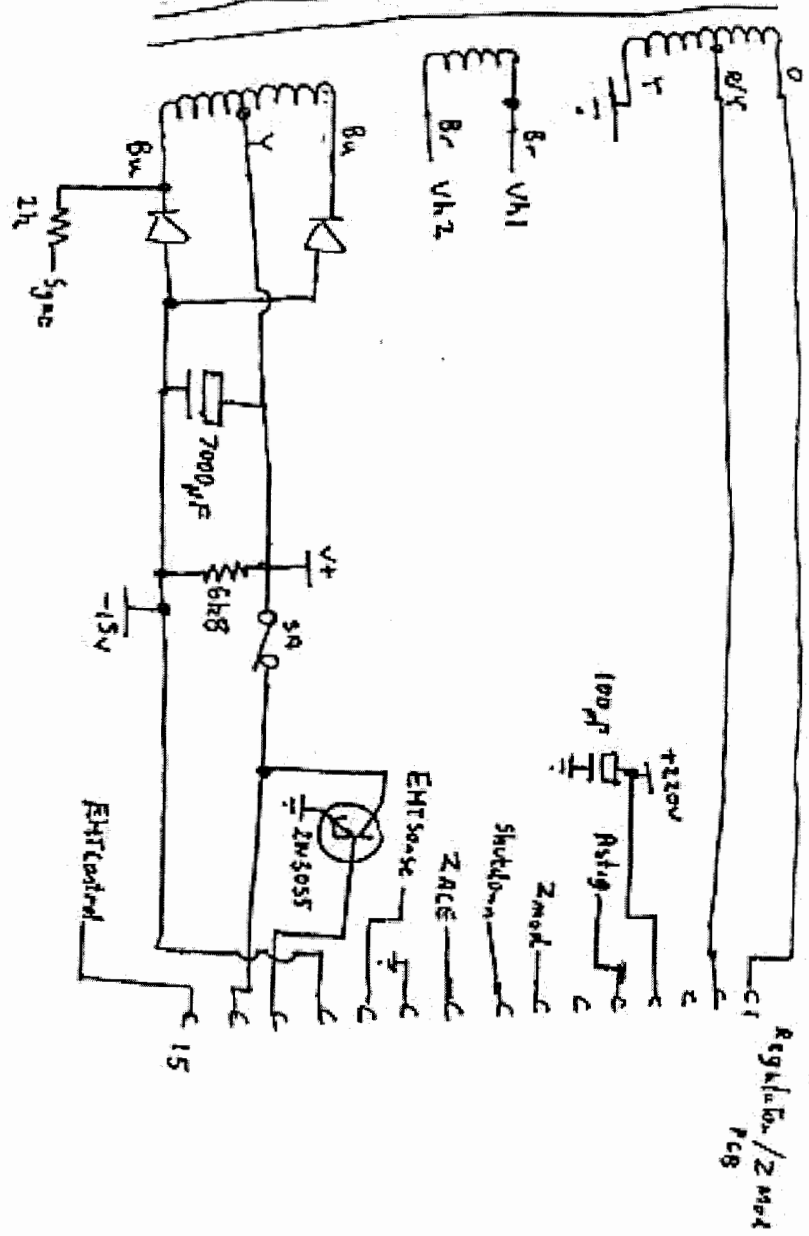
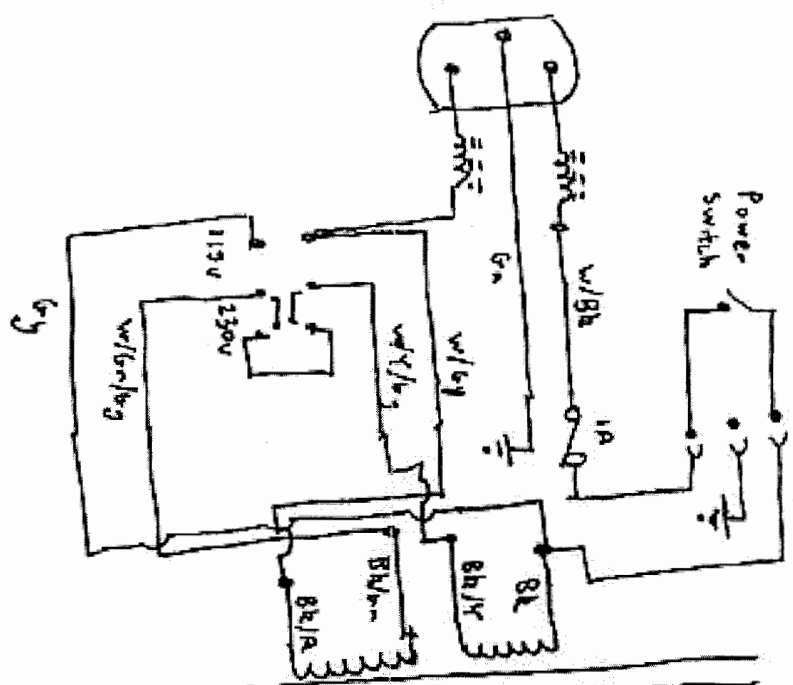
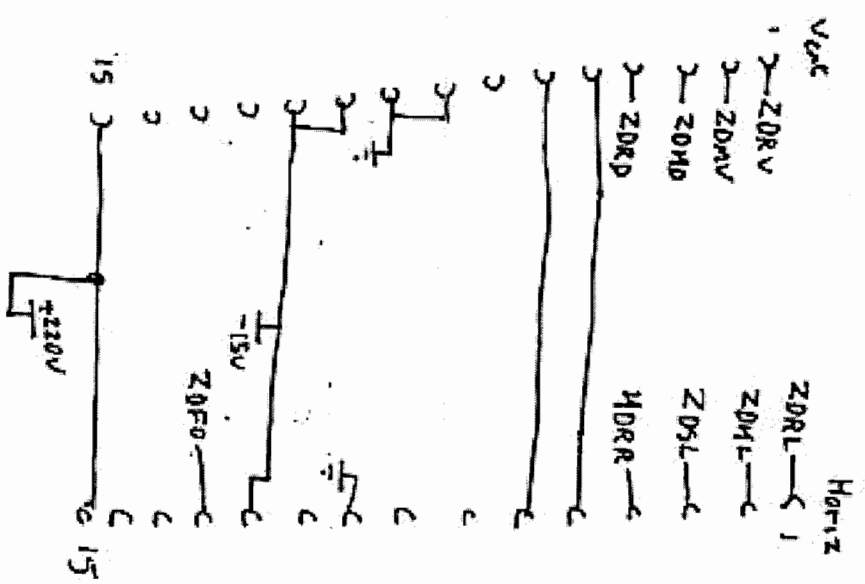
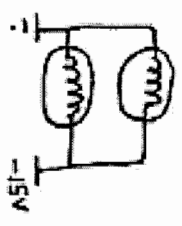
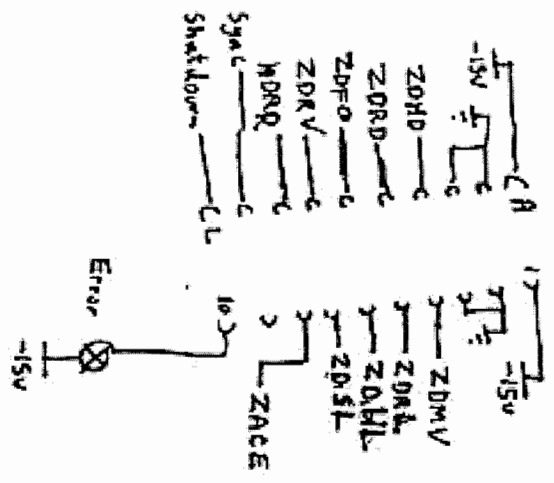




LV PSU

HP9100 PSU sheet ①

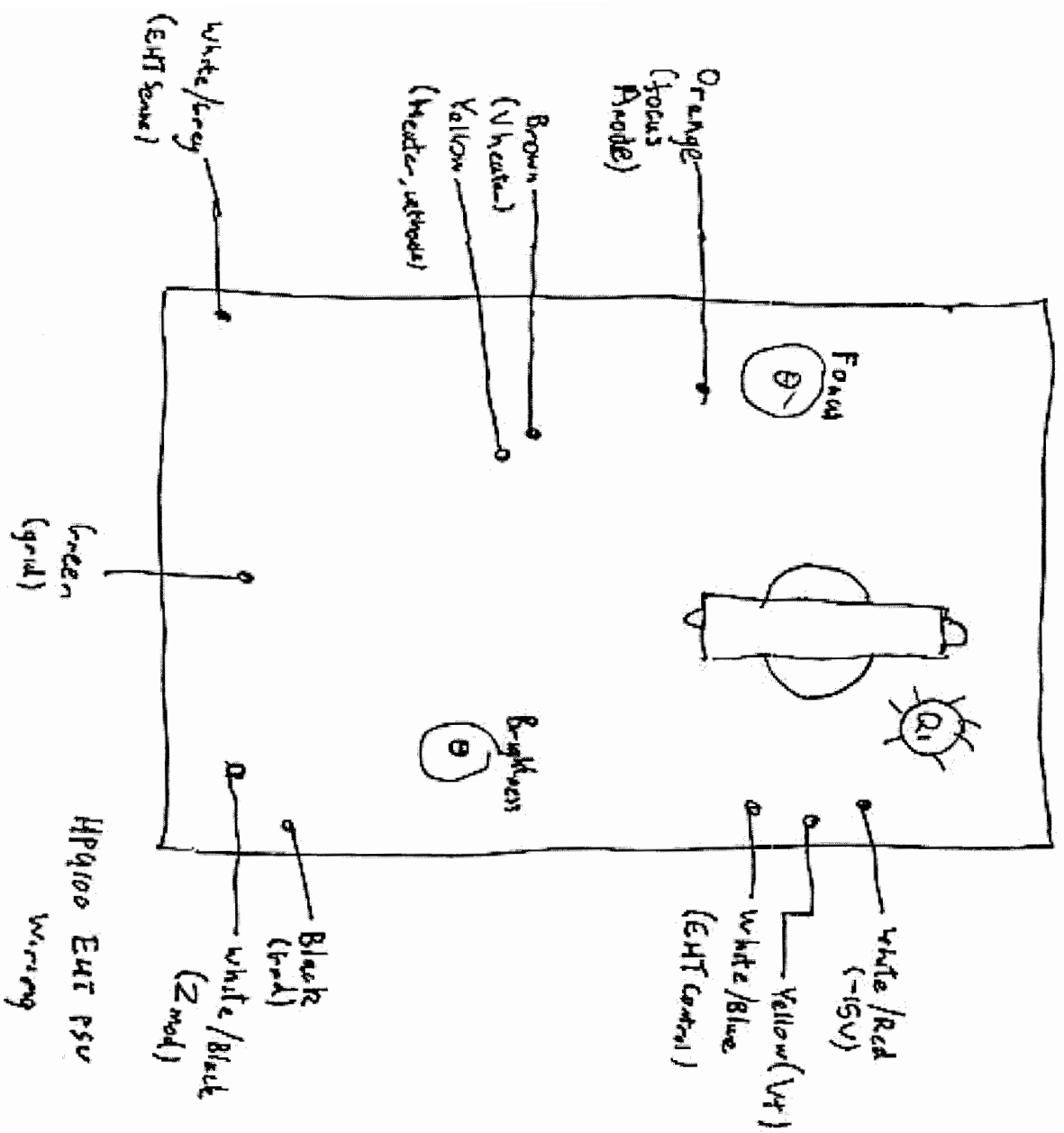
(To previous)



Top case wiring

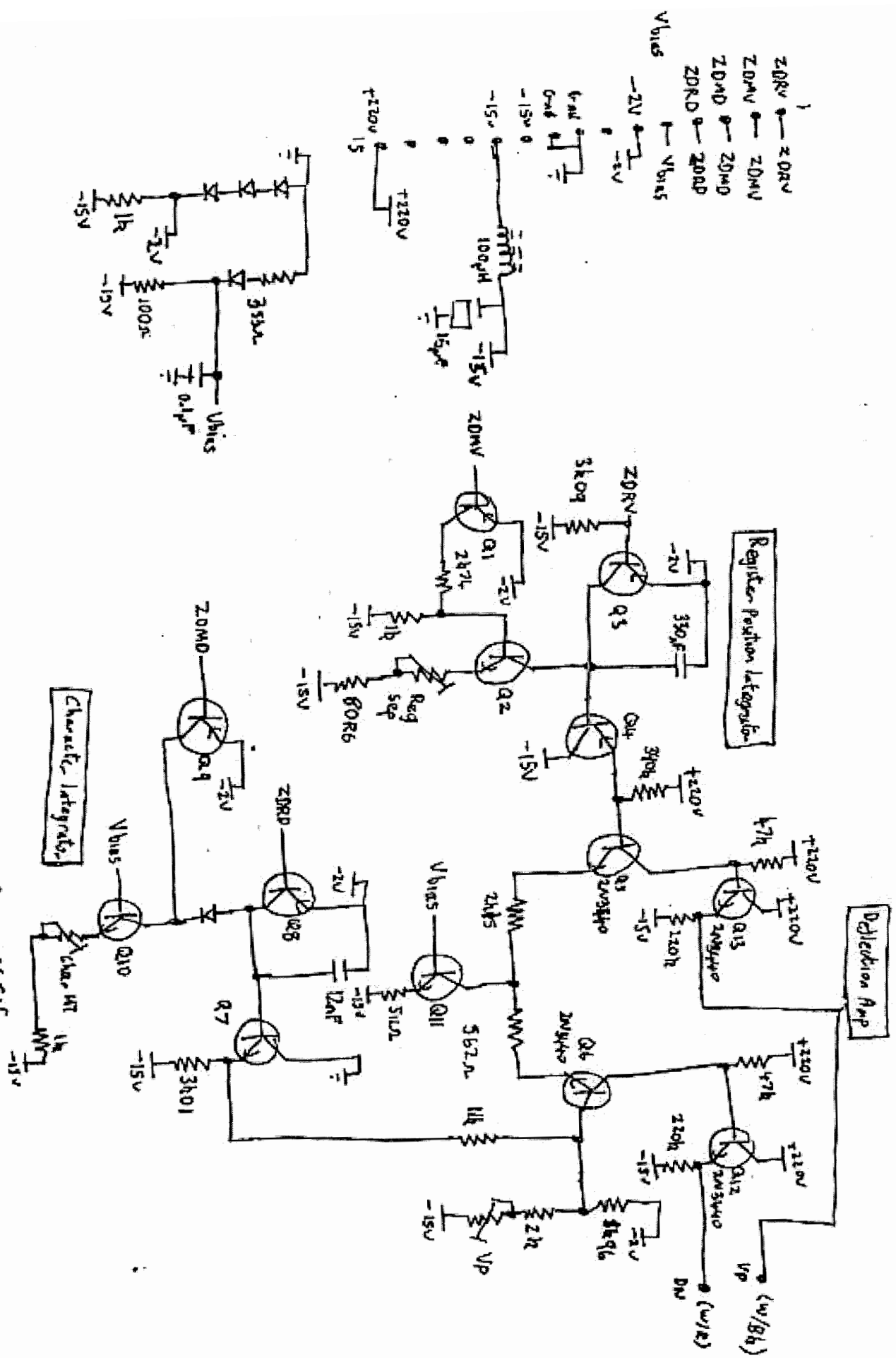
HP4100 PSU sketch ②



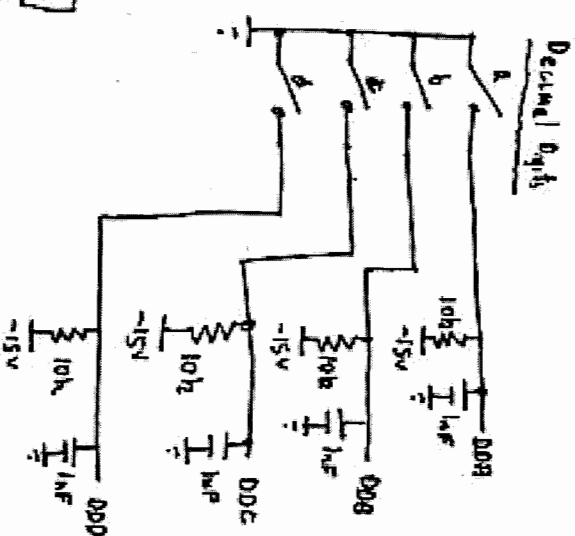
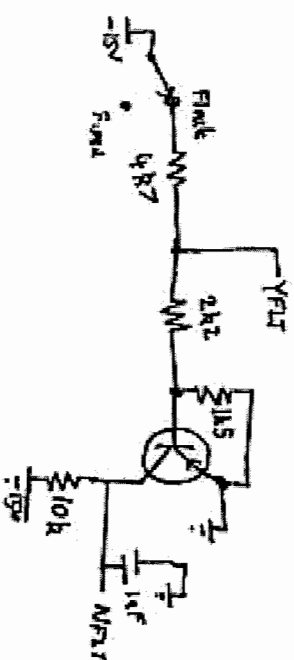




HP 9100 Regulator / Zmod PC8
(09100-66521)

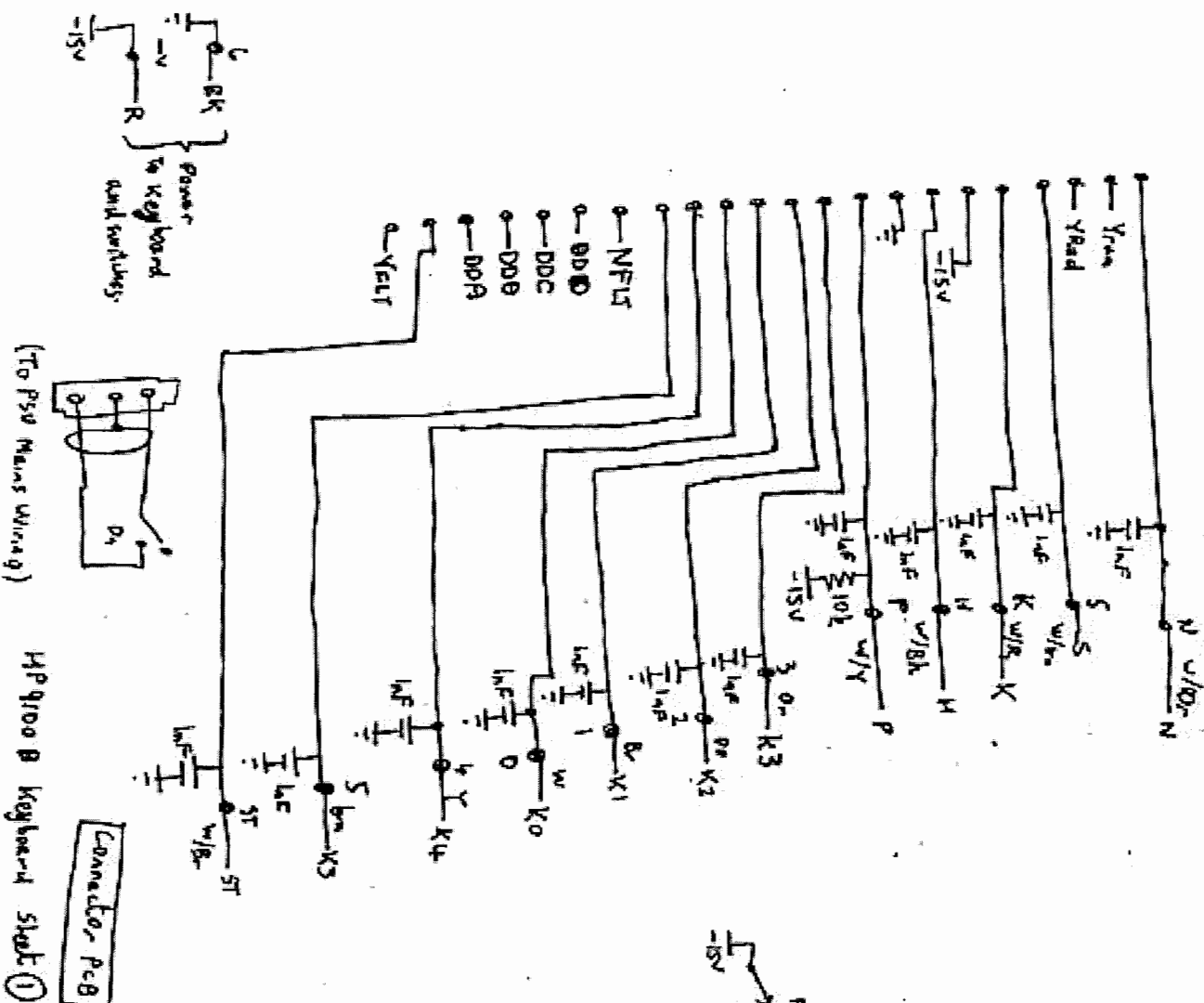


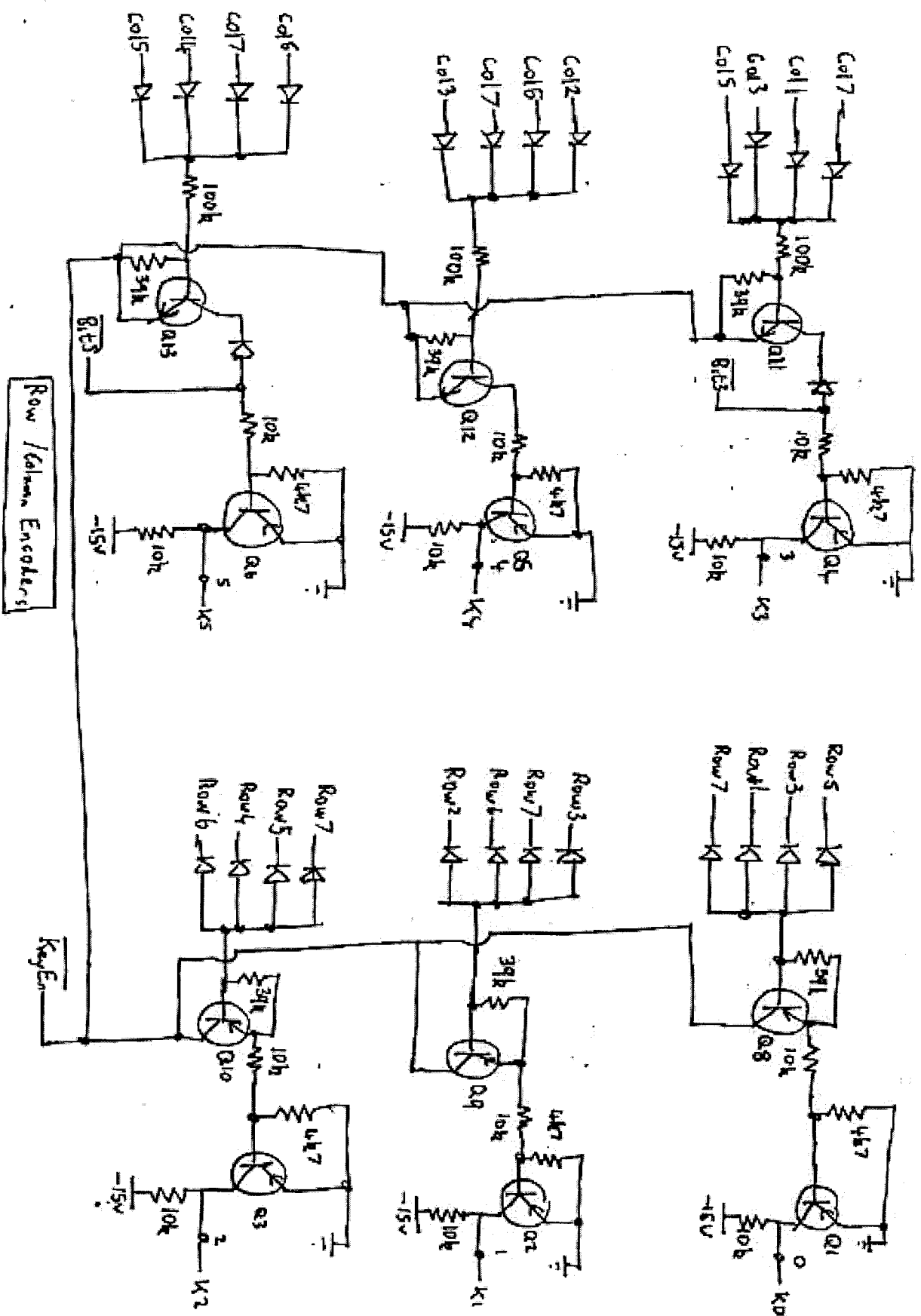
MP9100 Vertical Deflection PCB 09100-66516



Conducts Cloned

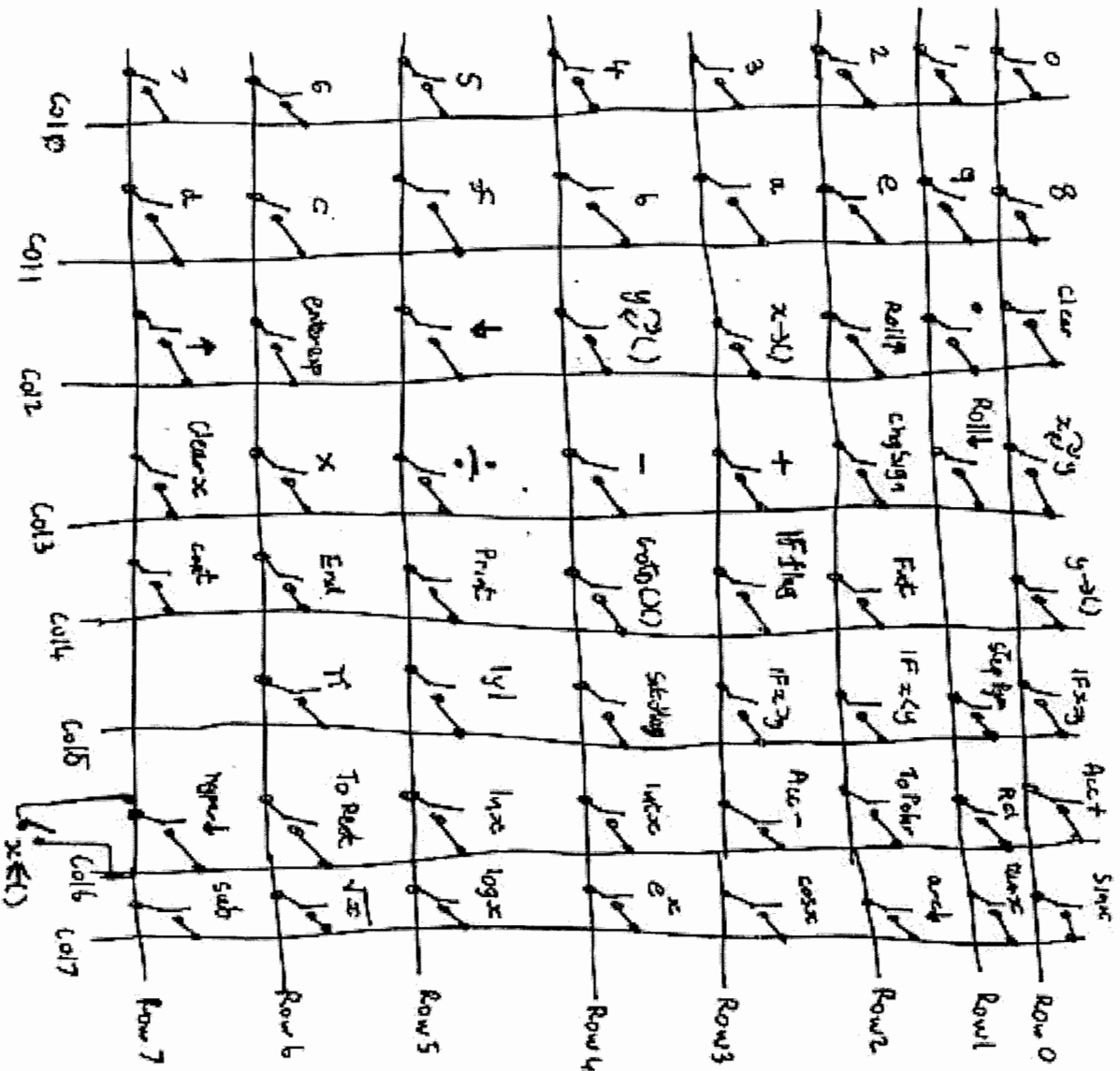
0 a b d
1 b d
2 a c
3 c
4 a
5 a a c
6 a b c
7 b c
8 a b
9 b





HP9100B Keyboard Shift (Z)

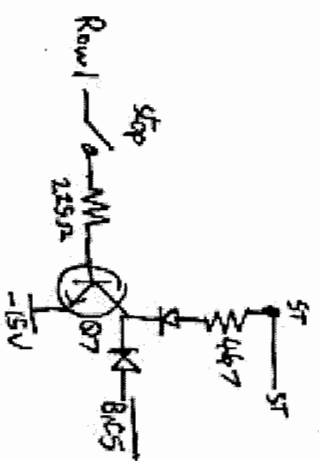
09100-66581



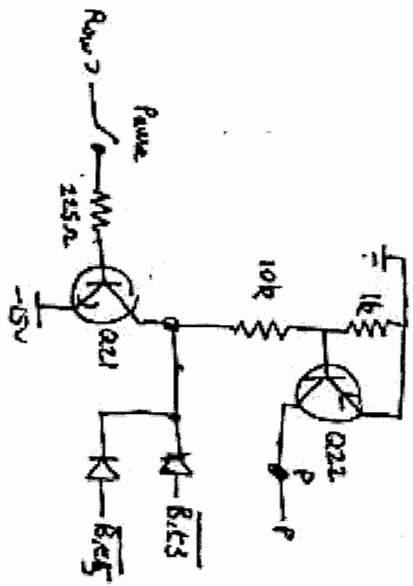
Key Matrix

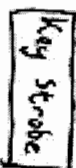
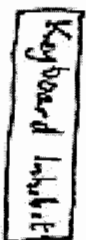
MPQ1008 Keyboard Sheet 2

09100-66581



Pause, Stop Keys



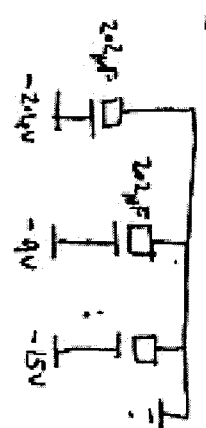
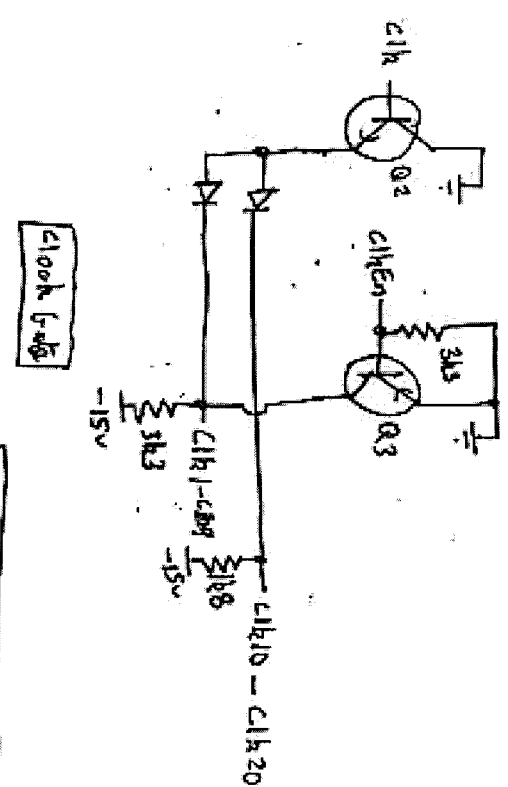
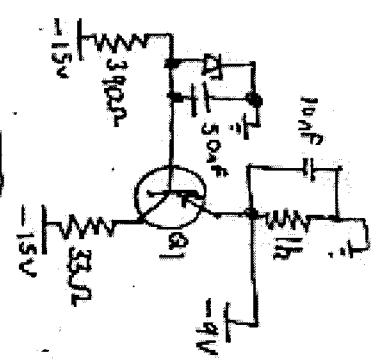
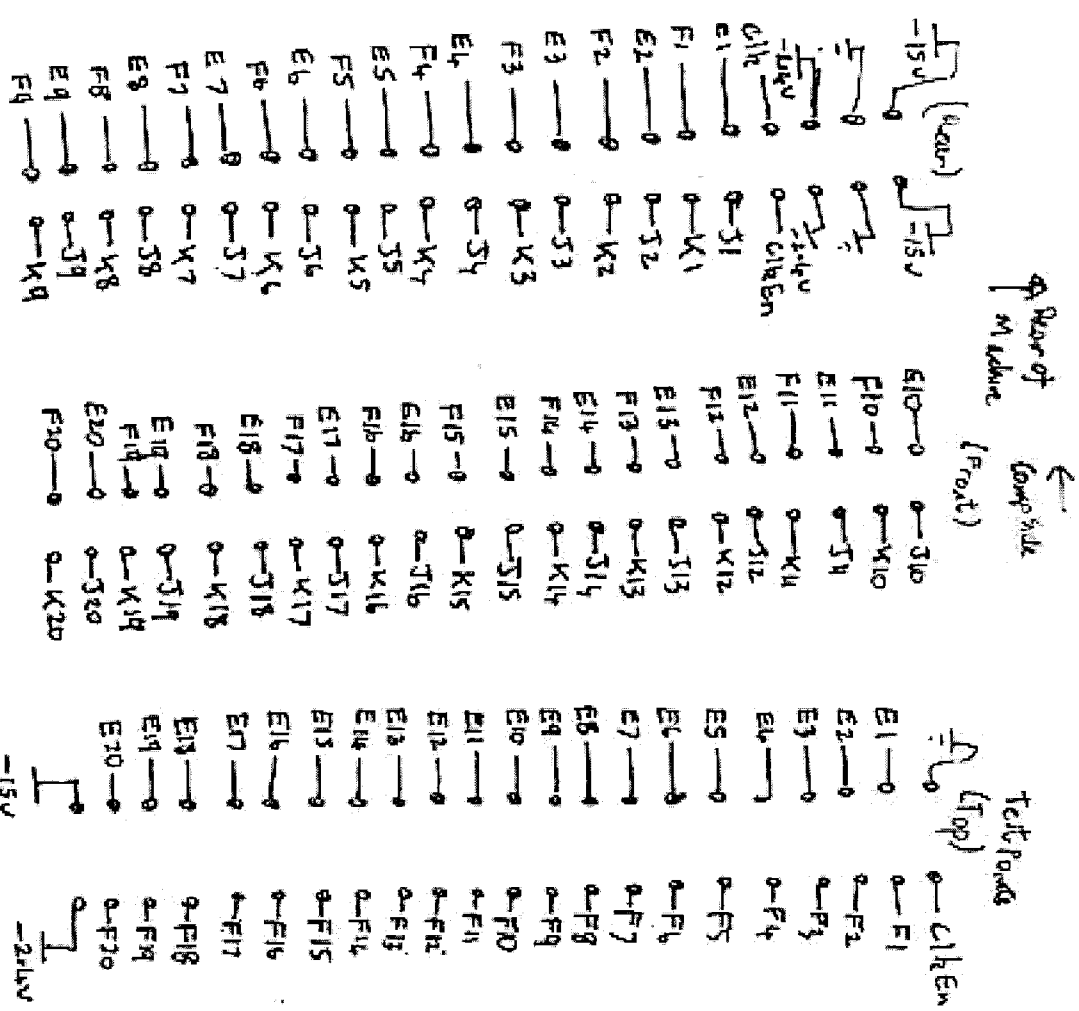


09100-66581

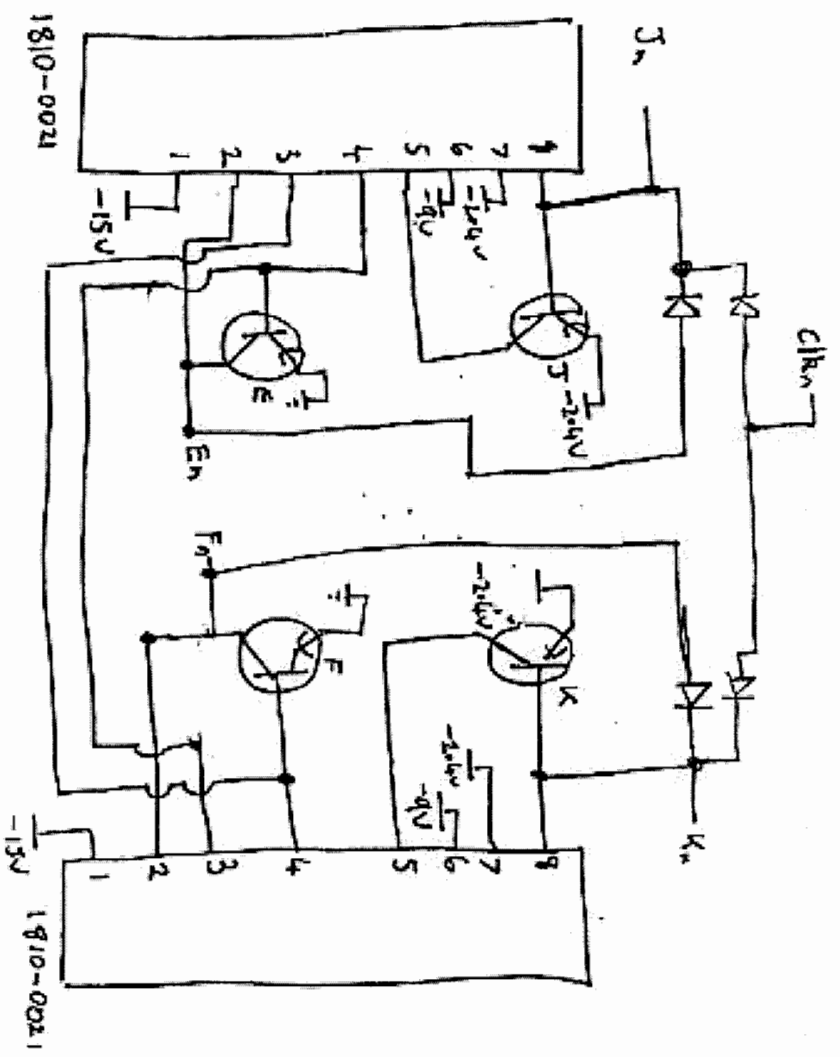
T_0 Polu	1y1	Arcl
62	55	72
T_0 Aut	int x	hyperb
66	64	67
RCL	e^x	$\sin x$
61	74	70
Acc -	$\ln x$	$\cos x$
63	65	73
Acc +	$\log x$	$\tan x$
60	75	71

a	b	x^y
13	14	30
c	d	Roll
16	17	31
e	f	Roll
12	15	22
$y-x()$	$y \rightarrow ()$	$\cdot \nabla$
40	24	25
$x \rightarrow ()$	$x \leftarrow ()$	∇
23	67	27

HPa100 B keyboards ①



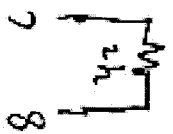
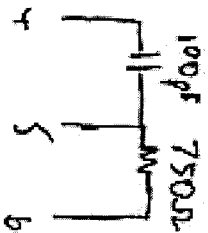
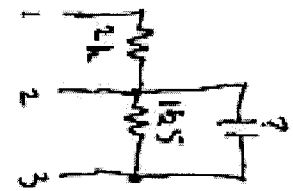
HP9100 Flip-Flop PCB sheet ① 09100-66502



Flip-flop (1 of 20)

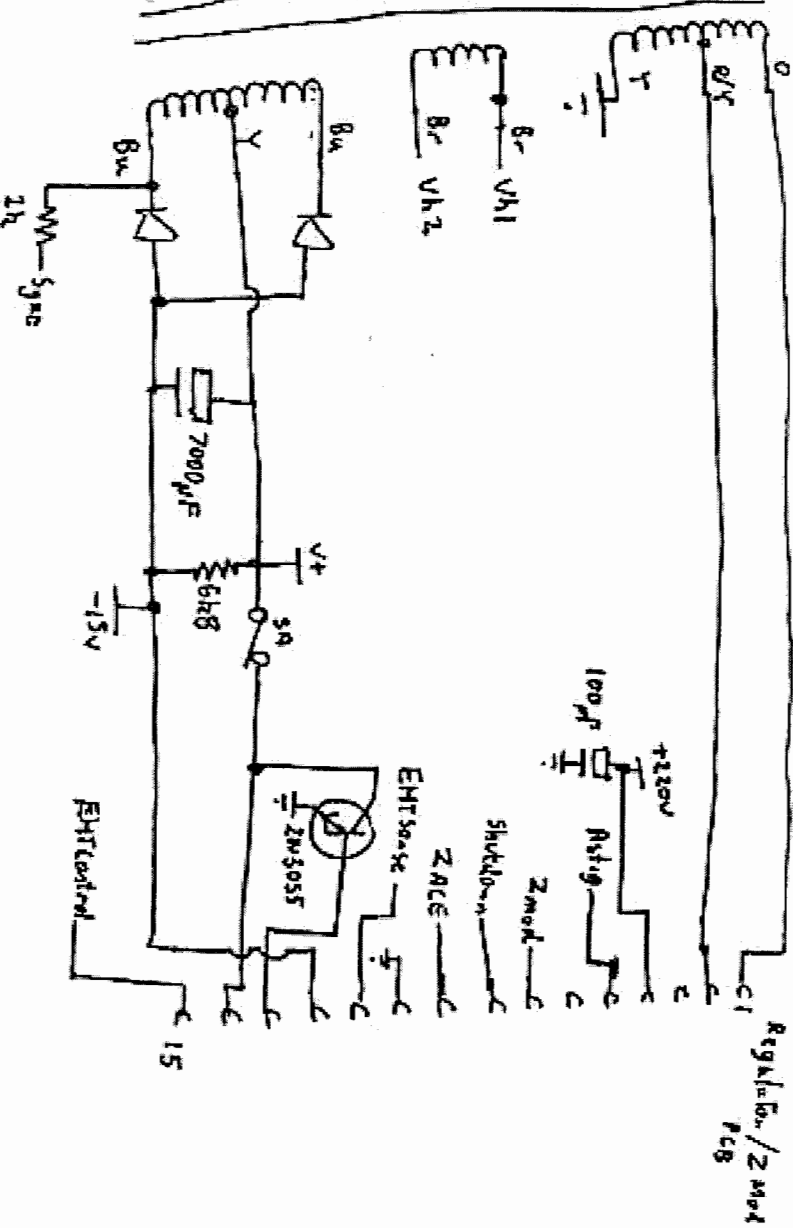
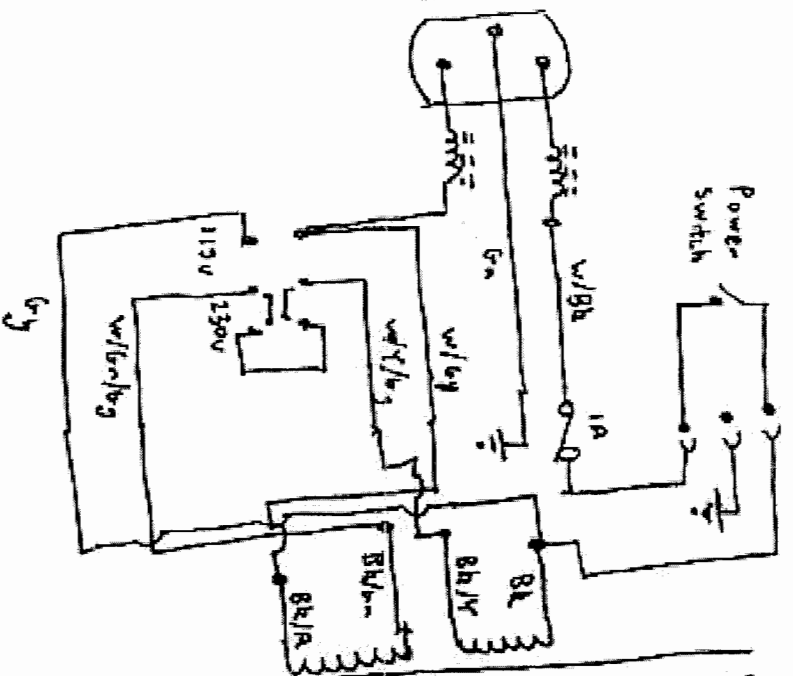
1810-0021
Hybrid

	1	2	3	4	5	6	7	8
1	-	2k	3.5k	81	81	81	81	81
2	2k	-	1.5k	81	81	81	81	81
3	3.5k	1.5k	-	81	81	81	81	81
4	81	81	81	-	750 Ω	750 Ω	81	81
5	81	81	81	750 Ω	-	750 Ω	81	81
6	81	81	81	81	81	-	2k	-
7	81	81	81	81	81	2k	-	-
8	81	81	81	81	81	-	-	-





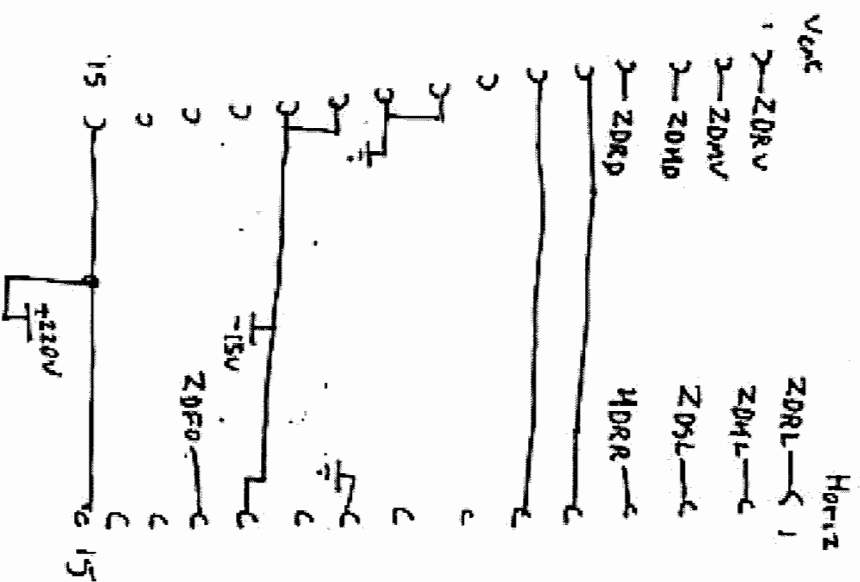
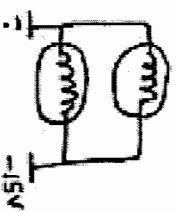
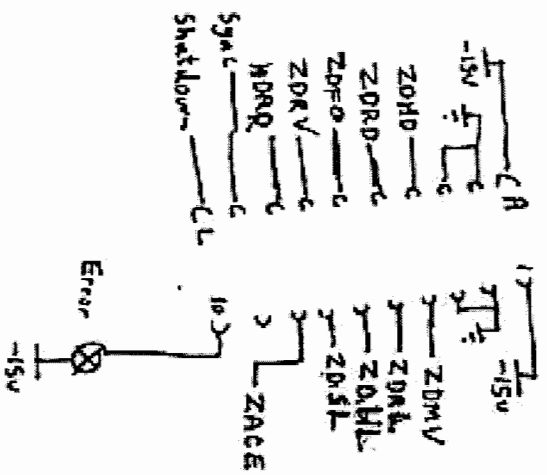
MP9100 Flip-Flop



LV PSU

HP9100 PSU sheet ①

(To processor)



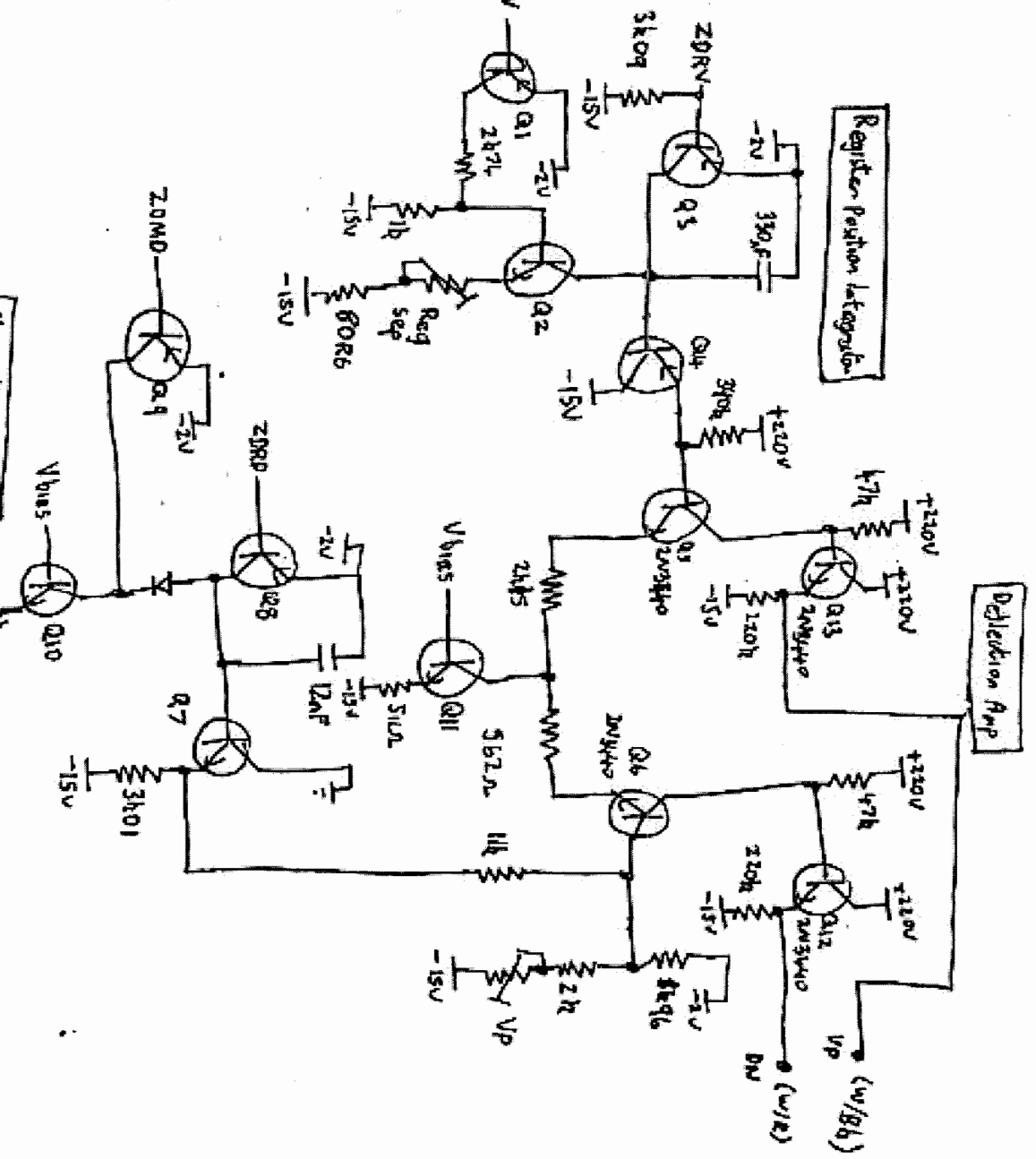
Top case wiring

MP4110 PSU sheet ②



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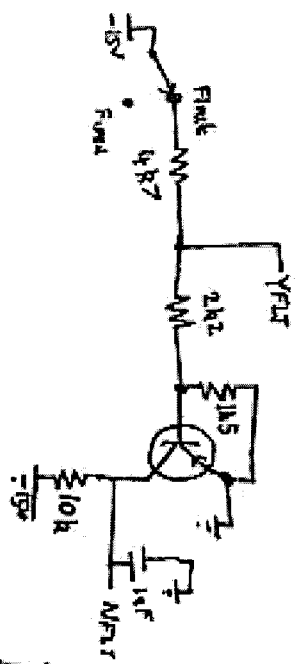
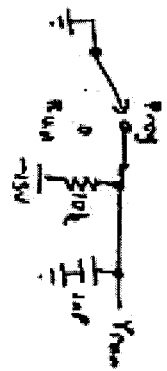
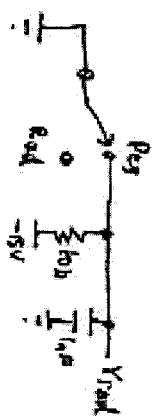


HP9100 Vertical Deflection PCB 09100-66516

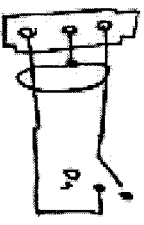
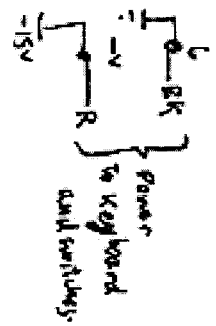
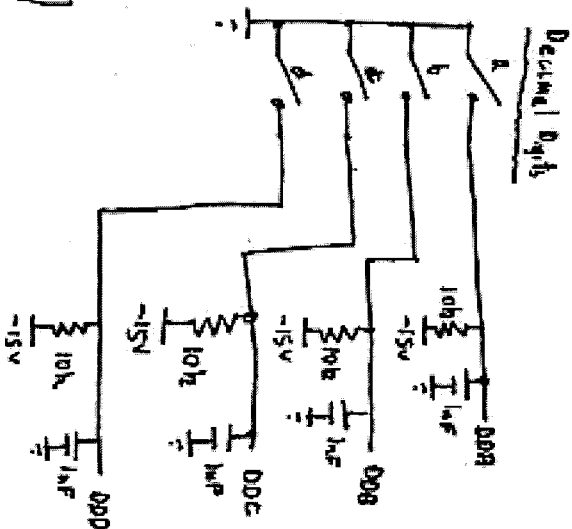


1 centre

HP9100 Horizontal Deflection PCB 09100-66515



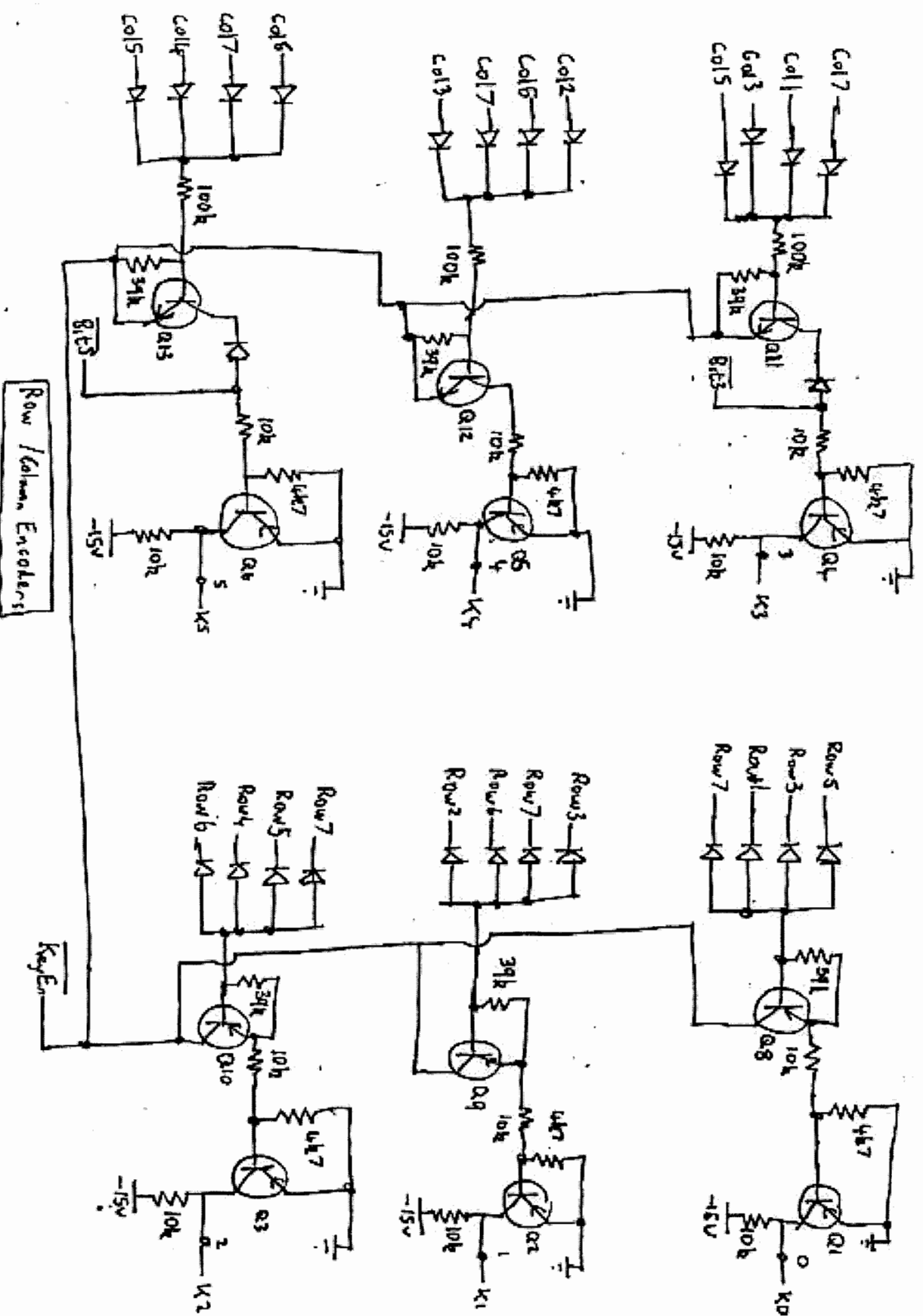
- Contacts closed
- 0 a b d
 - 1 b d
 - 2 a c
 - 3 c
 - 4 a
 - 5 none
 - 6 a b c
 - 7 b c
 - 8 a b
 - 9 b



(To PSU Means Wiring)

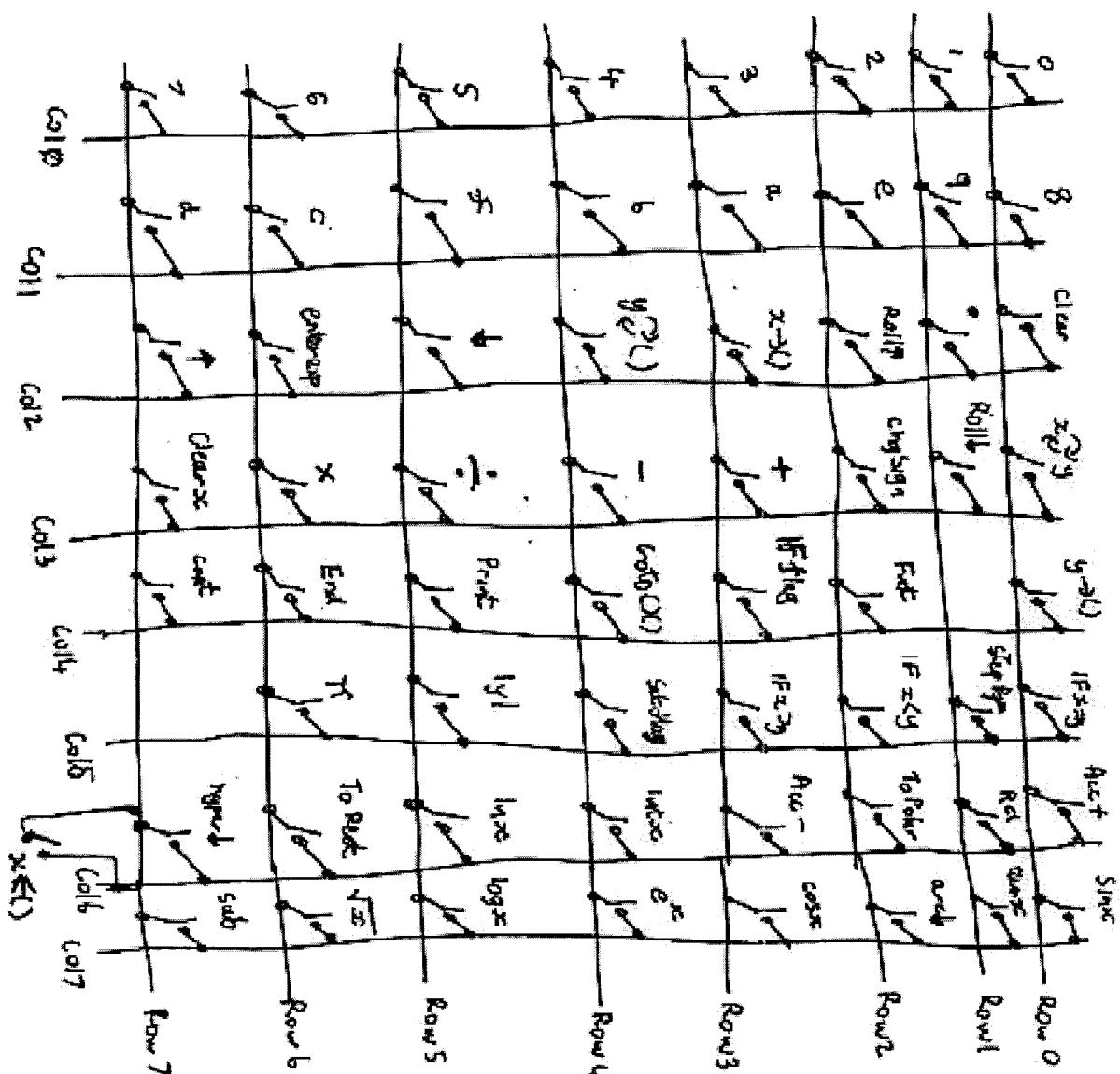
HP 9100 B Keyboard Shift ①

Connector PCB



HP9100B Keyboard Shift ②

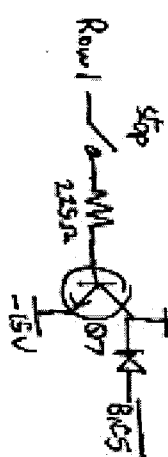
09100-66581



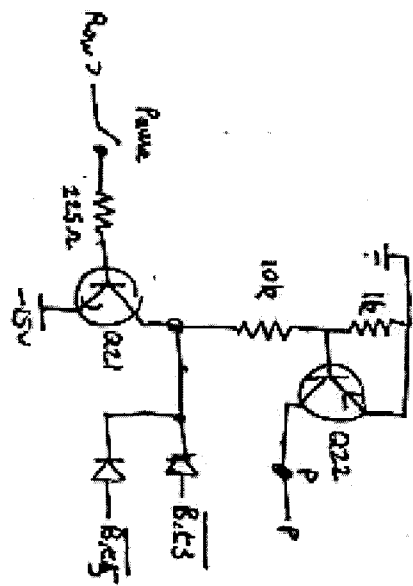
Key Matrix

MP9100B Keyboard Sheet 2

09100-66581



Pause, Stop Keys



T_0 polar	$ y $	arcl
62	55	72
T_0 rot	$\ln x$	hyperb
66	64	67
RCL	e^x	$\sin x$
61	74	70
Acc -	$\ln x$	$\cos x$
63	65	73
Acc +	$\log x$	$\tan x$
60	75	71

a	b	$x \rightarrow y$
13	14	30
c	d	Roll
16	17	31
e	f	Roll
12	15	22
$y \rightarrow x$	$y \rightarrow x$	$\cdot \phi$
10	24	25
$x \rightarrow y$	$x \rightarrow y$	$\uparrow$
23	67	27

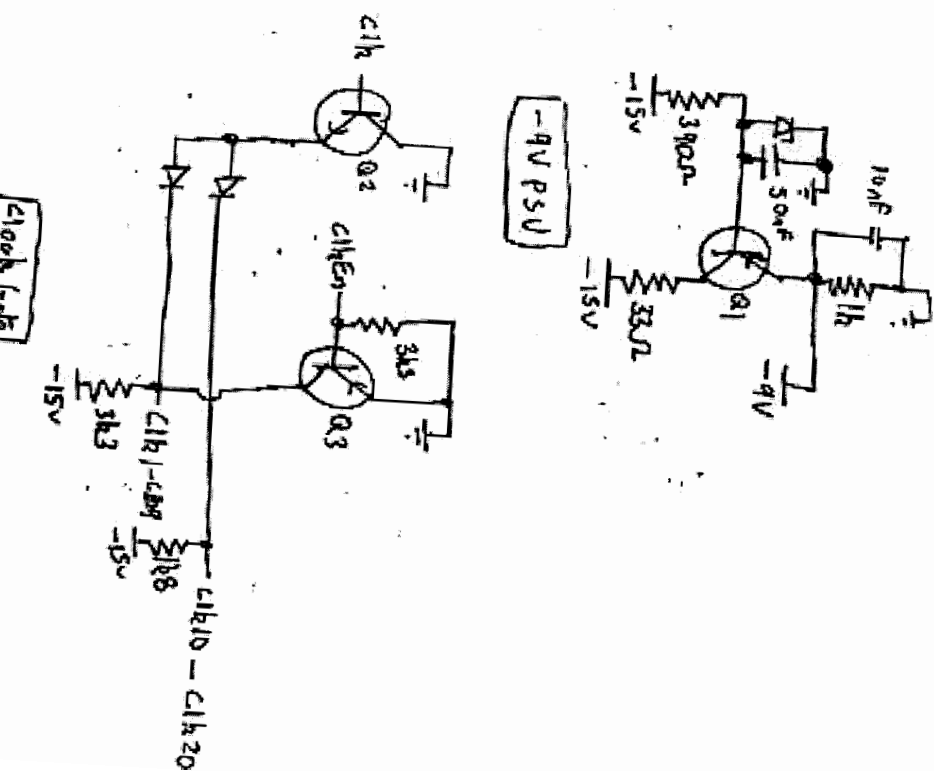
MPA100 B Kigwaps ①

$\sqrt{x}$	Chg Sign	Enter E_p	Clear x
7 6	3 2	2 6	3 7
$\div$	7	8	9
3 5	0 7	1 0	1 1
$\times$	4	5	6
3 6	0 4	0 5	0 6
-	1	2	3
3 4	0 1	0 2	0 3
+	0	0	0 0
3 3	0 0	2 1	5 6

Clear	IF Flag	Set flag
2 0	4 3	5 4
Print	IF $x \leq y$	Pause
4 2	5 2	5 7
Print	IF $x \leq y$	stop
4 5	5 0	4 1
sub	IF $x > y$	End
7 7	5 3	4 6
cont	goto ()	stop Pgm
4 7	4 4	5 1

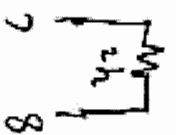
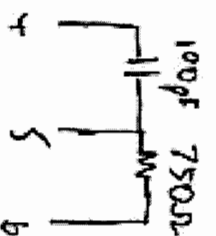
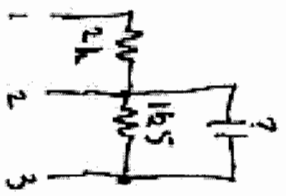
HP 9100B keypad (2)

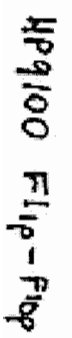
(Parent)



1810-0021
Hybrid

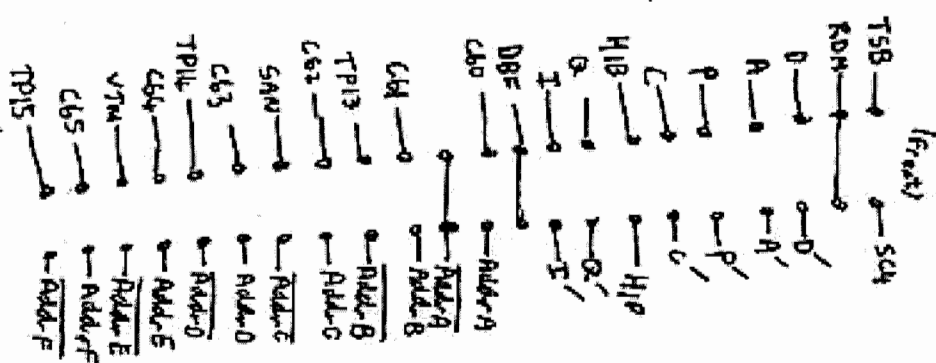
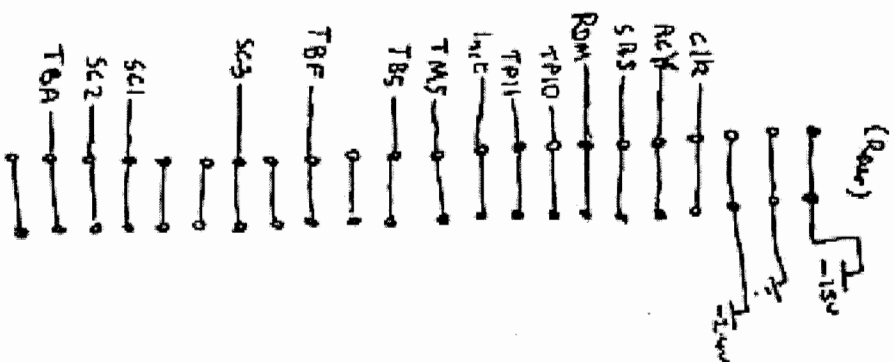
	1	2	3	4	5	6	7	8
1	-	2h	3.5k	81	81	81	81	81
2	2h	-	1.5k	81	81	81	81	81
3	3.5k	1.5k	-	81	81	81	81	81
4	∞	∞	∞	750Ω 81	750Ω	750Ω	81	81
5	∞	∞	∞	750Ω	750Ω	81	81	81
6	∞	∞	∞	∞	∞	∞	-	2h
7	∞	∞	∞	∞	∞	∞	2h	-
8	∞	∞	∞	∞	∞	∞	-	-



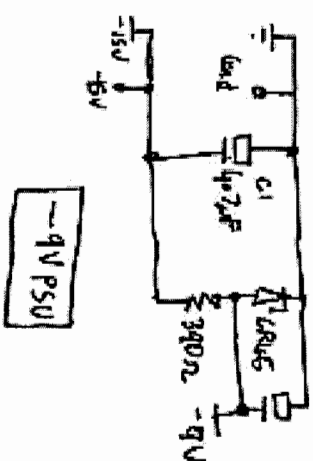
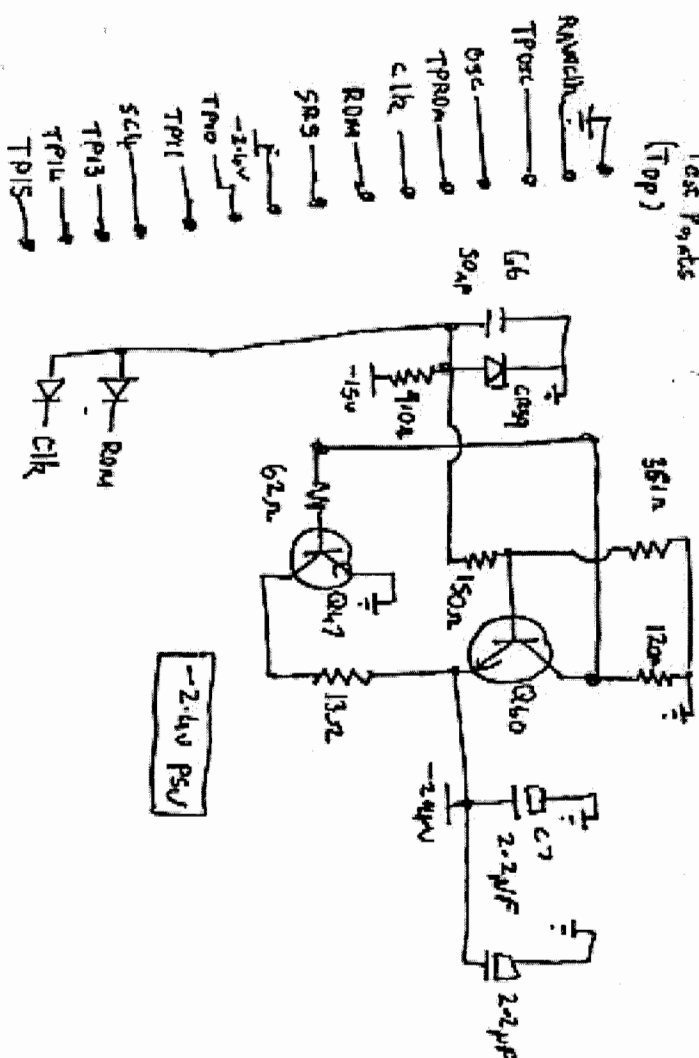


HP9100 Flip-Flop

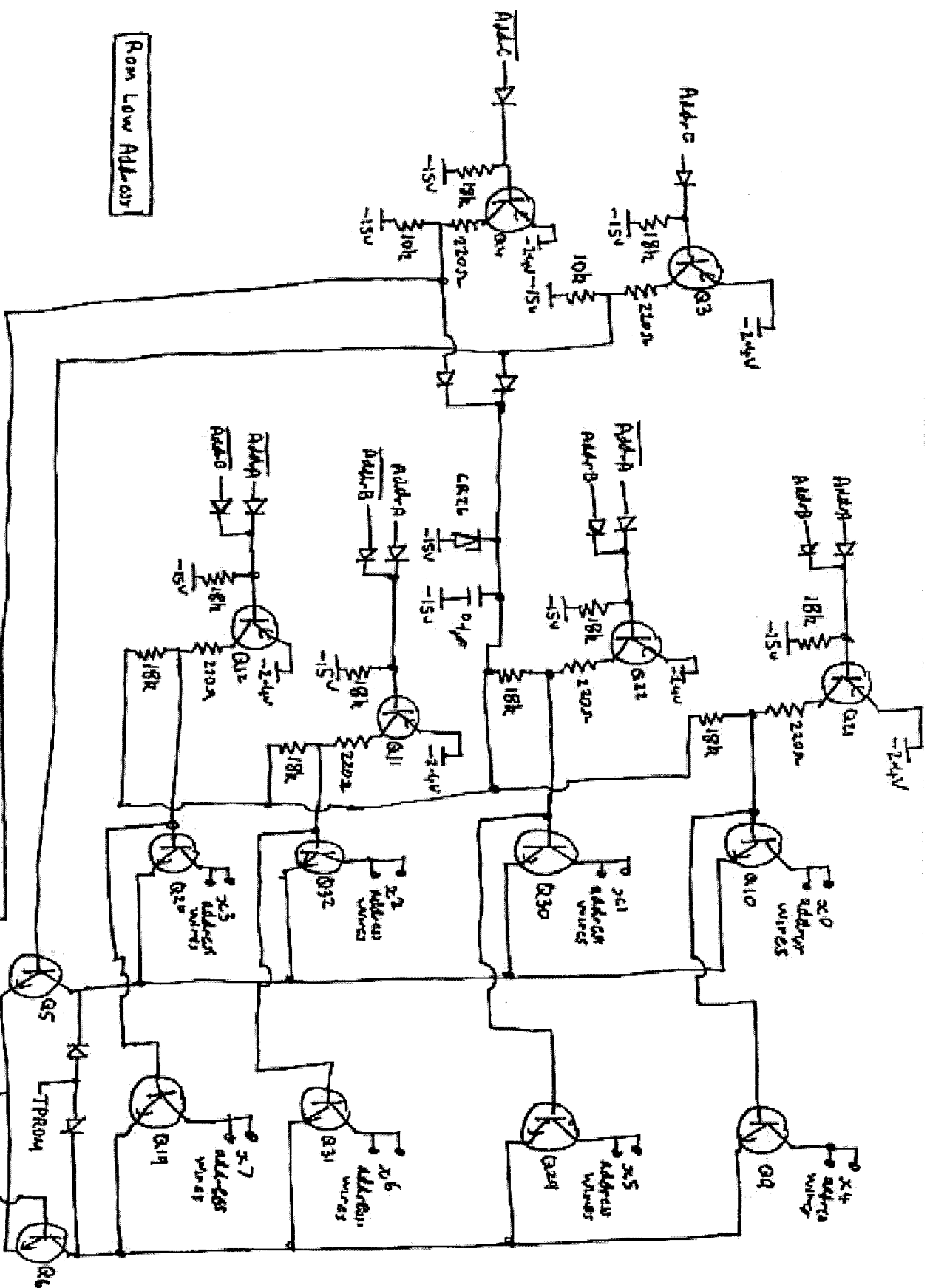
34567



Test Ports
(Top)

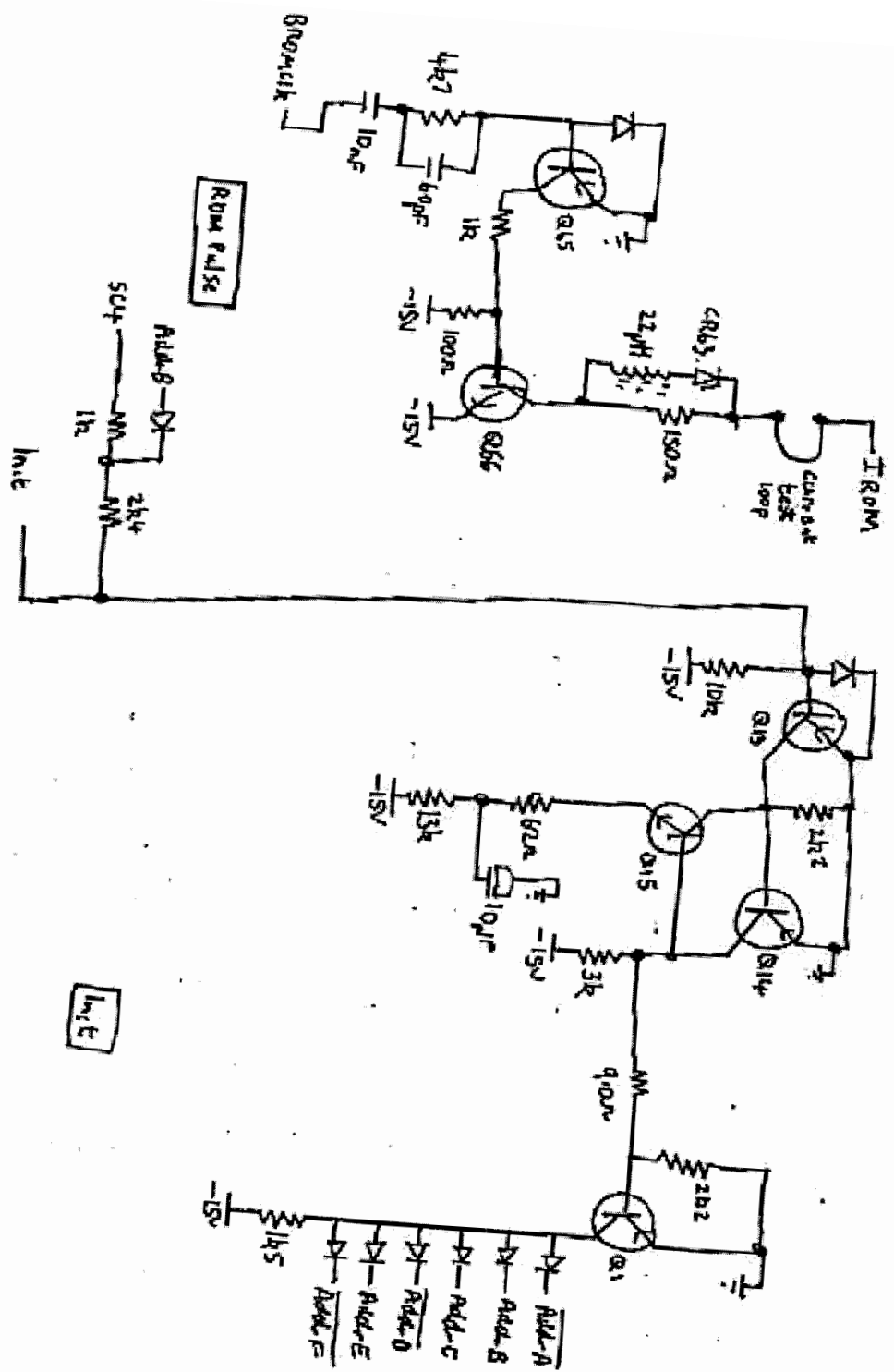


HPq100B Control Logic PCB 09100-6655) sheet ①

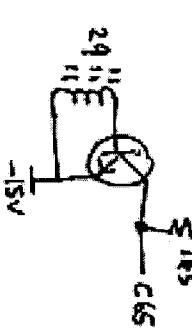
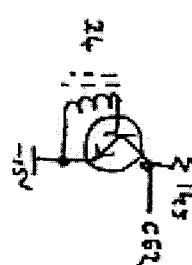
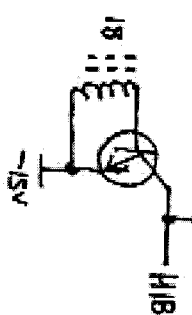
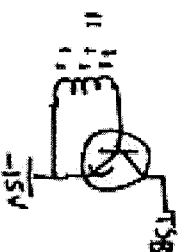
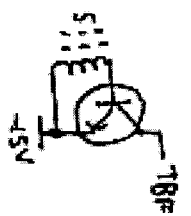
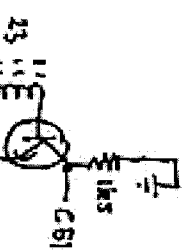
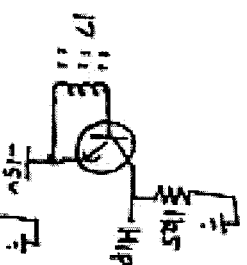
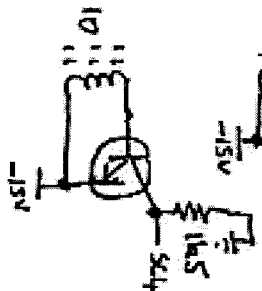
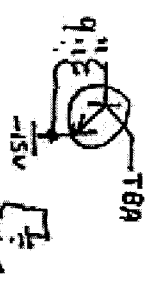
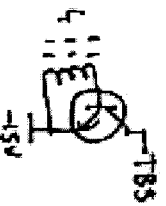
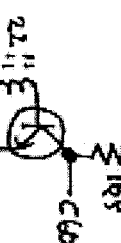
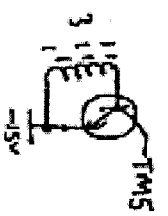
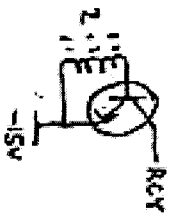
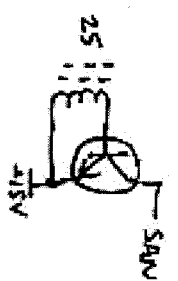
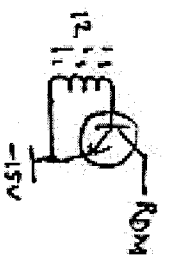
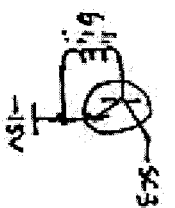
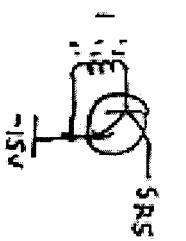


HP9100g Control Logic - 908 Sheet ④ 09100-66551

±ROM



HP 9100B Control Logic PCB sheet 5 09100-66551

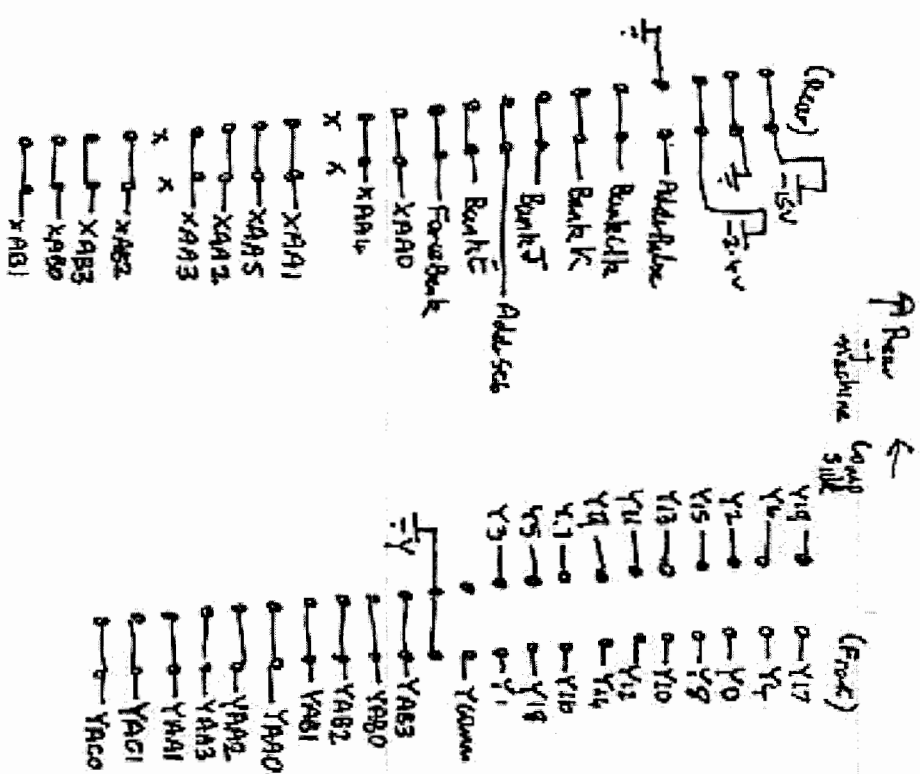


ROM P24

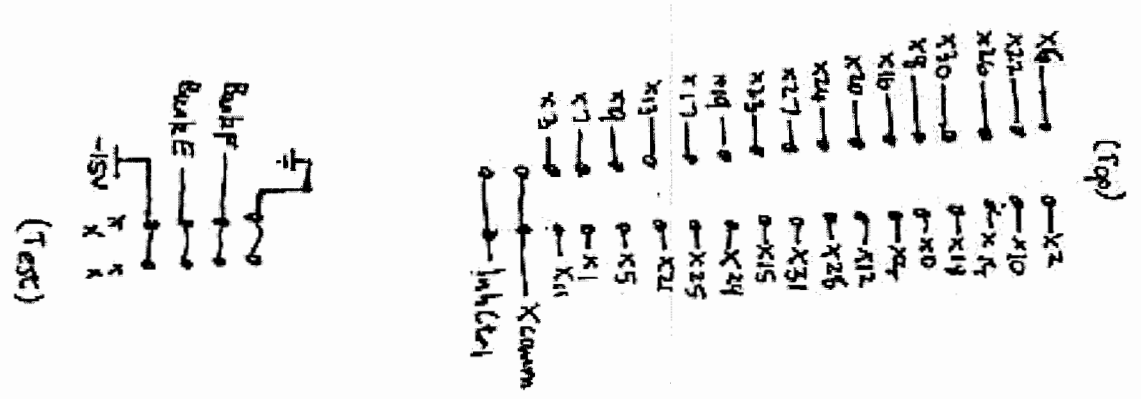
Comp 1 at rear of machine.

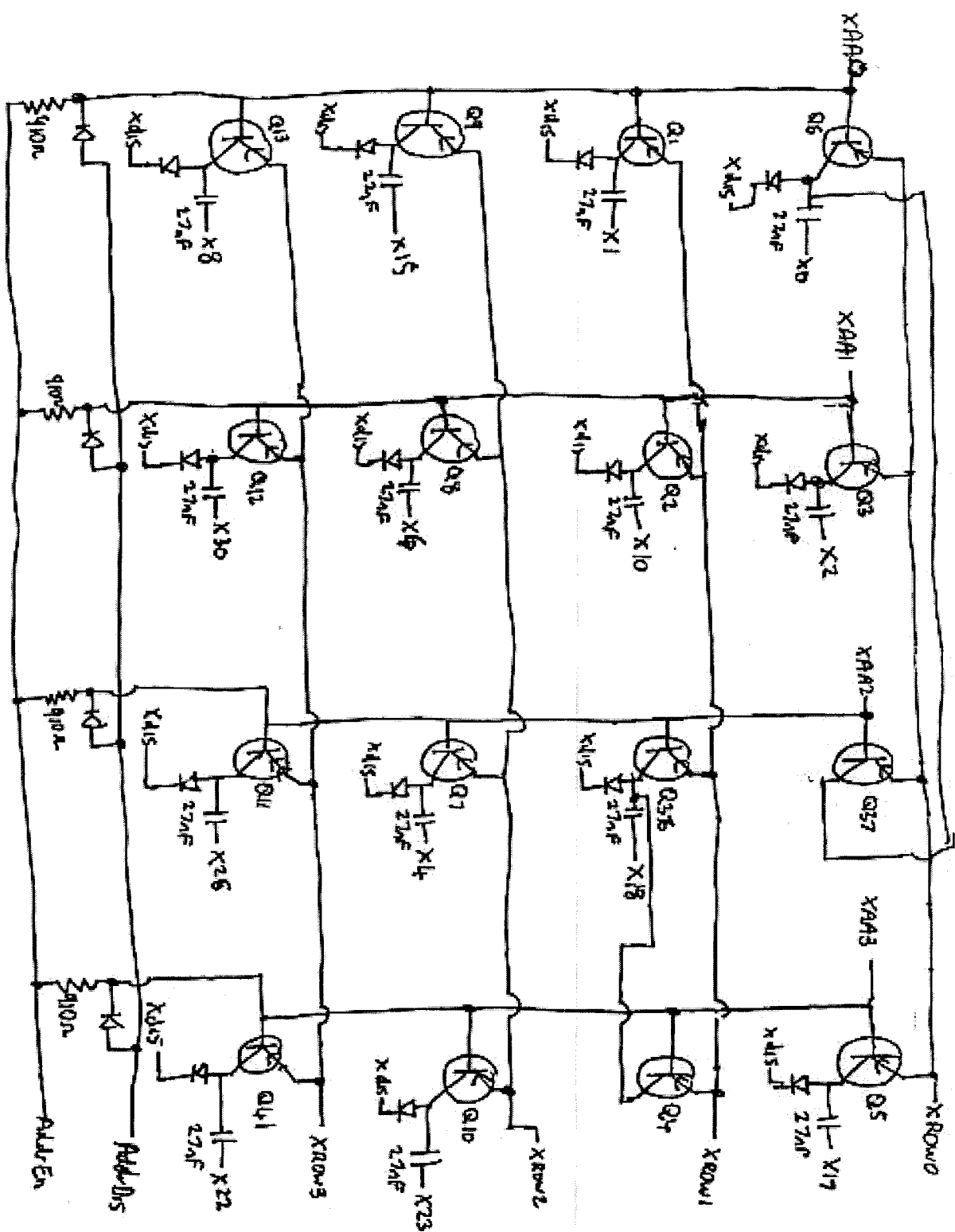
HP9100B Control Logic PCB Sheet 6

09100-66551

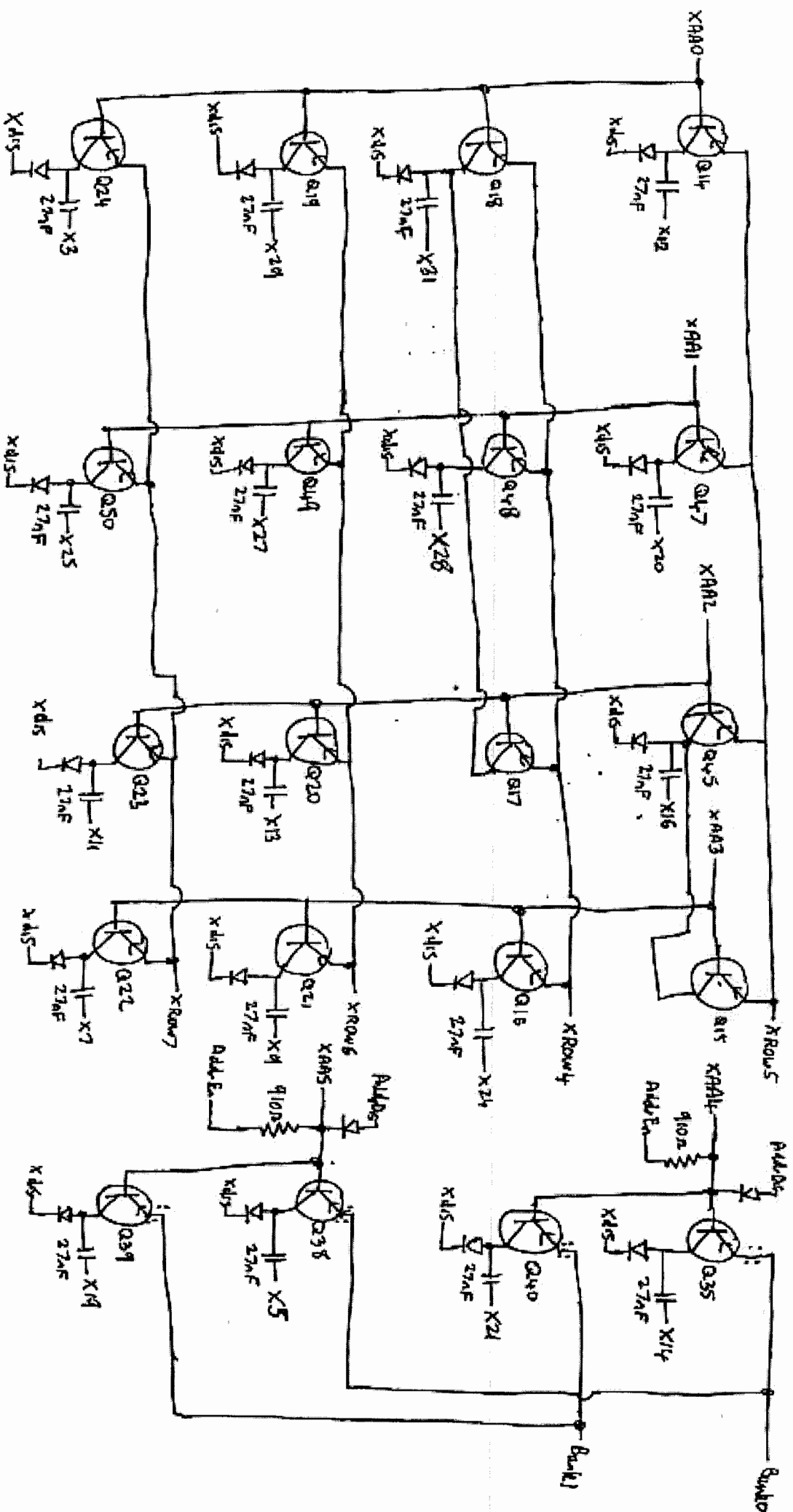


Connections





X Drivers 1



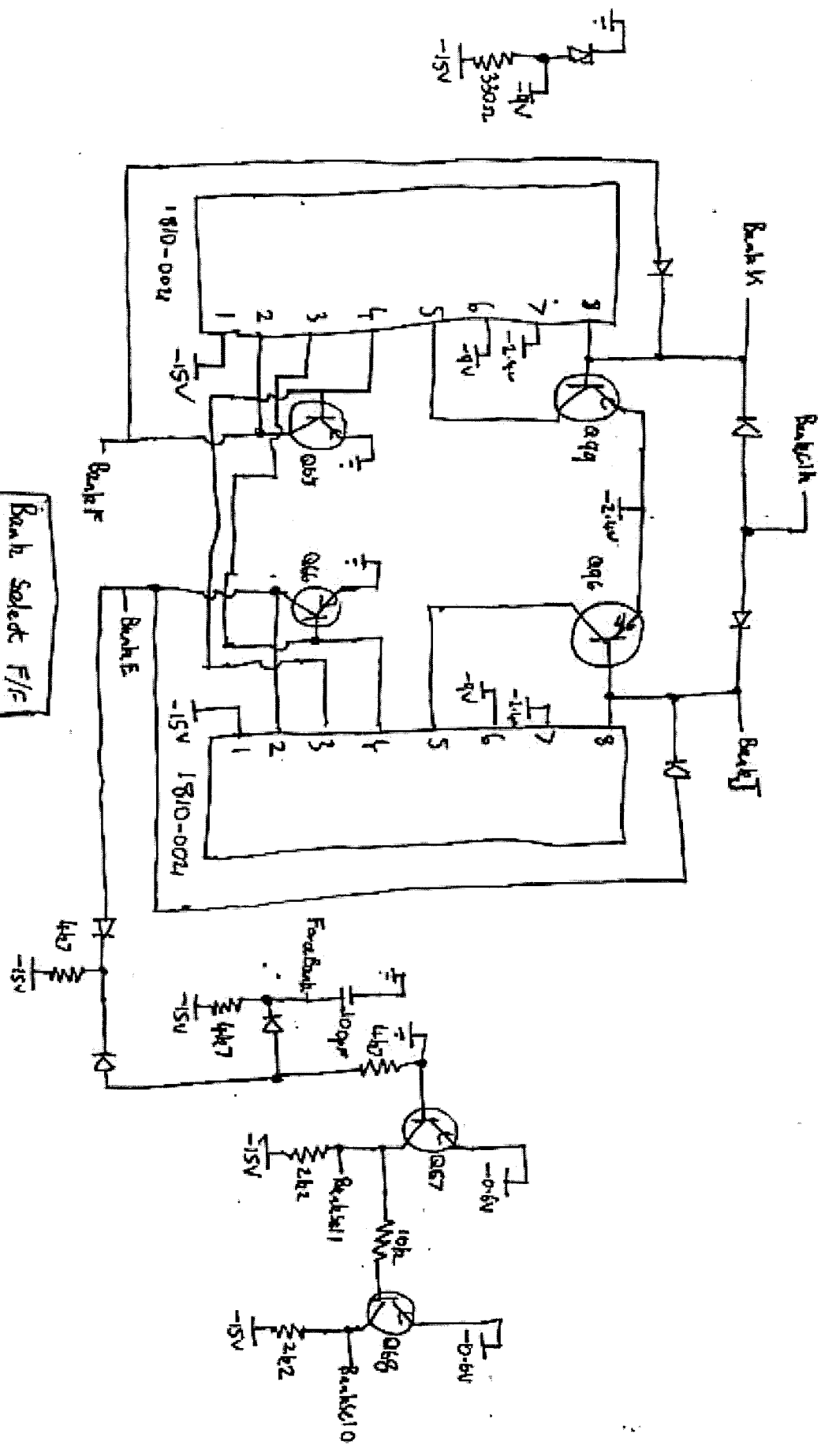
X Drivers 2

HP9100B Core Address PCB Sheet 3

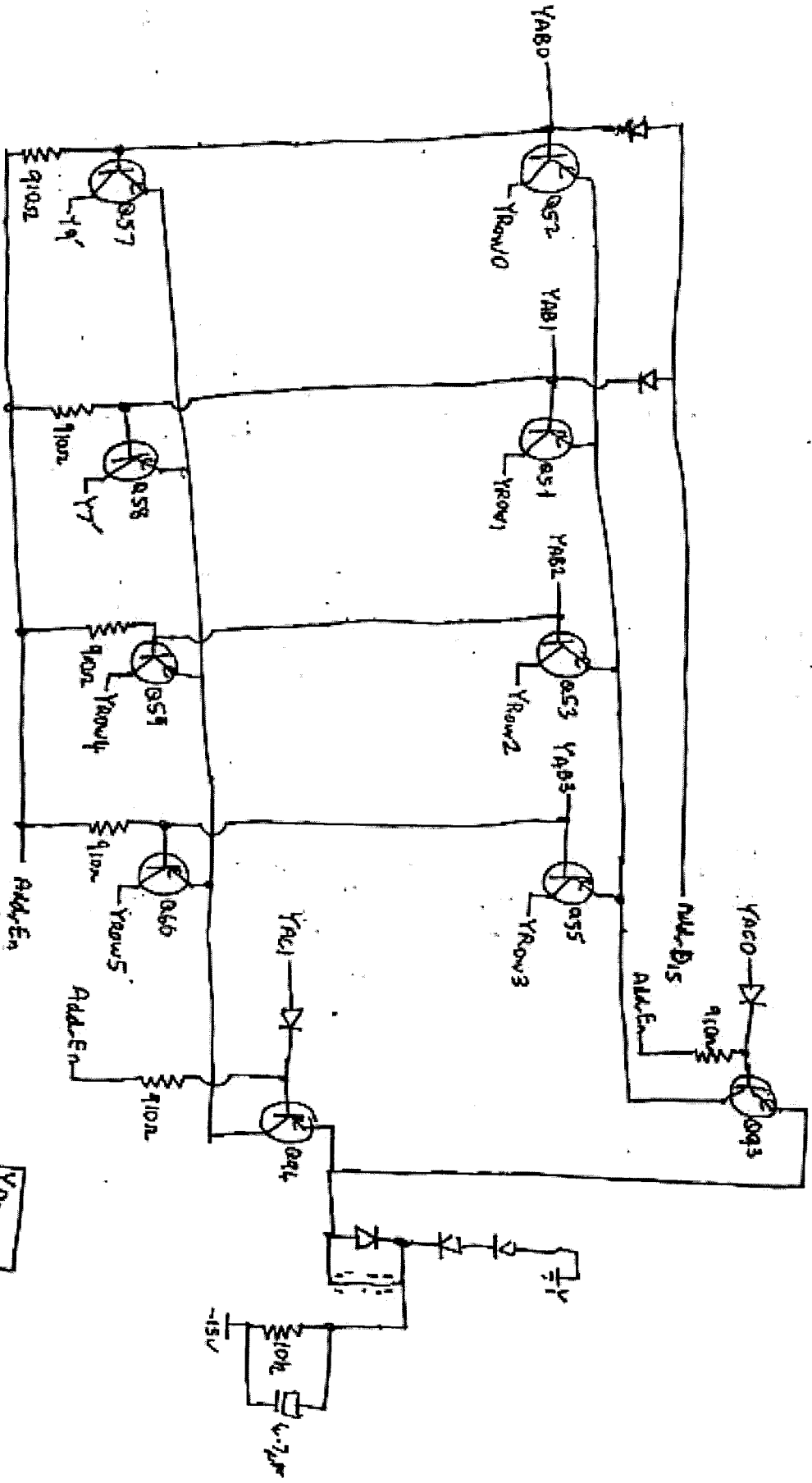
09100-66553



09100 - 66553

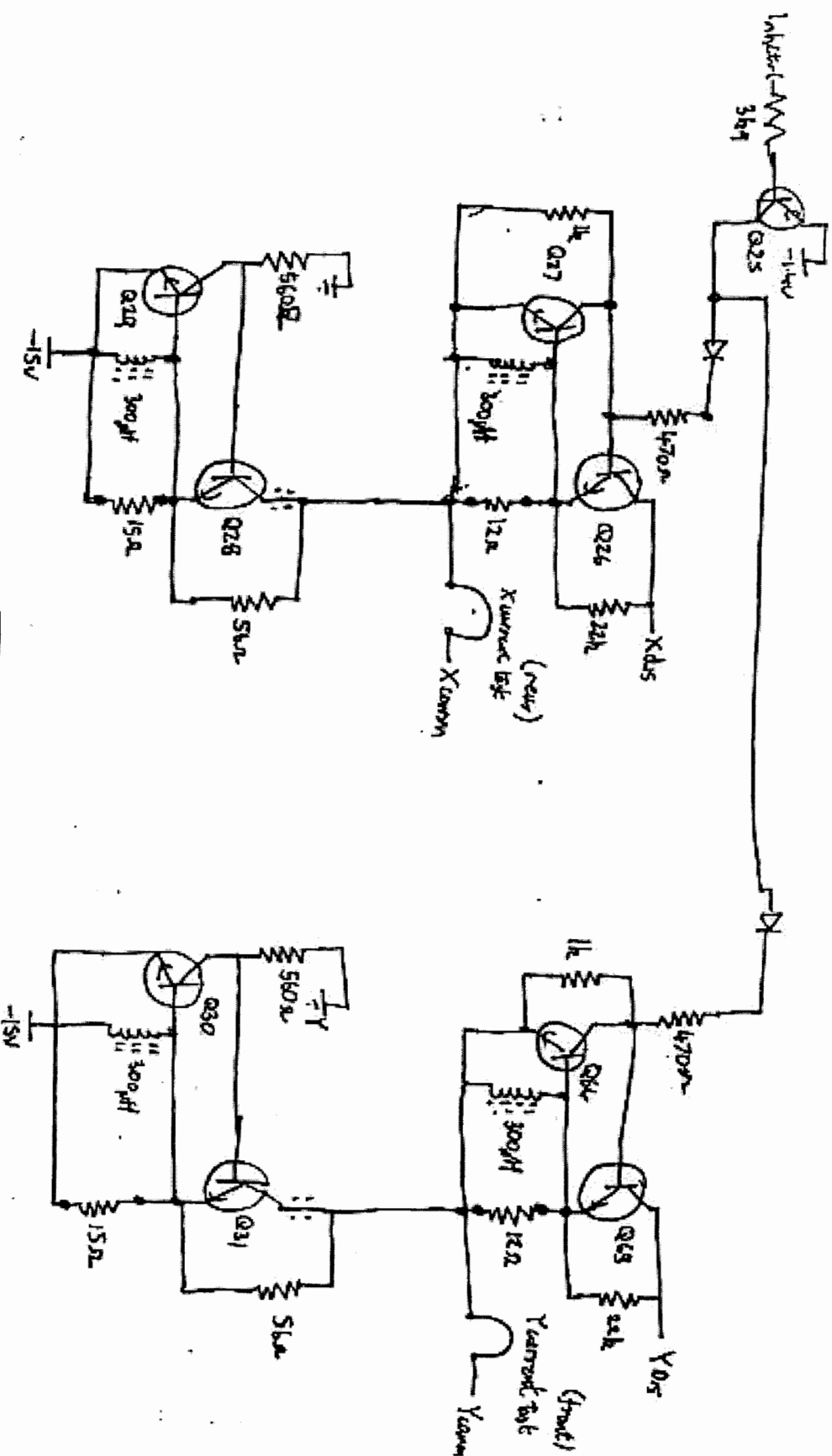


HP91008 Core Address PCO Sheet ⑤ 09100-66553

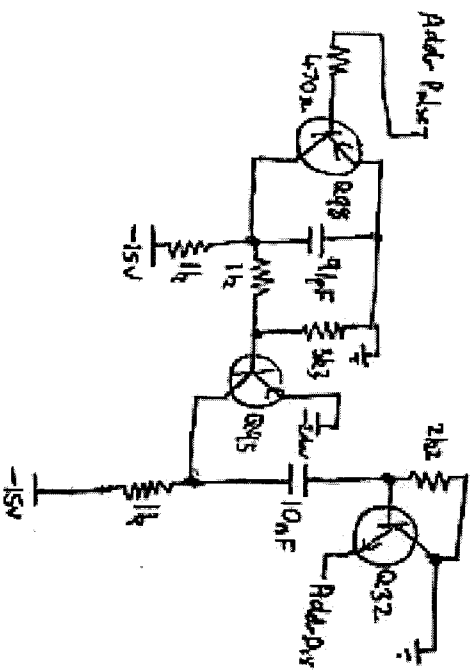
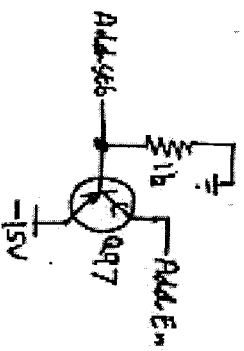


HP9100B Core Address PCB sheet ⑦

09100-66553

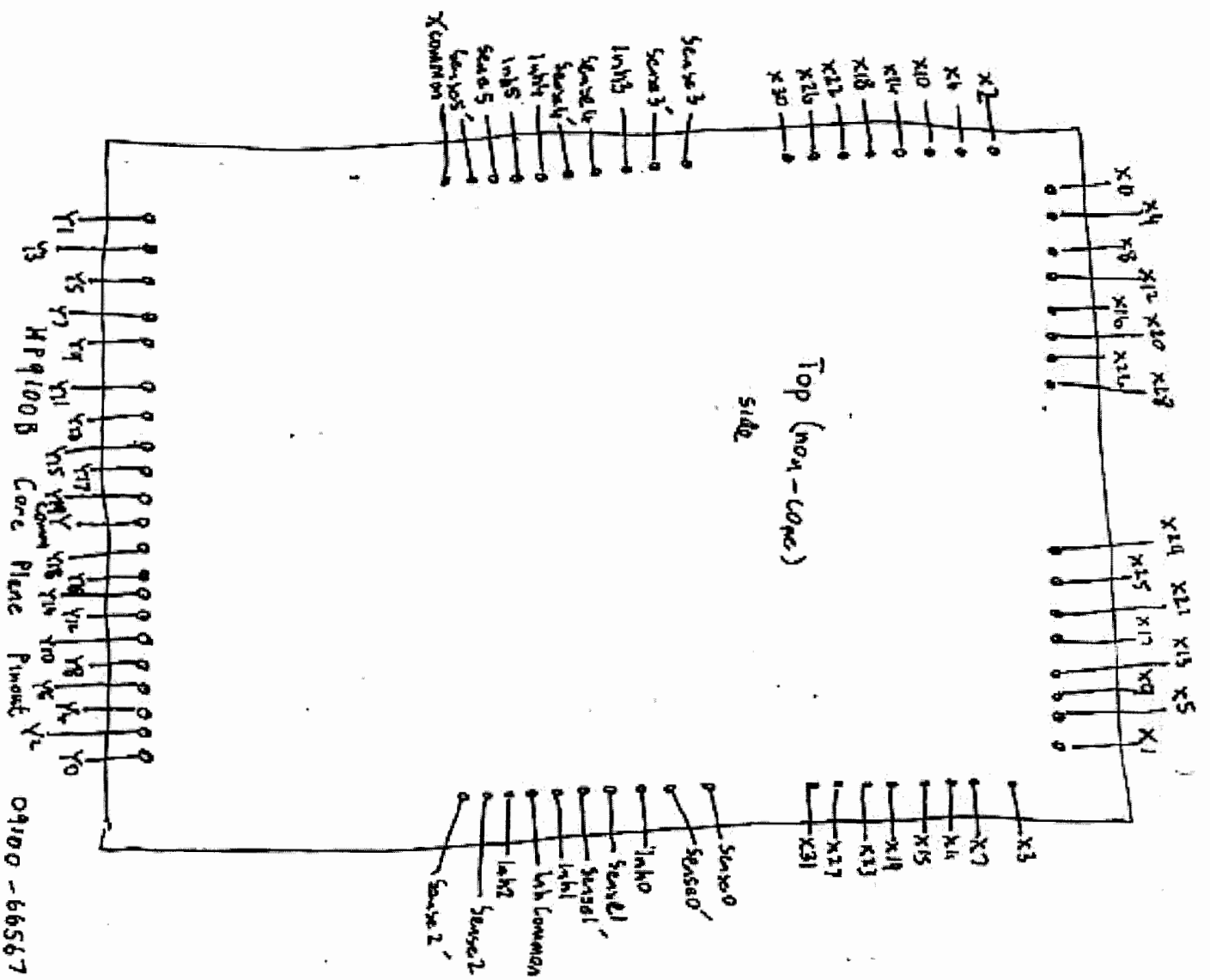


Current Source

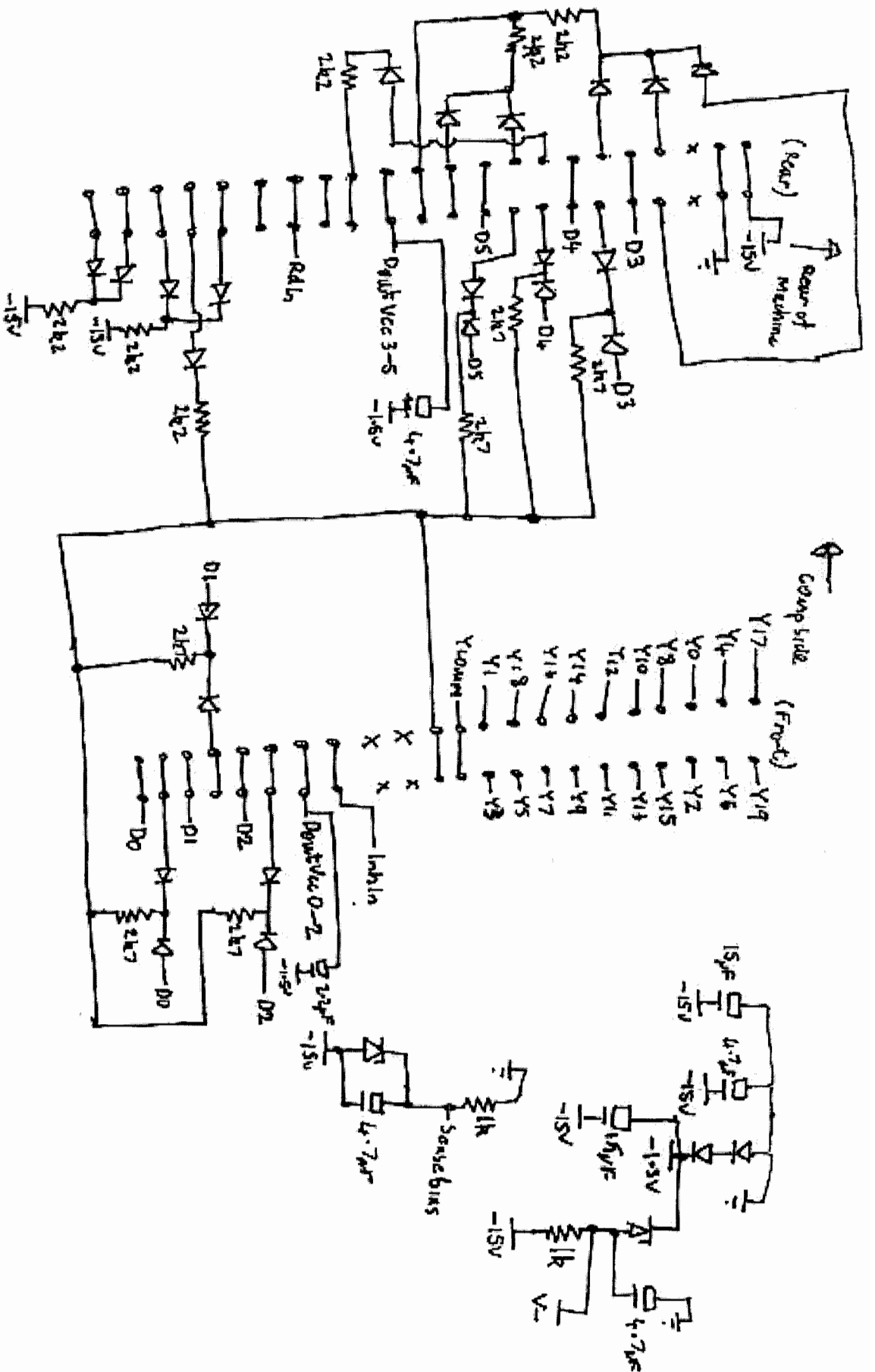


Address Control

Top (not-core)
side



09100-66567



Connectors, misc logic

(Top)

X-EDM
Lmth et al

— 4 —

Left Place

MPQ100B Core/Data PCB Sheet ②

09100-66554

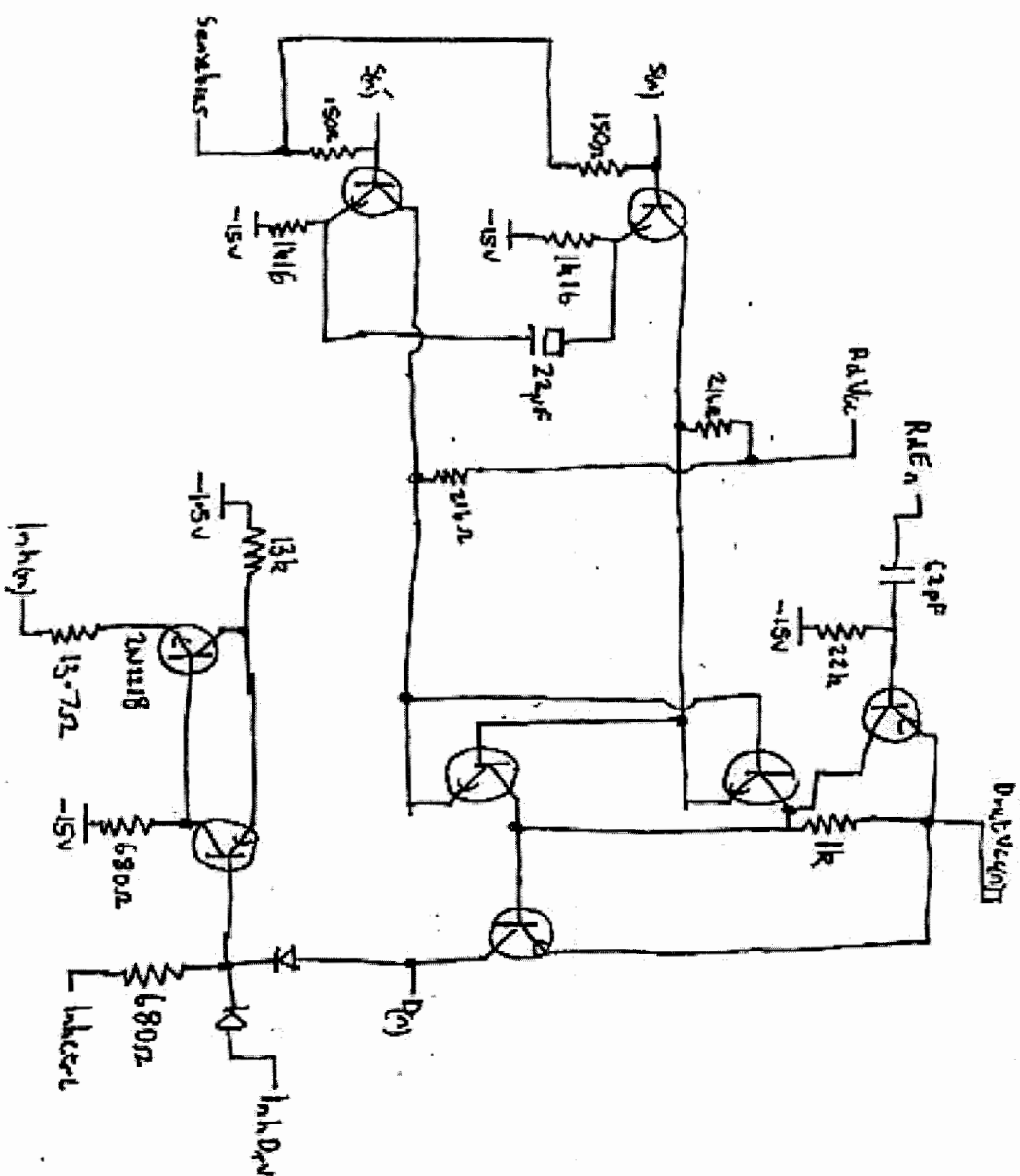
10/10/10

65-9549-109

Love Place

12X20K6

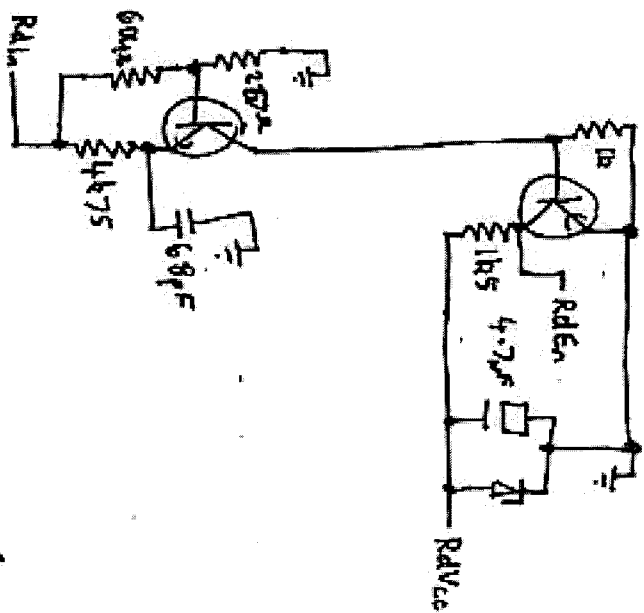
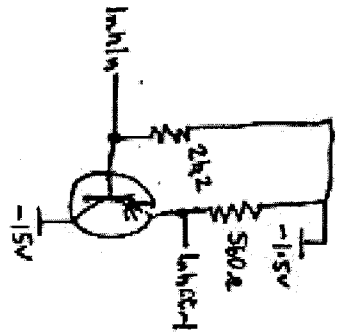
Sample	Depth (m)
Sample 1	50
Sample 2	50
Sample 3	51
Sample 4	51
Sample 5	52
Sample 6	52
Sample 7	53
Sample 8	53
Sample 9	54
Sample 10	54
Sample 11	55
Sample 12	55



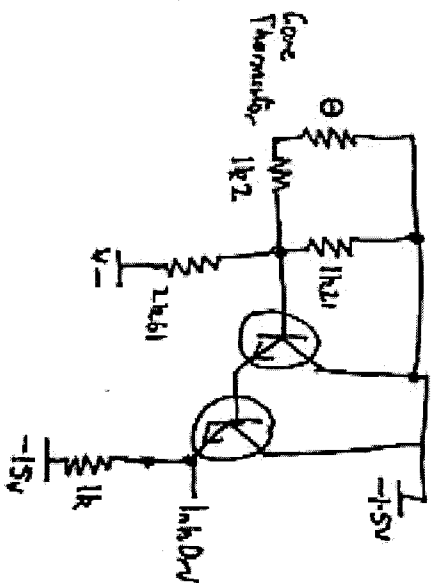
Sense/Inhibit (1 of 6)

KP9100B core/data PCB sheet ③

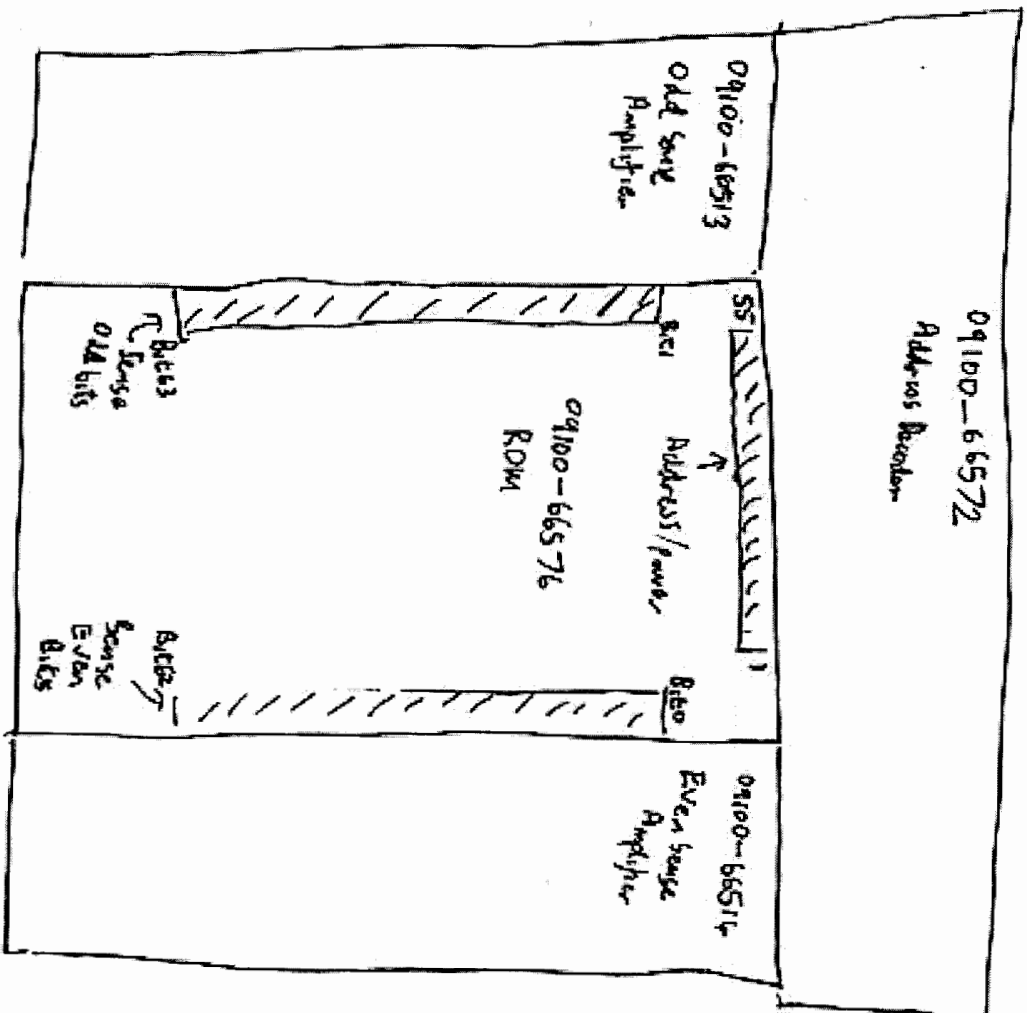
09100-66554



Control Logic

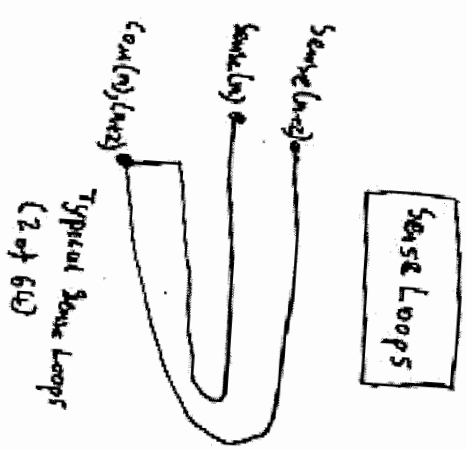


HP 9100 B core / DATA PCB sheet (4)



HP4100B ROM Array layout

Rev of
↑
Pachite

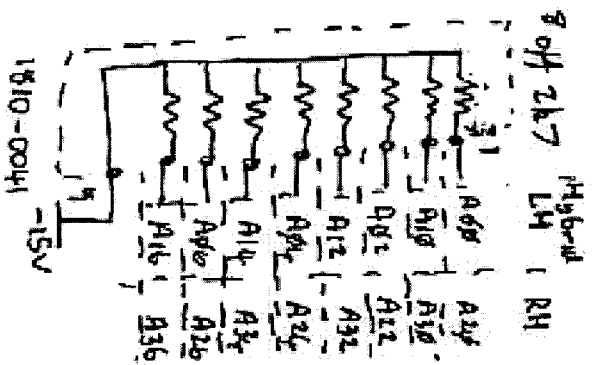


[All ROM connections shown from top
(track side) of PCB]

HP4100B ROM sheet ① 09100-66576

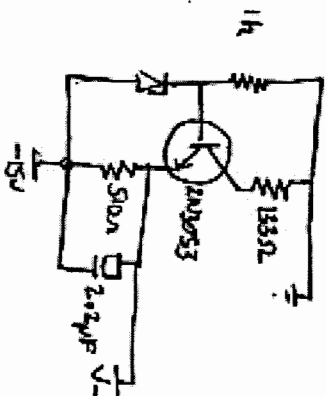


RM Sense Connections
(to 09100-66514)

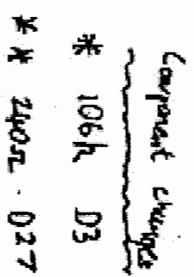


Hybrid
(Part of Row Driver, Sheet 1)

Hybrid Resistor Network
Connections

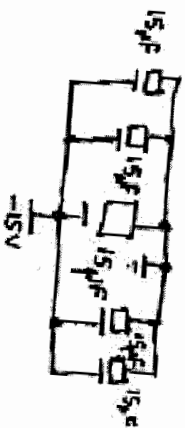


Some connections
to ROM (0400-66576)

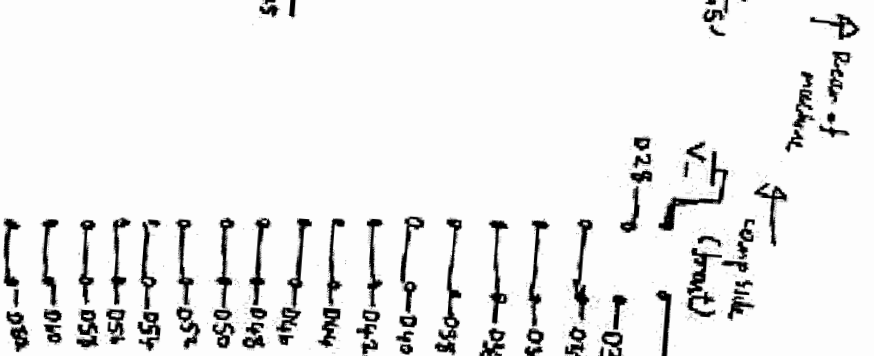
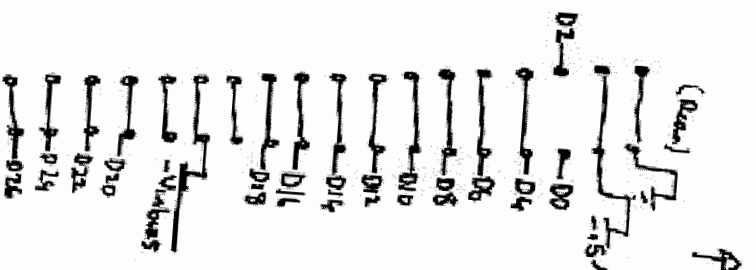


comQ2,3-0	→ sense0
sense1-0	→ sense1
comQ1,6-0	→ sense4
sense6-0	→ sense5
comQ8,10-0	→ sense8
sense10-0	→ sense9
comQ12,14-0	→ sense12
sense14-0	→ sense13
comQ18-0	→ sense16
sense18-0	→ sense17
comQ20,21-0	→ sense20
sense21-0	→ sense21
comQ24,26-0	→ sense24
sense26-0	→ sense25
comQ28,30-0	→ sense28
sense30-0	→ sense29
comQ34,36-0	→ sense32
sense36-0	→ sense33
comQ40,42-0	→ sense40
sense42-0	→ sense41
comQ44,46-0	→ sense44
sense46-0	→ sense43
comQ48,50-0	→ sense48
sense50-0	→ sense47
comQ52,54-0	→ sense52
sense54-0	→ sense51
comQ56,58-0	→ sense56
sense58-0	→ sense55
comQ60,62-0	→ sense60
sense62-0	→ sense59

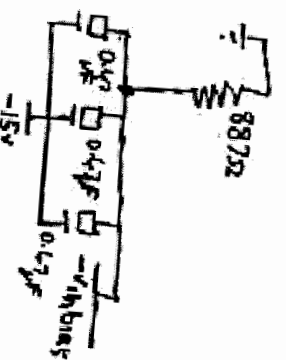
Sense Connections
to Rom (09100-66576)



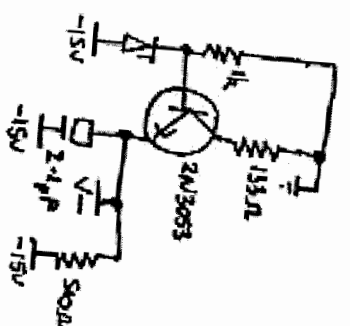
Connector 5



Output control



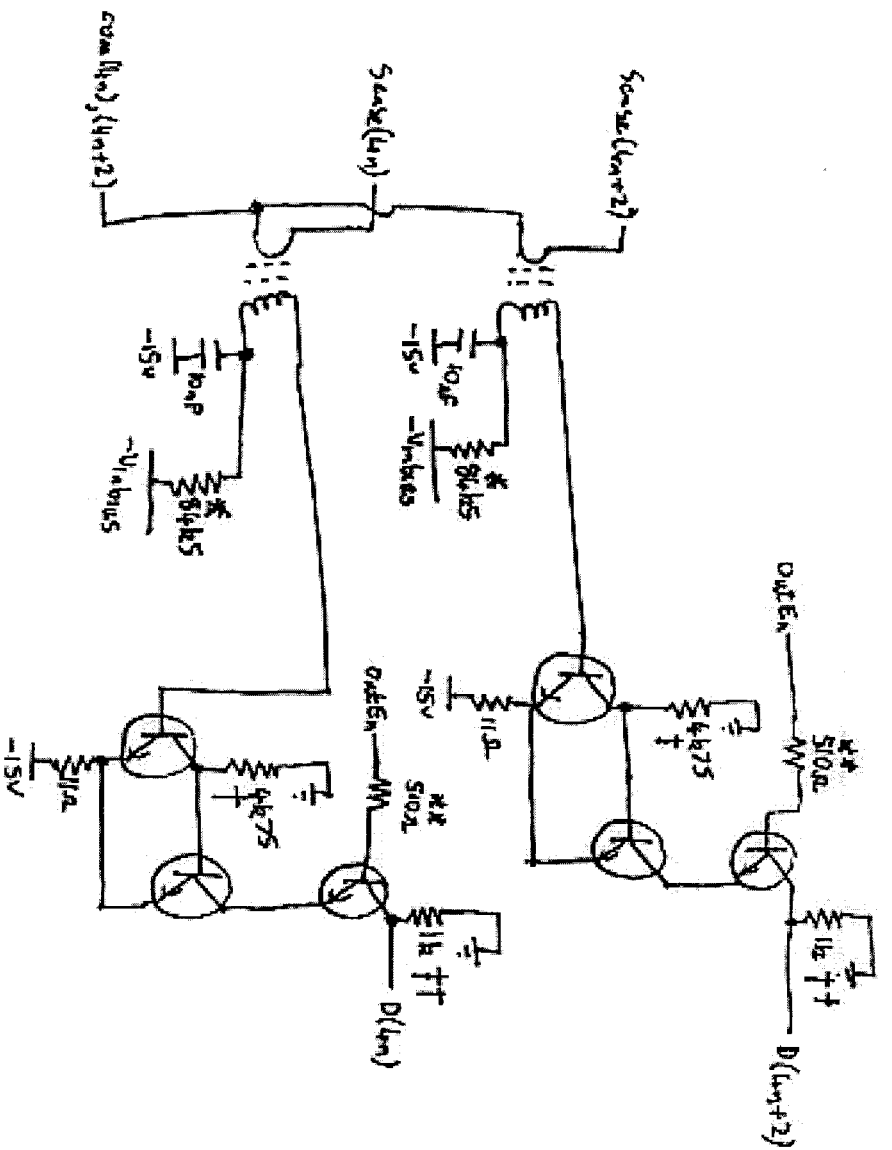
PSU



HP 9100 B Even Sense Amplifier

Sheet 1

09100-66514

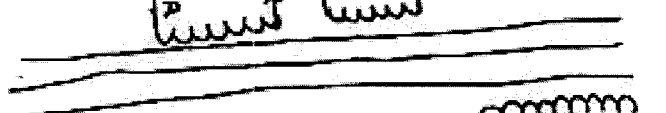


Component changes -
 # 110k D4
 75k D12
 + 2k32 D12, D18
 # 240k D0, D0, D1, D12, D25, D5
 150k D18 D48
 ++ 240k D0

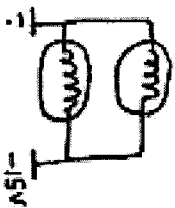
Sense Amplifier (2 of 32)

HP 91008 Even Sense Amplifier sub(2)

09100-66514

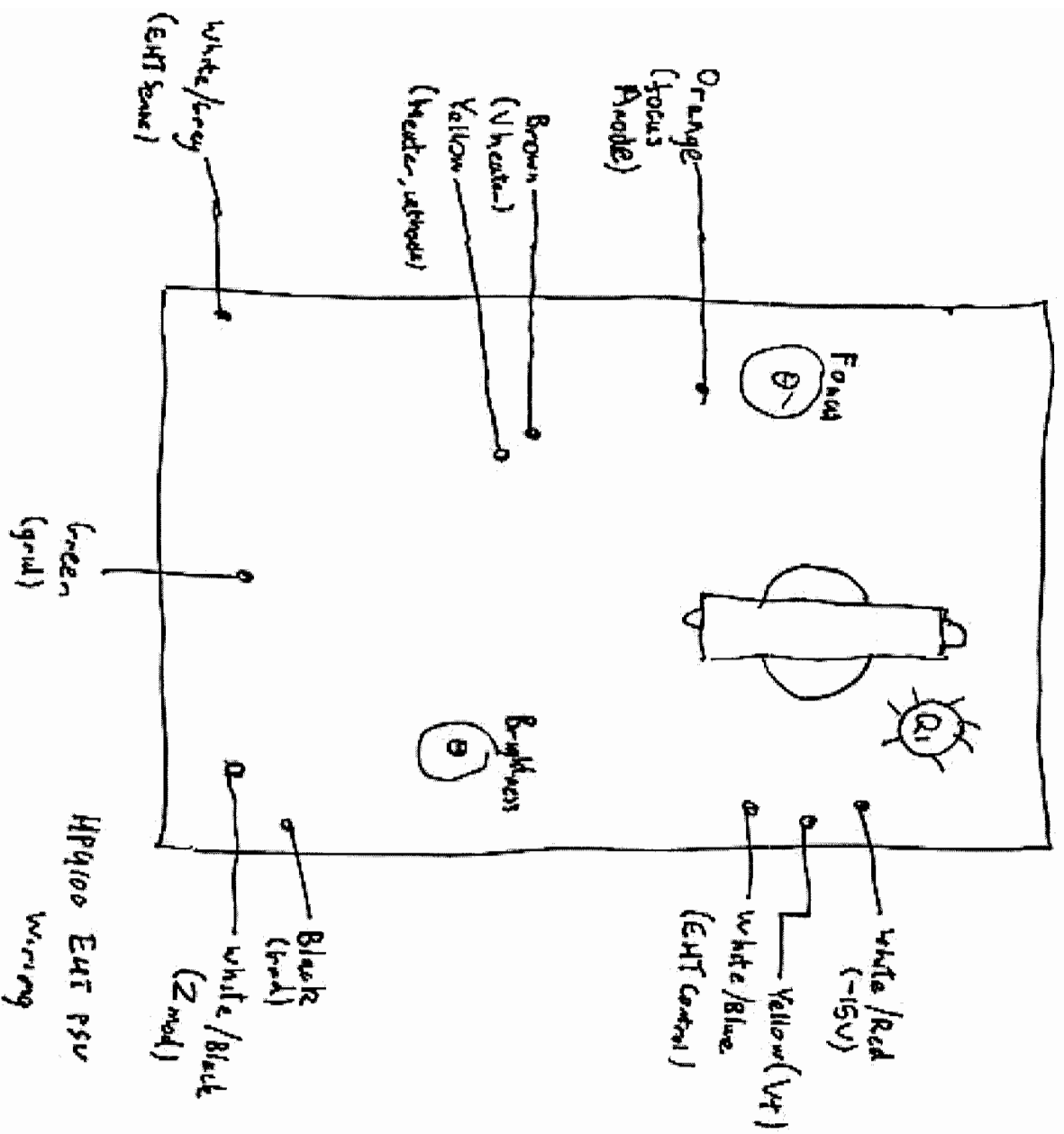


HP9100 PSU Sheet ①



HP4100 PSU Slot ②

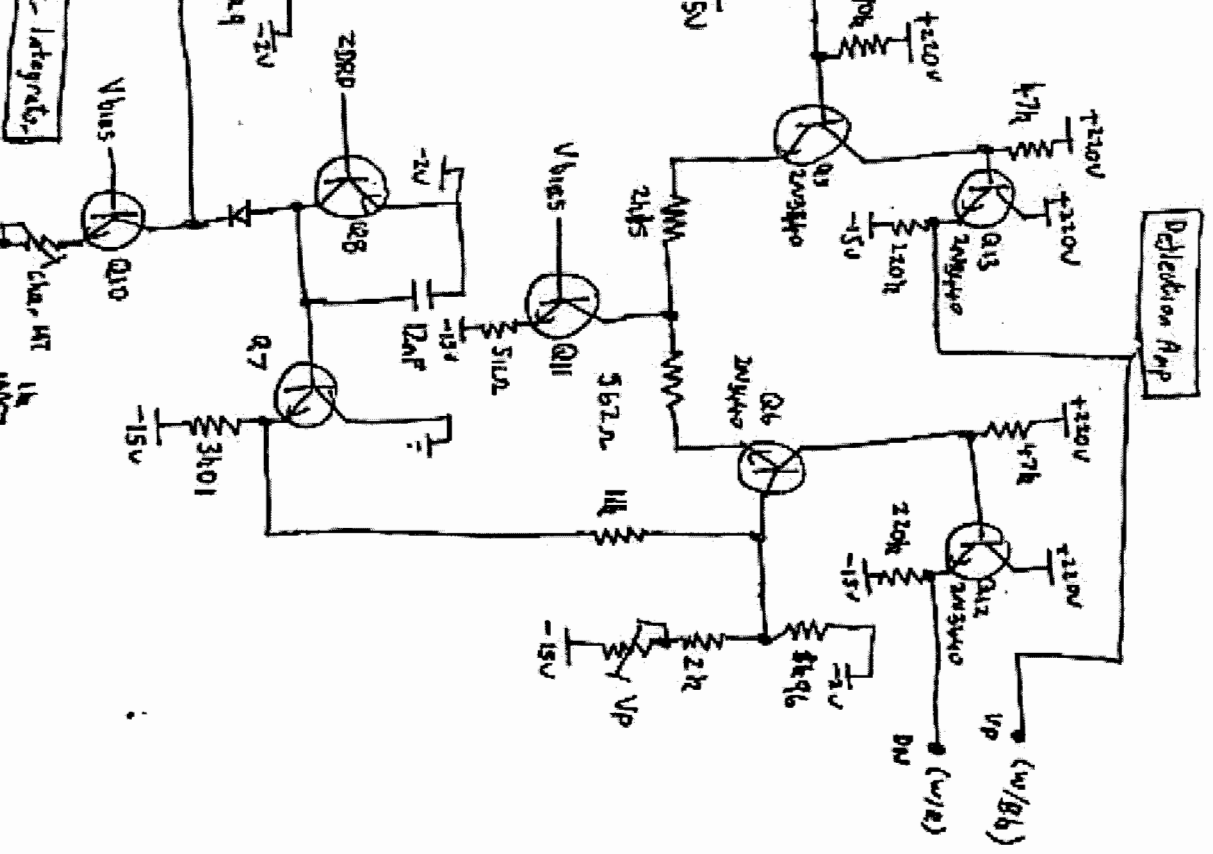
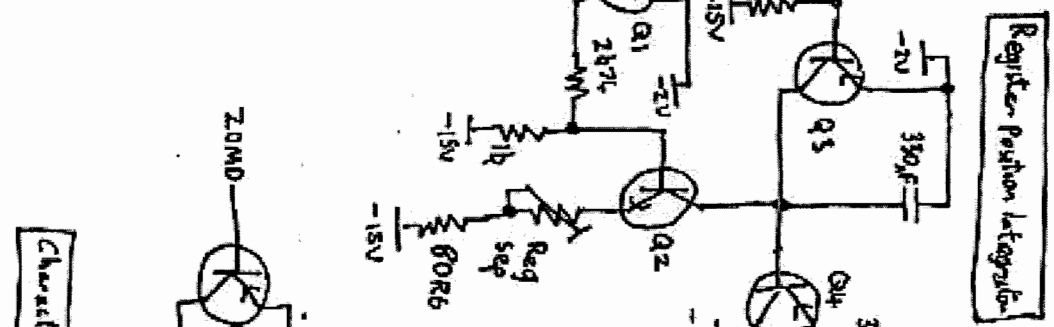
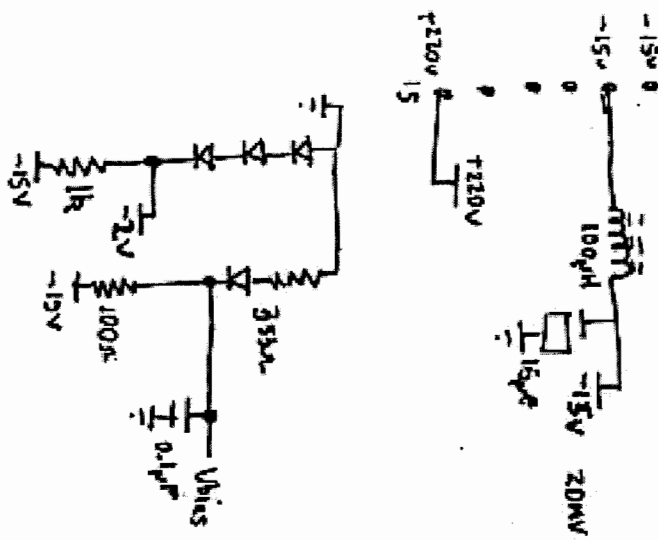




:

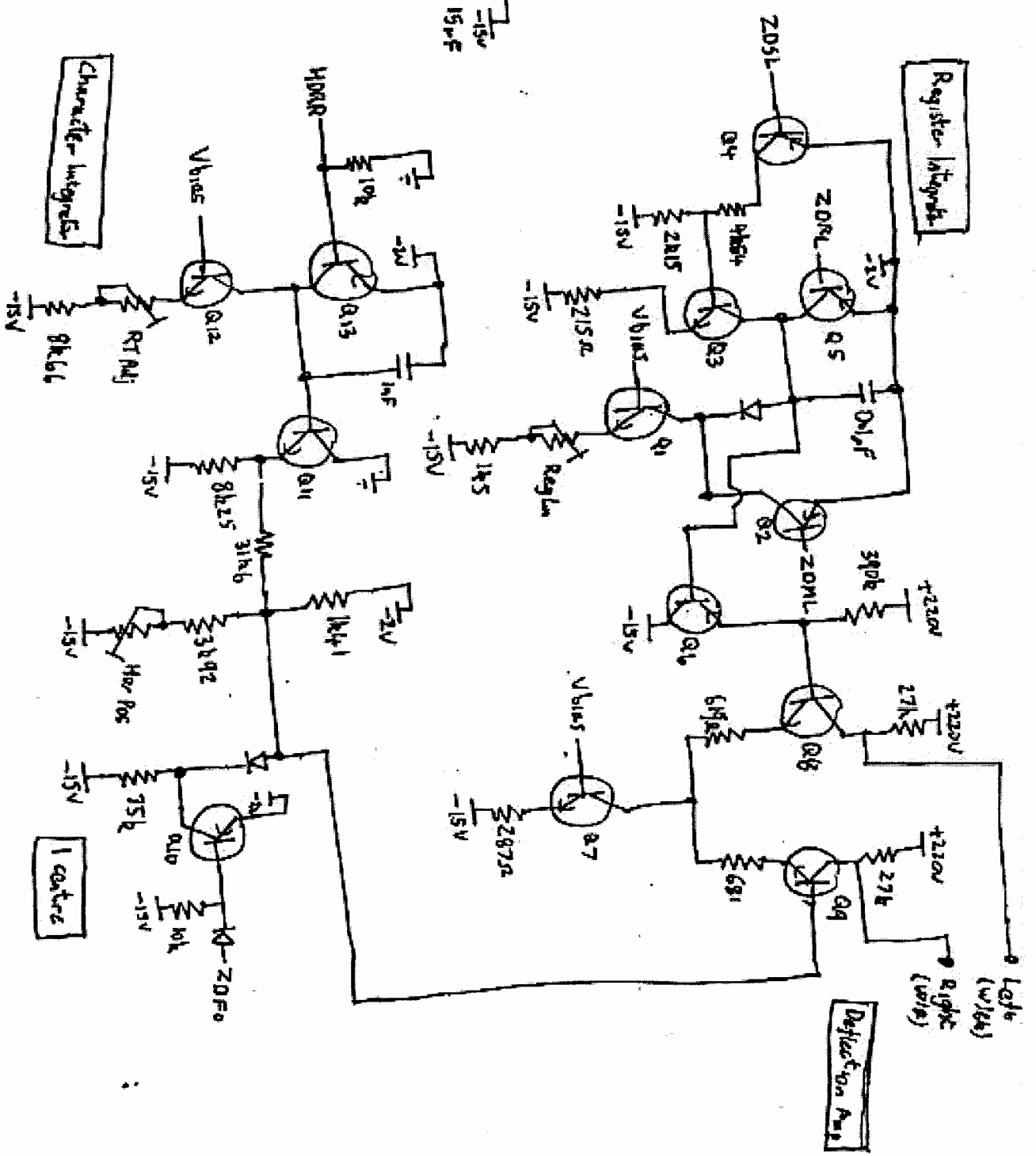
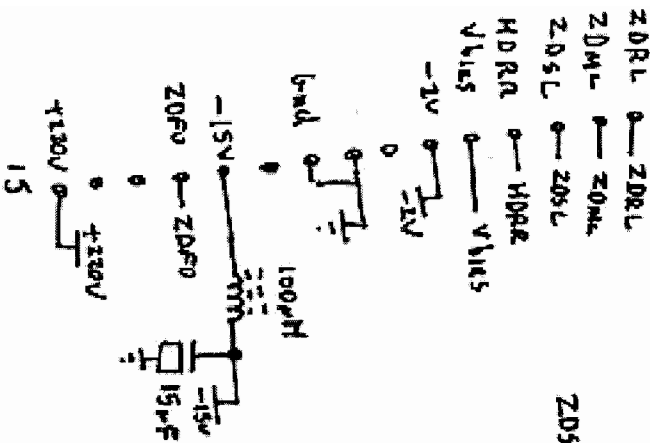


$Z_{DRV} \rightarrow 20kV$
 $Z_{DAV} \rightarrow 20M$
 $Z_{DAD} \rightarrow 20M$
 $Z_{DRD} \rightarrow 20k$
 $V_{bias} \rightarrow -2V$
 $V_{bias} \rightarrow -15V$

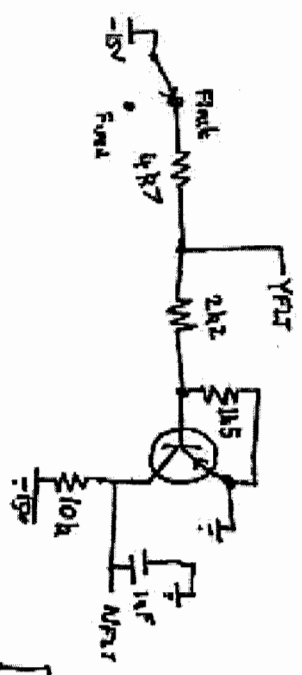
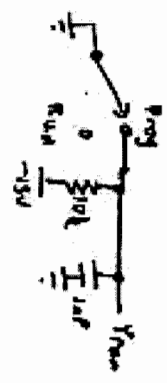
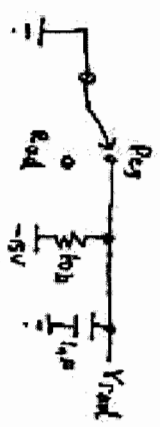


Characteristics Integrator

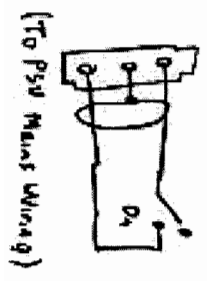
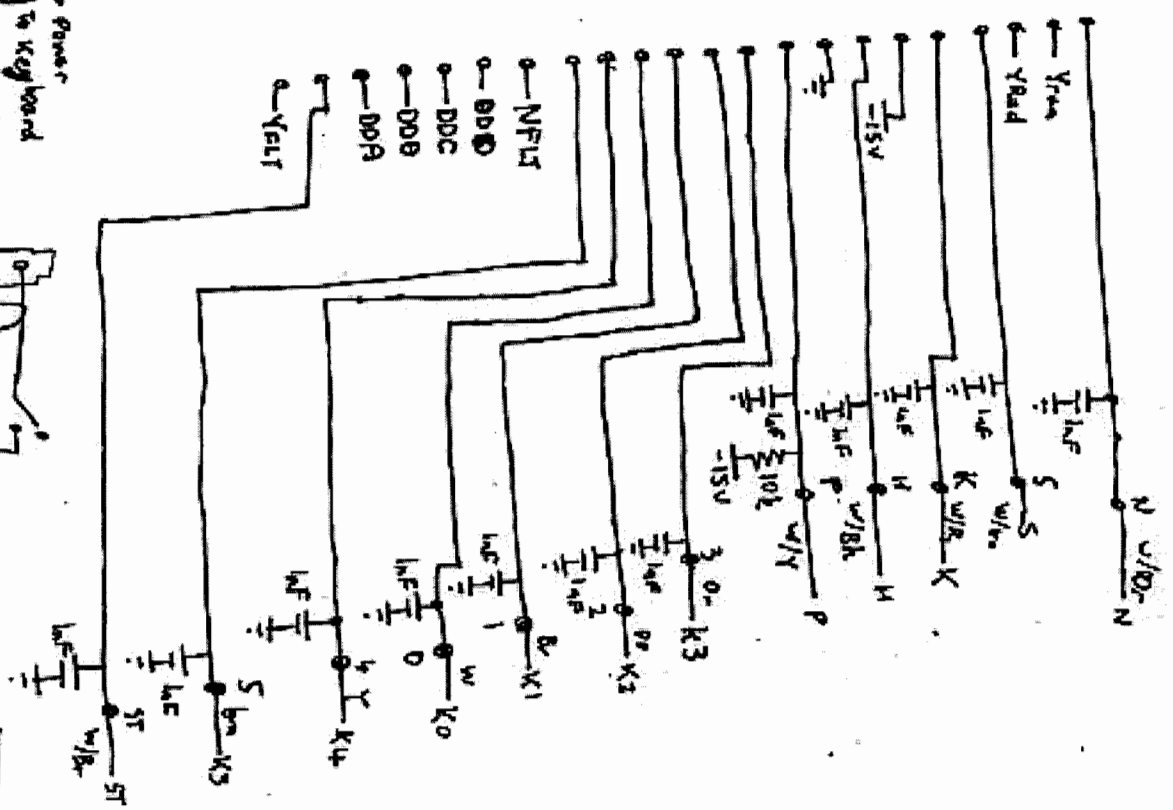
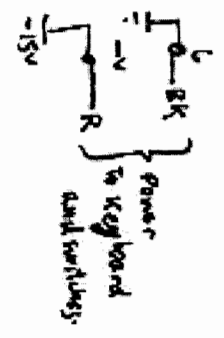
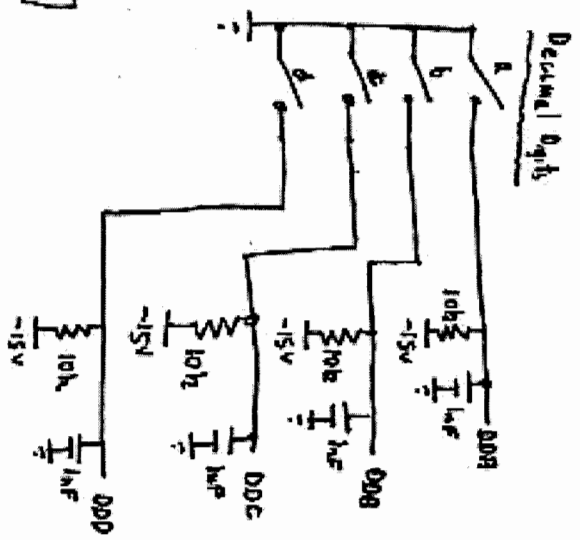
MP9100 Vertical Deflection PCB 01100-66516



HP9100 Horizontal Deflection PCB 09100-66515



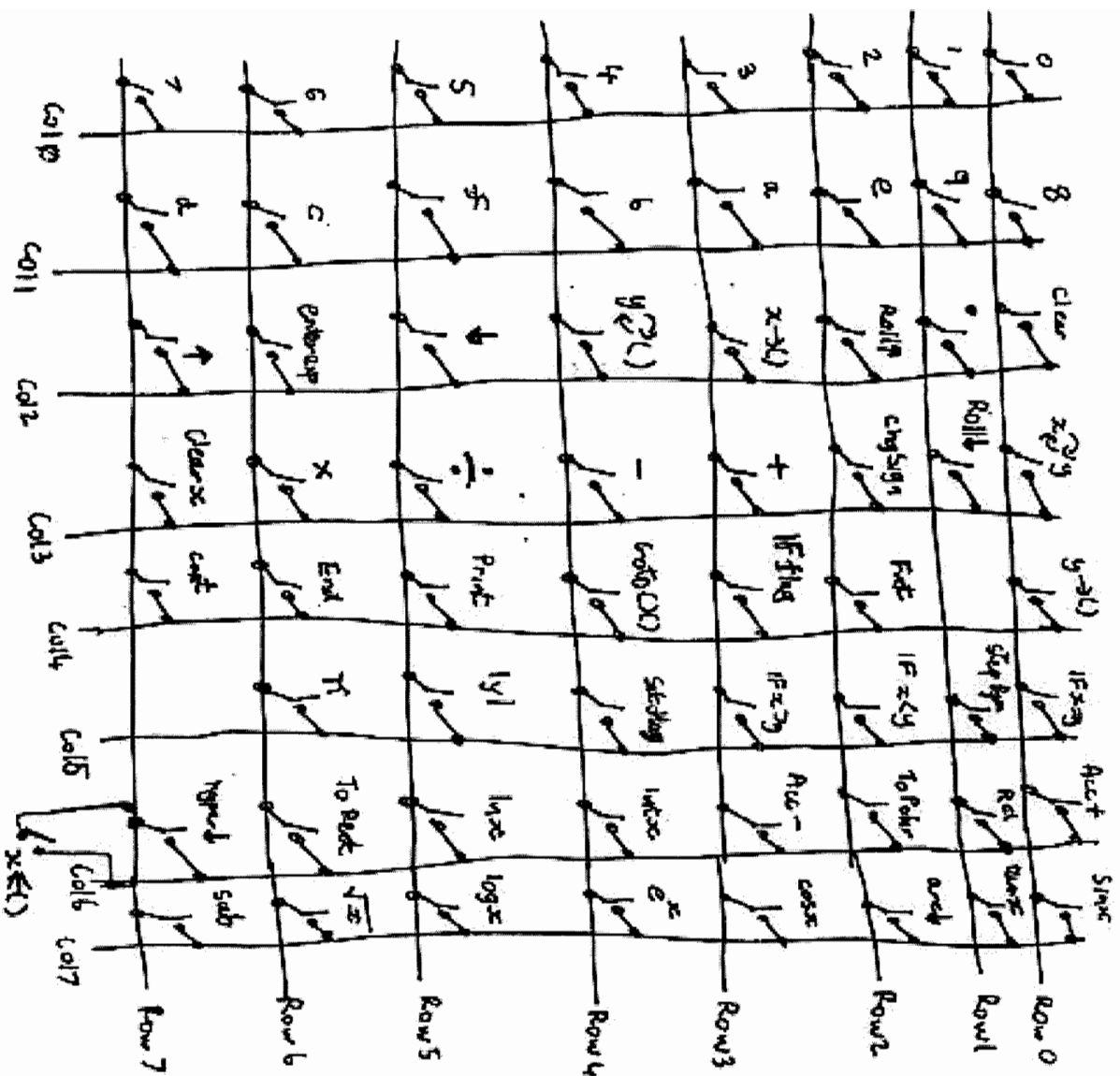
- Contacts Closed
- 0 a b d
 - 1 b a
 - 2 a c
 - 3 c
 - 4 a
 - 5 a b c
 - 6 a b c
 - 7 b c
 - 8 a b
 - 9 b



HP 9100 B Keyboard Sheet 1

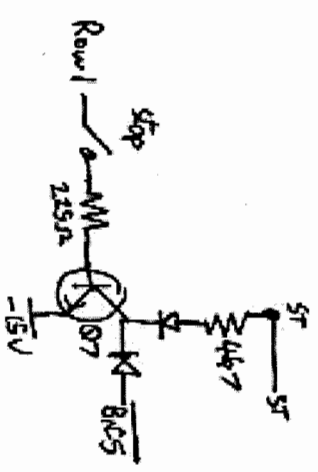
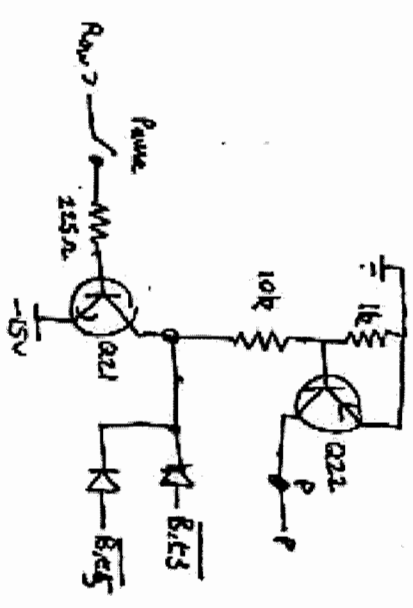
Connector PCB

(To PSU Mains Wiring)



Key Matrix

HP9100B Keyboard Sheet ③ 09100-66581



Pause, Stop Keys

T^0 Polar	191	Arcl
62	55	72
T^0 Rot	Int x	hyperd
66	64	67
RCL	e^x	$\sin x$
61	74	70
Acc -	$\ln x$	$\cos x$
63	65	73
Acc +	$\log x$	$\tan x$
60	75	71

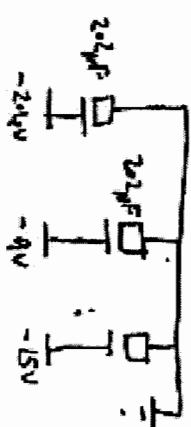
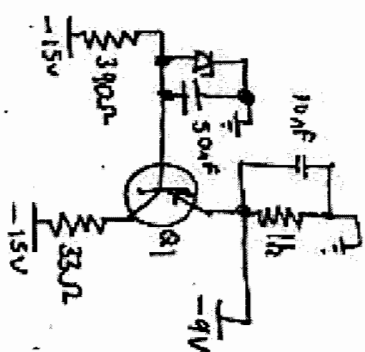
a	b	$x \rightarrow y$
13	14	30
c	d	Roll
16	17	31
e	f	Roll
12	15	22
$y \rightarrow x$	$y \rightarrow x$	$\cdot \phi$
40	24	25
$x \rightarrow y$	$x \leftarrow y$	$\uparrow$
23	67	27

HPA100 B keycaps ①

$\sqrt{x}$	Chg Syn	Enter E_p	Clear x
76	32	26	37
$\div$	7	8	9
35	07	10	11
$\times$	4	6	8
36	04	05	08
-	1	2	3
34	01	02	03
+	0	0	0
33	00	21	56

Clear	IF Flag	Set flag
20	43	54
Fmt	IF $x \leq y$	Round
42	52	57
Print	IF $x = y$	stop
45	50	41
sub	IF $x > y$	End
77	53	46
cont	proto()	stop Pgm
47	44	51

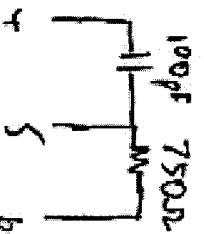
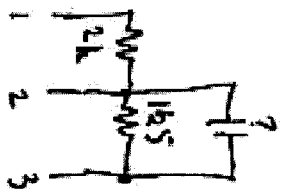
MP91008 keypad ②

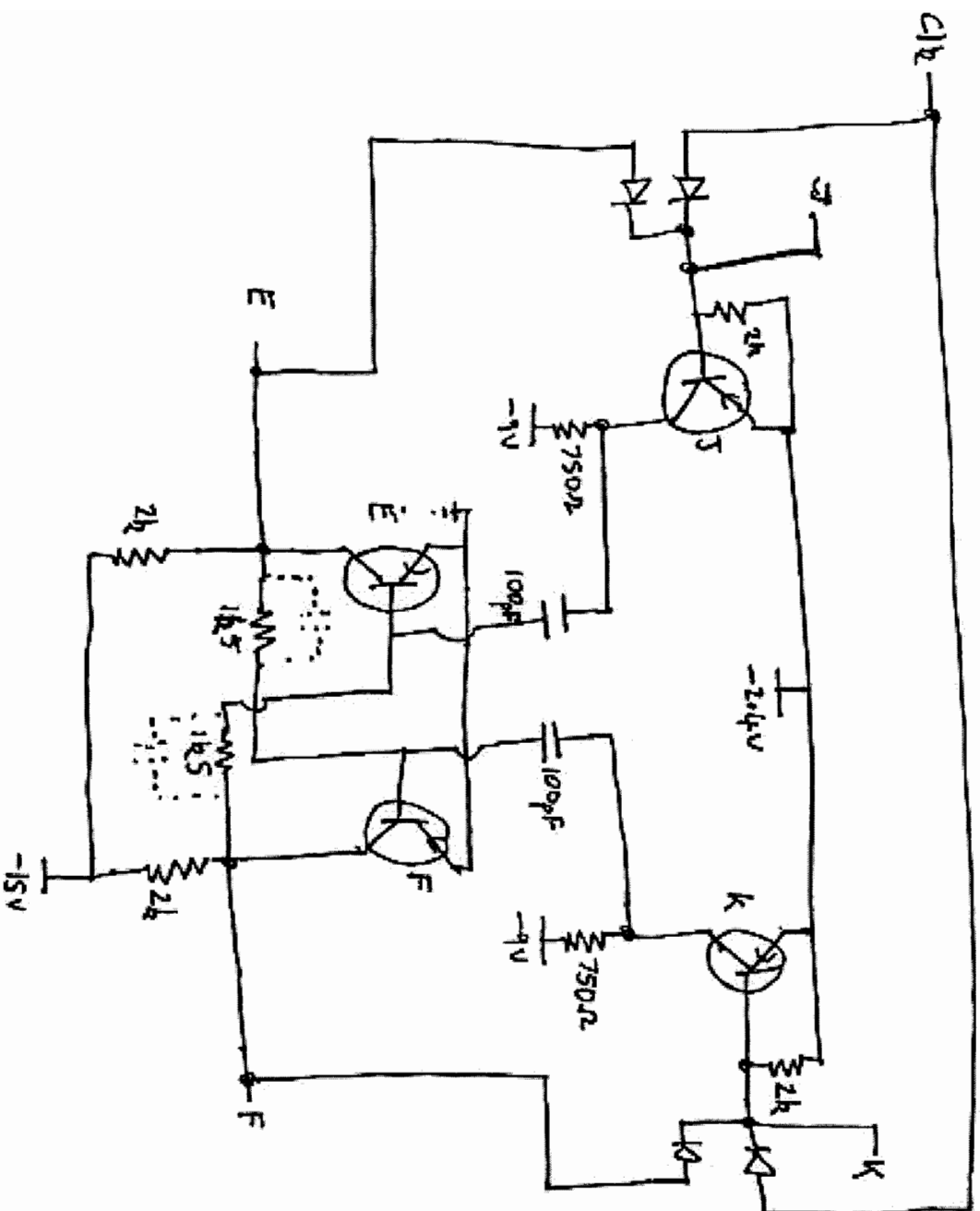


①

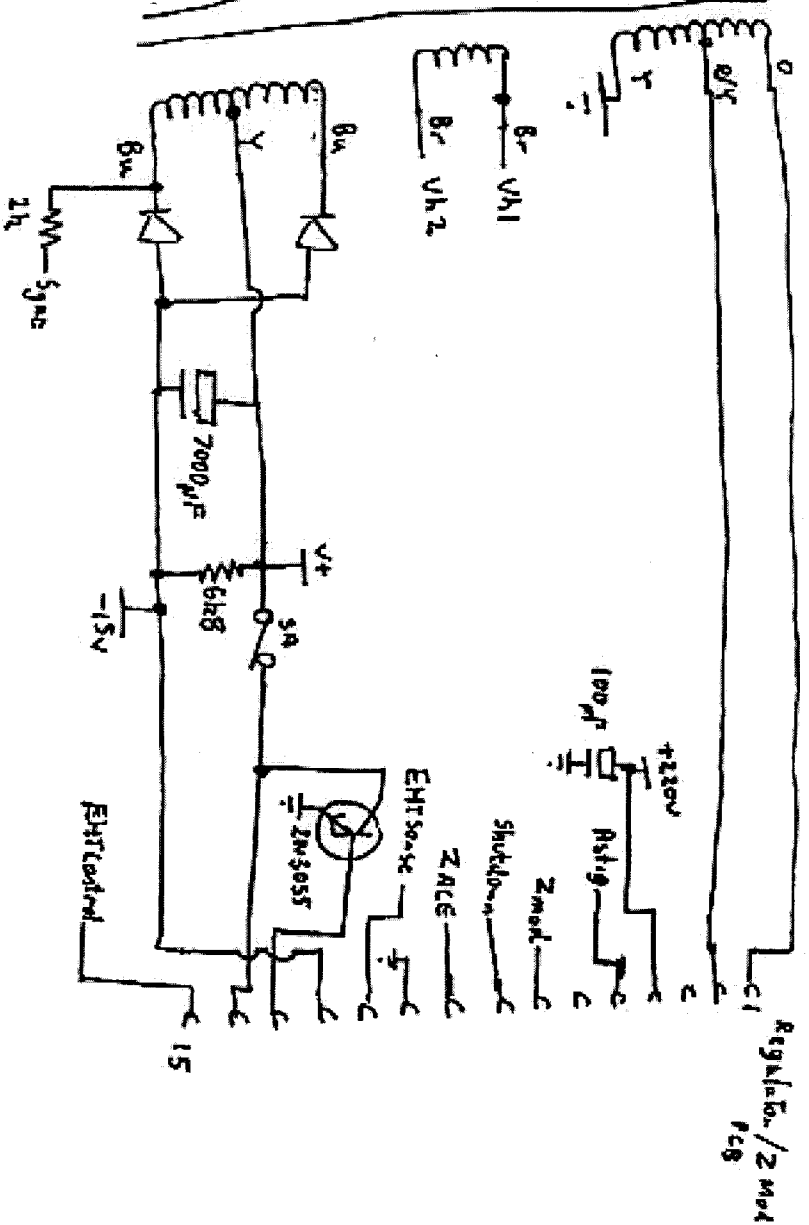
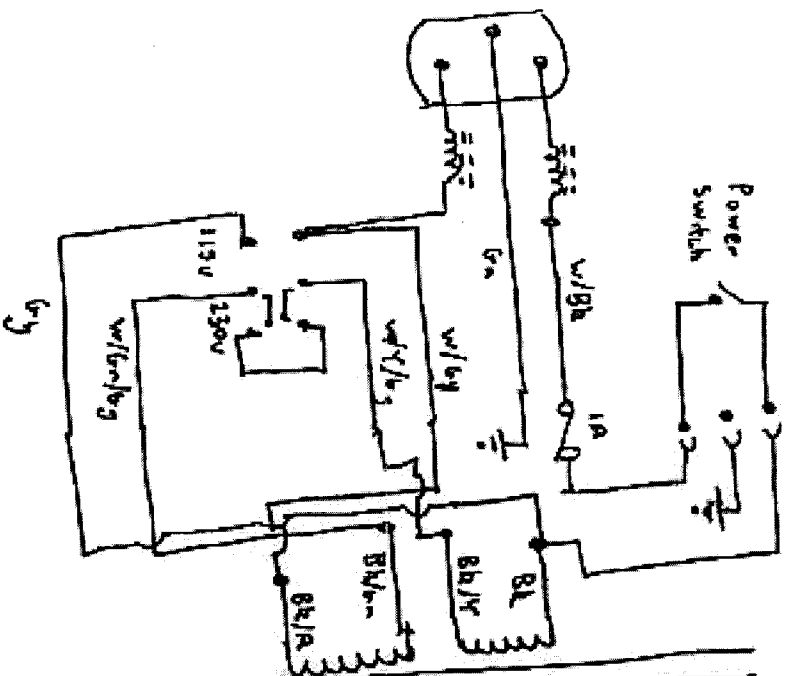
1410-0021
Hybrid

	1	2	3	4	5	6	7	8
1	-	2k	3.5k	81	81	81	81	81
2	2k	-	1.5k	81	81	81	81	81
3	3.5k	1.5k	-	81	81	81	81	81
4	∞	∞	∞	750Ω 81	750Ω	750Ω	81	81
5	∞	∞	∞	750Ω	750Ω	81	81	81
6	∞	∞	∞	∞	∞	81	-	2k
7	∞	∞	∞	∞	∞	∞	2k	-
8	∞	∞	∞	∞	∞	∞	-	-





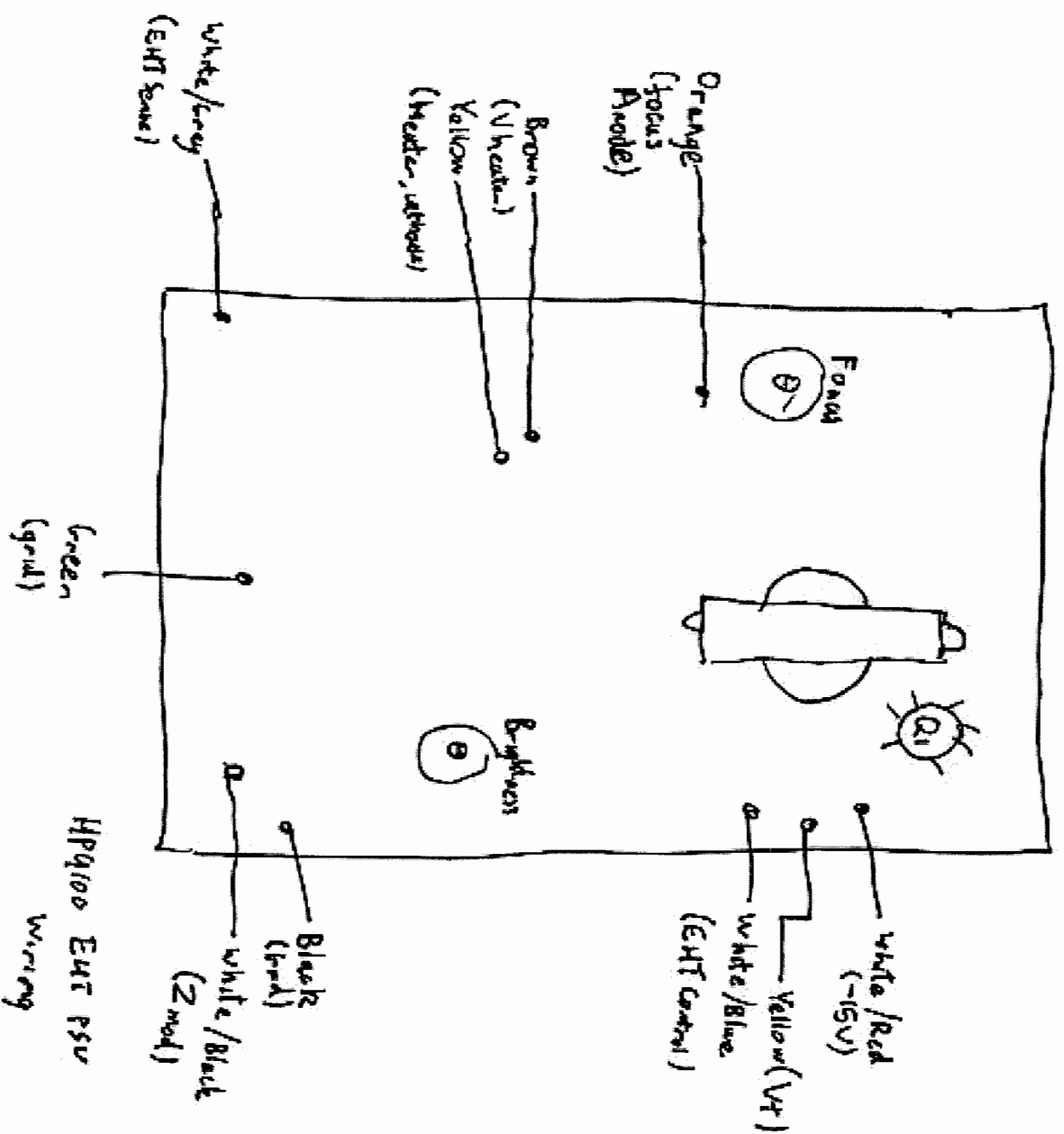
HP9100 Flip-Flop



LV PSU

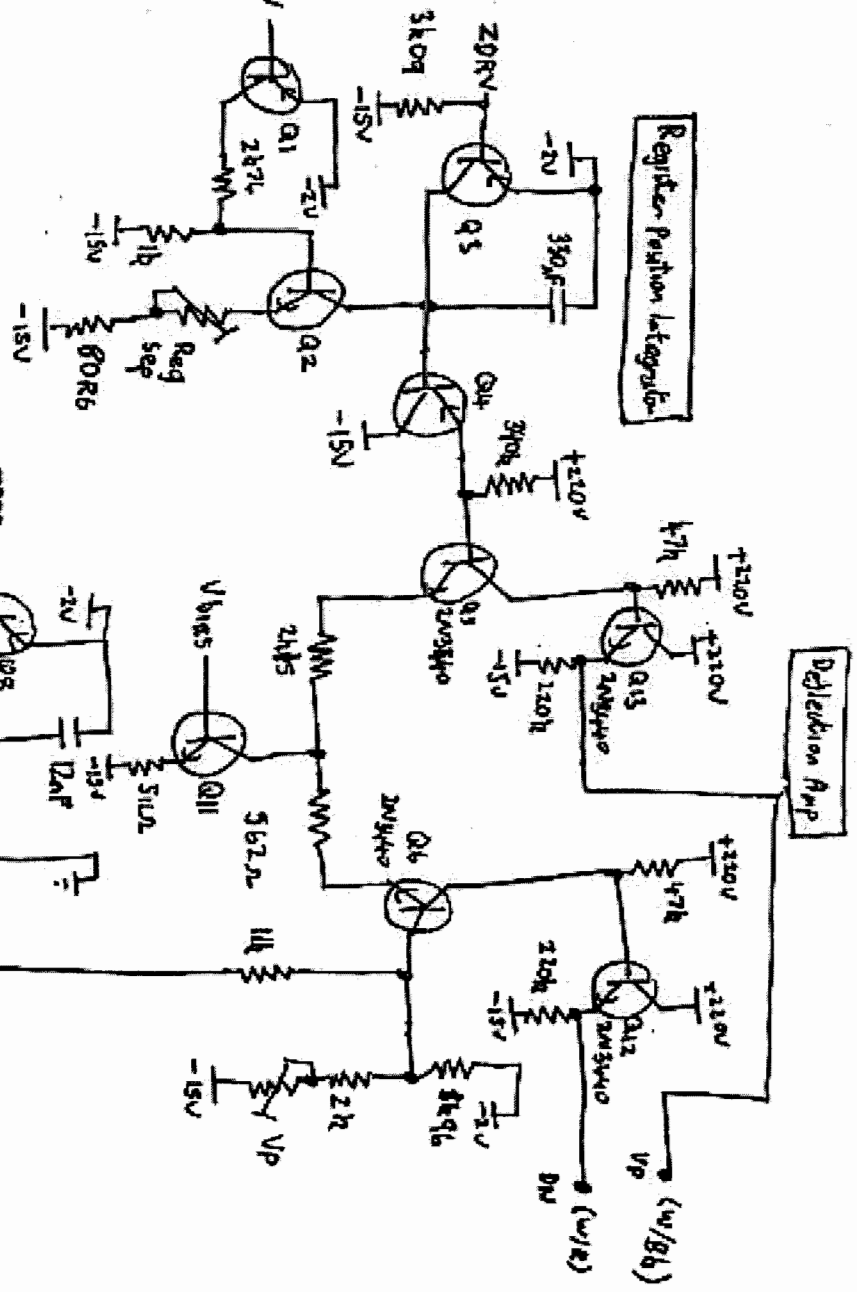
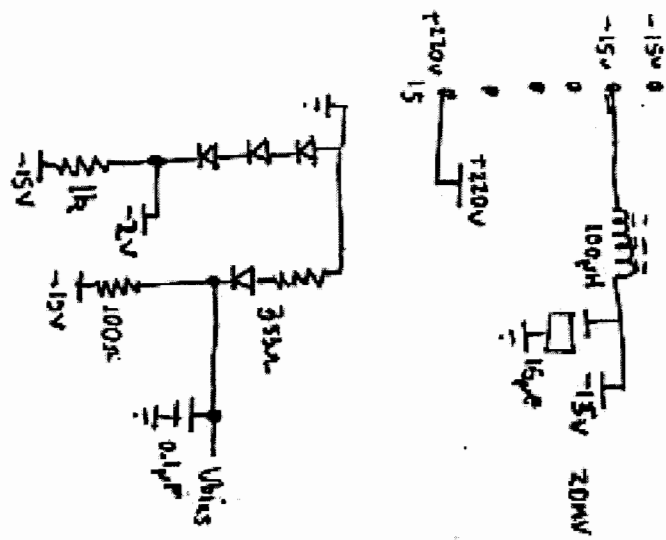
HP9100 PSU sheet ①







$Z_{ORV} \rightarrow Z_{ORV}$
 $Z_{OMV} \rightarrow Z_{OMV}$
 $Z_{DAD} \rightarrow Z_{DAD}$
 $Z_{ORD} \rightarrow Z_{ORD}$
 $V_{bias} \rightarrow V_{bias}$
 $-2V \rightarrow -2V$
 $-15V \rightarrow -15V$
 $6M \rightarrow 6M$
 $100\mu H \rightarrow 100\mu H$
 $15 \rightarrow 15$



Register Position Integrator

Deflection Amp

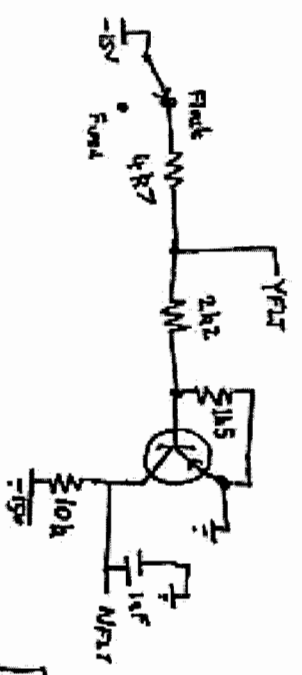
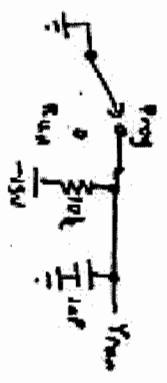
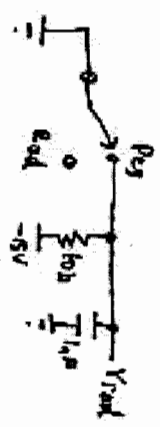
Characteristic Integrator

MP9100 Vertical Deflection PCB 09100-6651C

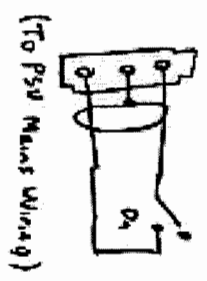
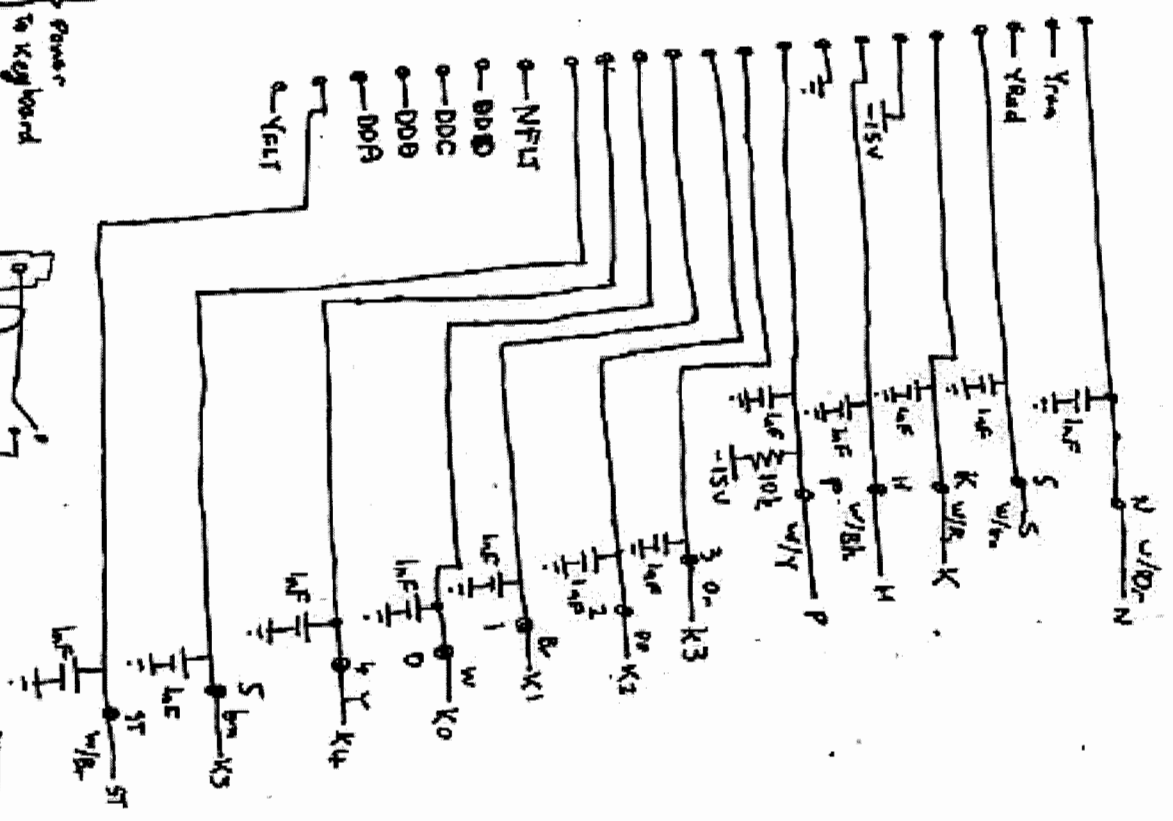
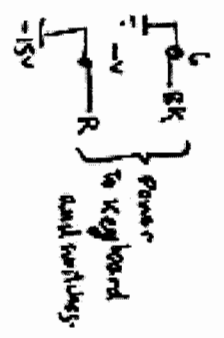


1 centre

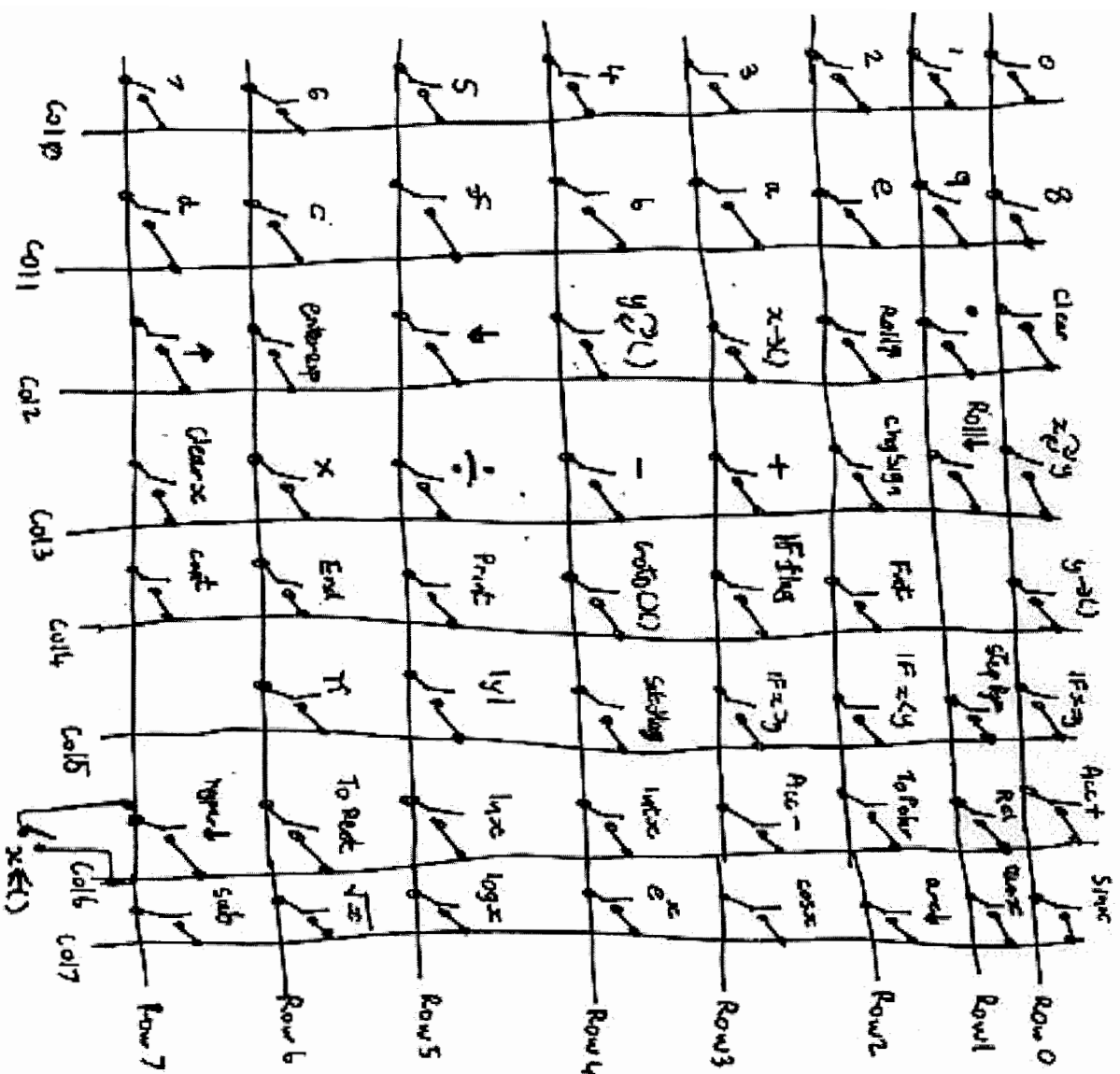
HP9100 Marzetti Deflection PCB 09100-66515



- Contacts Closed
- 0 a b d
 - 1 b d
 - 2 a c
 - 3 c
 - 4 a
 - 5 a a c
 - 6 a b c
 - 7 b c
 - 8 a b
 - 9 b



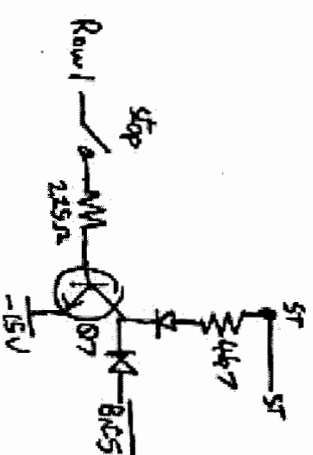
HP 9100 B Keyboard Sket ①



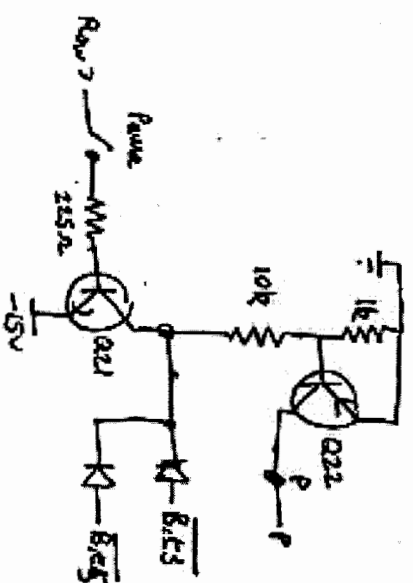
Key Matrix

MP41008 Keyboard Sheet 3

09100-66581



Pause, Stop Keys



T_0 Polur	191	Arcl
62	55	72
T_0 Aut	Int x	hyperf
66	64	67
RCL	e^x	$\sin x$
61	74	70
Acc -	$\ln x$	$\cos x$
63	65	73
Acc +	$\log x$	$\tan x$
60	75	71

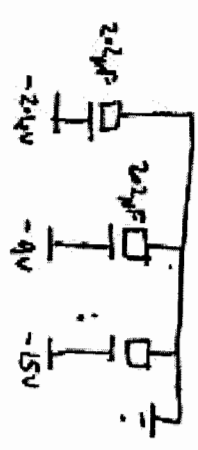
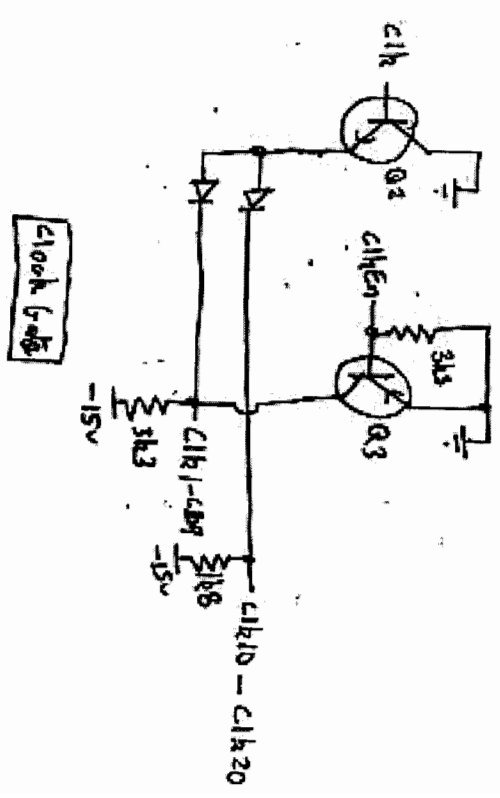
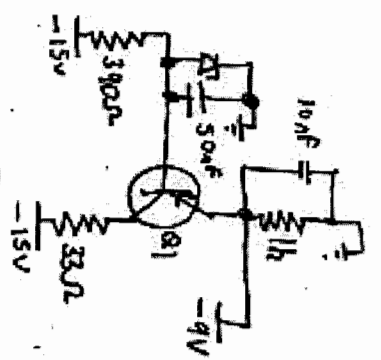
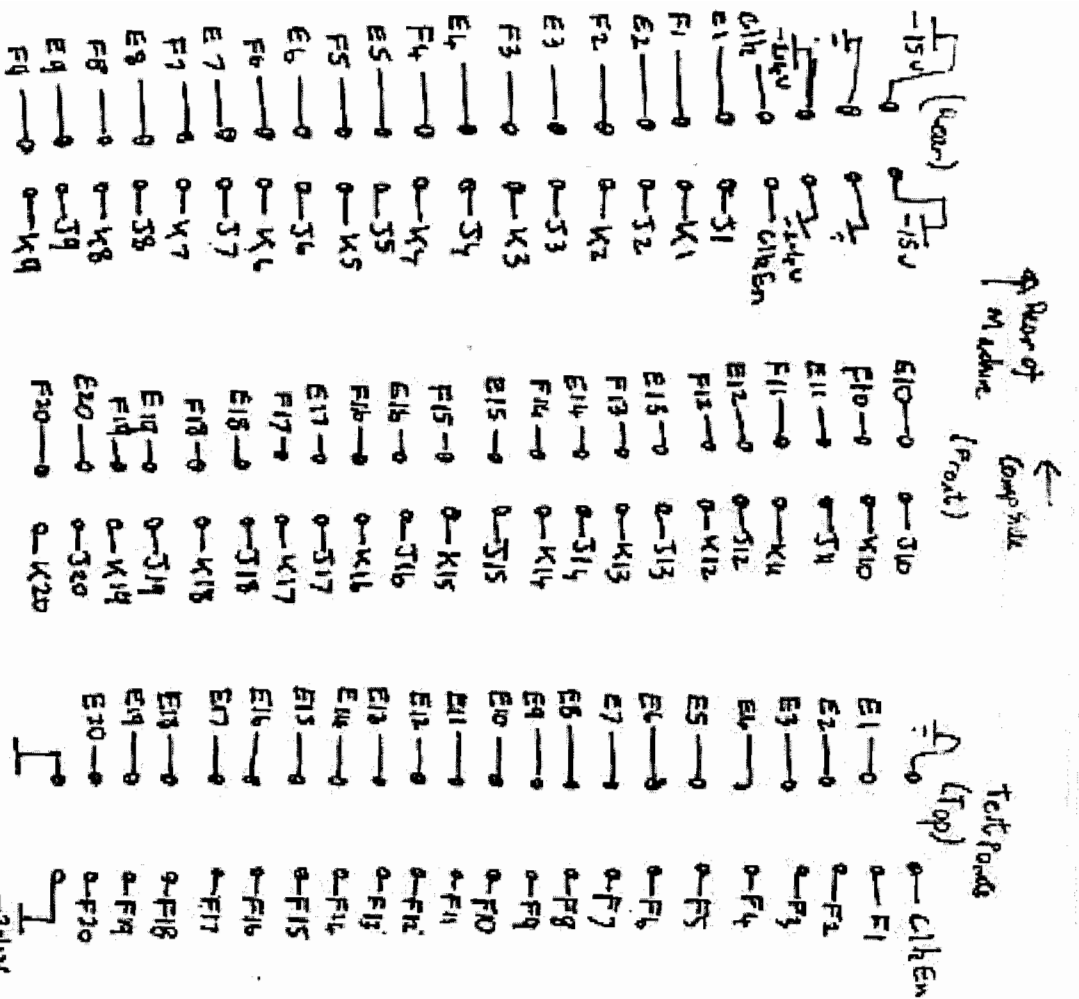
a	b	x^2y
13	14	30
c	d	Roll
16	17	31
e	f	Roll
12	15	22
$y-x()$	$y^2()$	$\cdot \phi$
40	24	25
$x-y()$	$x-y()$	$\uparrow$
23	67	27

HPa100 B Keycaps ①

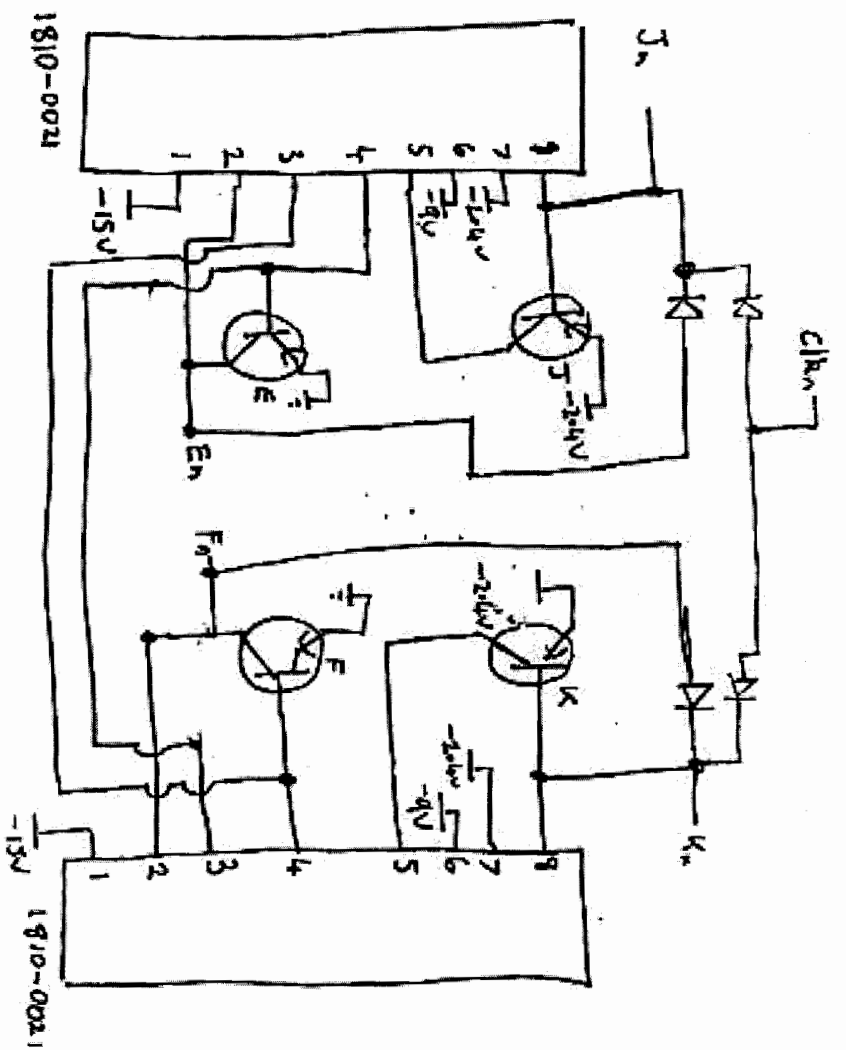
$\sqrt{x}$	Comp Syn	Enter E_p	Clear x
7 6	3 2	2 6	3 7
$\div$	7	8	9
3 5	0 7	1 0	1 1
$\times$	4	5	8
3 6	0 4	0 5	0 5
-	1	2	3
3 4	0 1	0 2	0 3
+	0	0	7 7
3 3	0 0	2 1	5 6

Clear	IF Flag	Set flag
2 0	4 3	5 4
Fmt	IF $x \leq y$	Power
4 2	5 2	5 7
Print	IF $x = y$	stop
4 5	5 0	4 1
sub	IF $x > y$	End
7 7	5 3	4 6
cont	format()	stop flag
4 7	4 4	5 1

HP 9100B keymap ②



HP9100 Flip-Flop PCB sheet ① 09100-66502

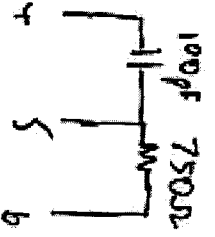
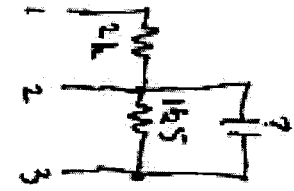


flip-flop (1 of 20)

HP9100 Flip-Flop PCB Sheet 2 09100-66502

1410-0021
Hybrid

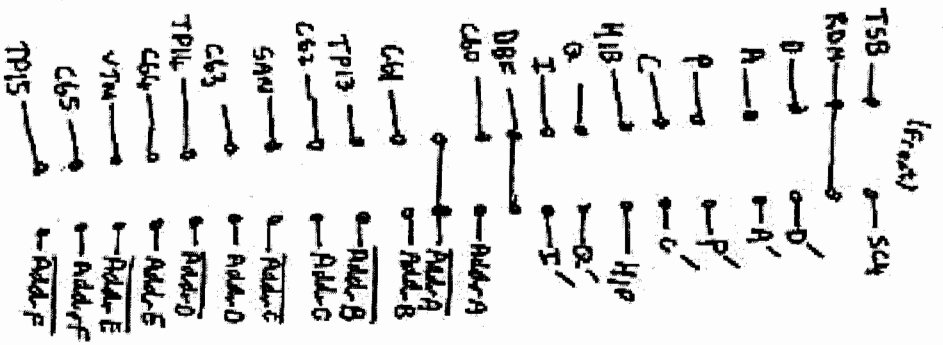
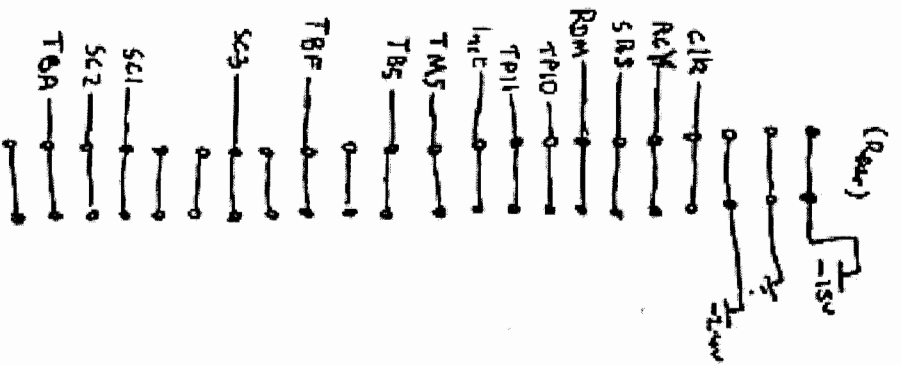
	1	2	3	4	5	6	7	8
1	-	2h	3.5h	2h	2h	2h	2h	2h
2	2h	-	1.5h	2h	2h	2h	2h	2h
3	3.5h	1.5h	-	2h	2h	2h	2h	2h
4	2h	2h	2h	-	2h	2h	2h	2h
5	2h	2h	2h	2h	-	2h	2h	2h
6	2h	2h	2h	2h	2h	-	2h	2h
7	2h	2h	2h	2h	2h	2h	-	2h
8	2h	2h	2h	2h	2h	2h	2h	-



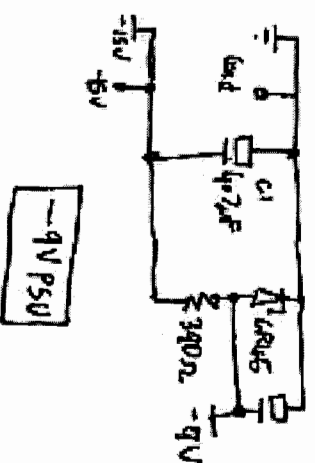
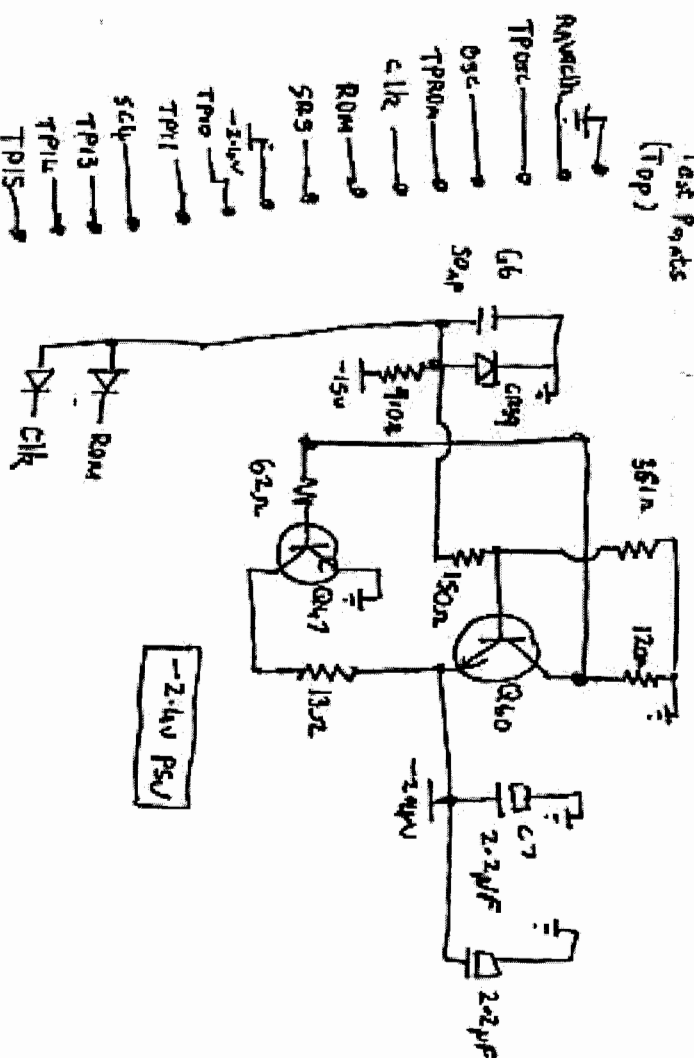


HP9100 Flip-Flip

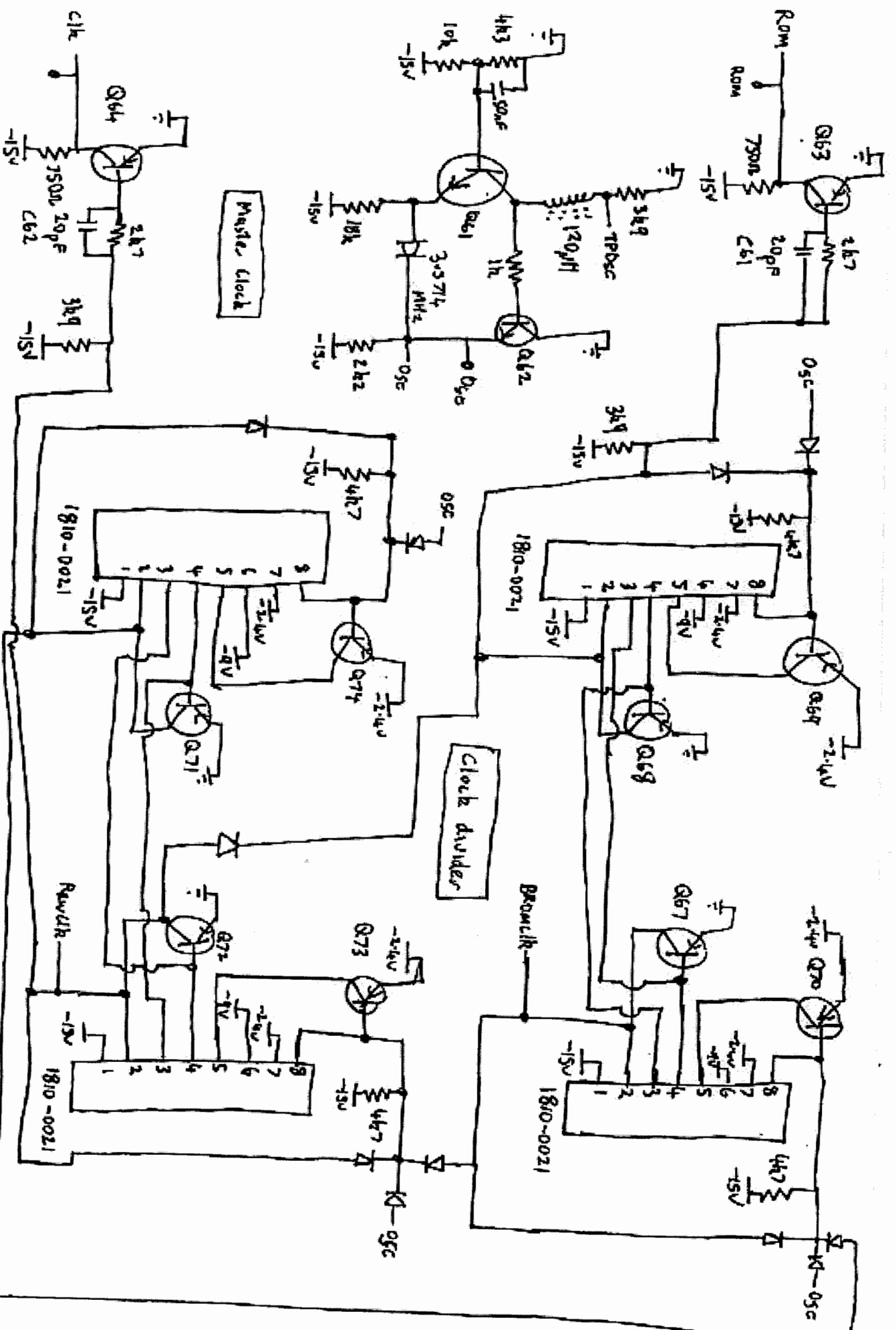
609 346

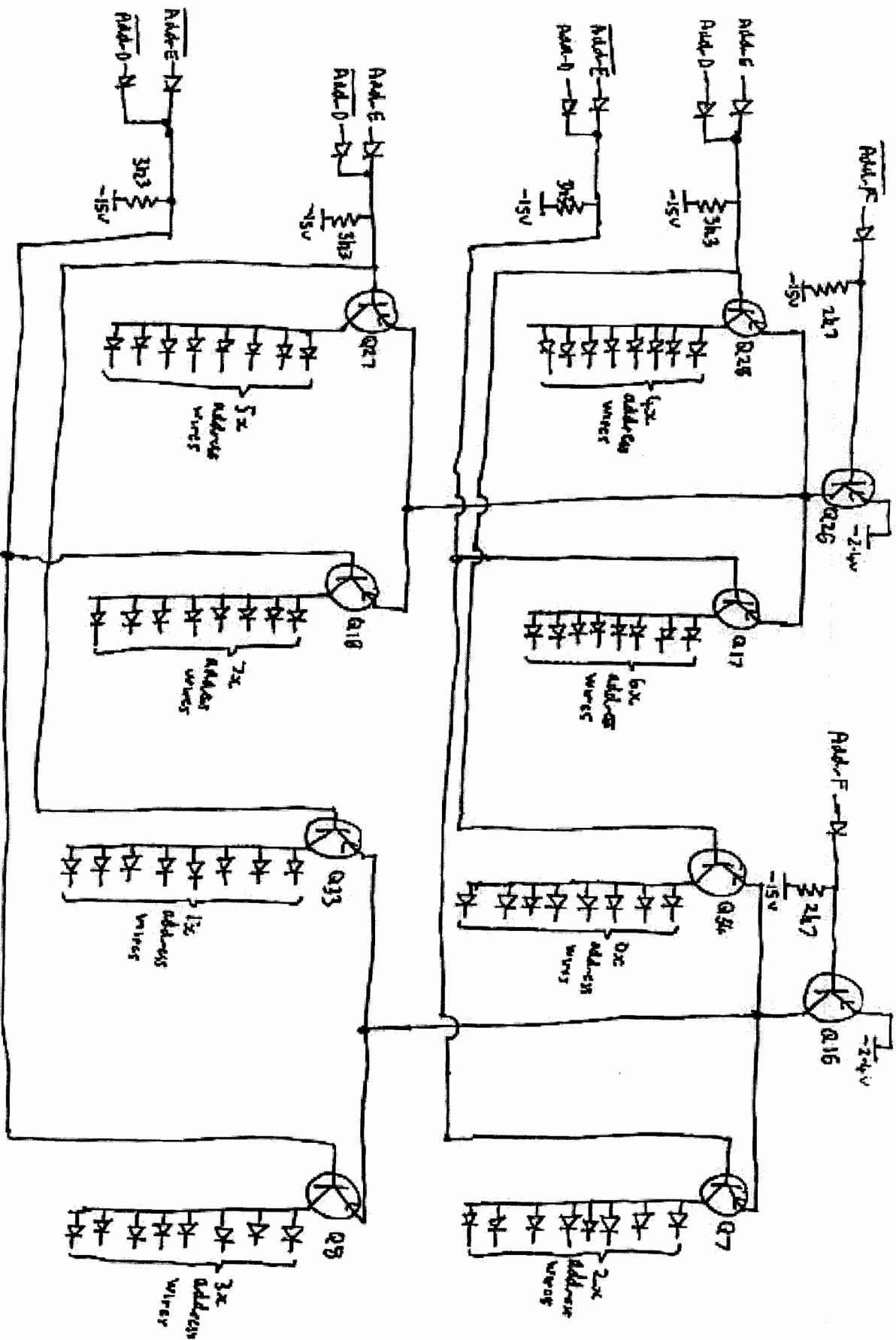


Test Ports
(Top)

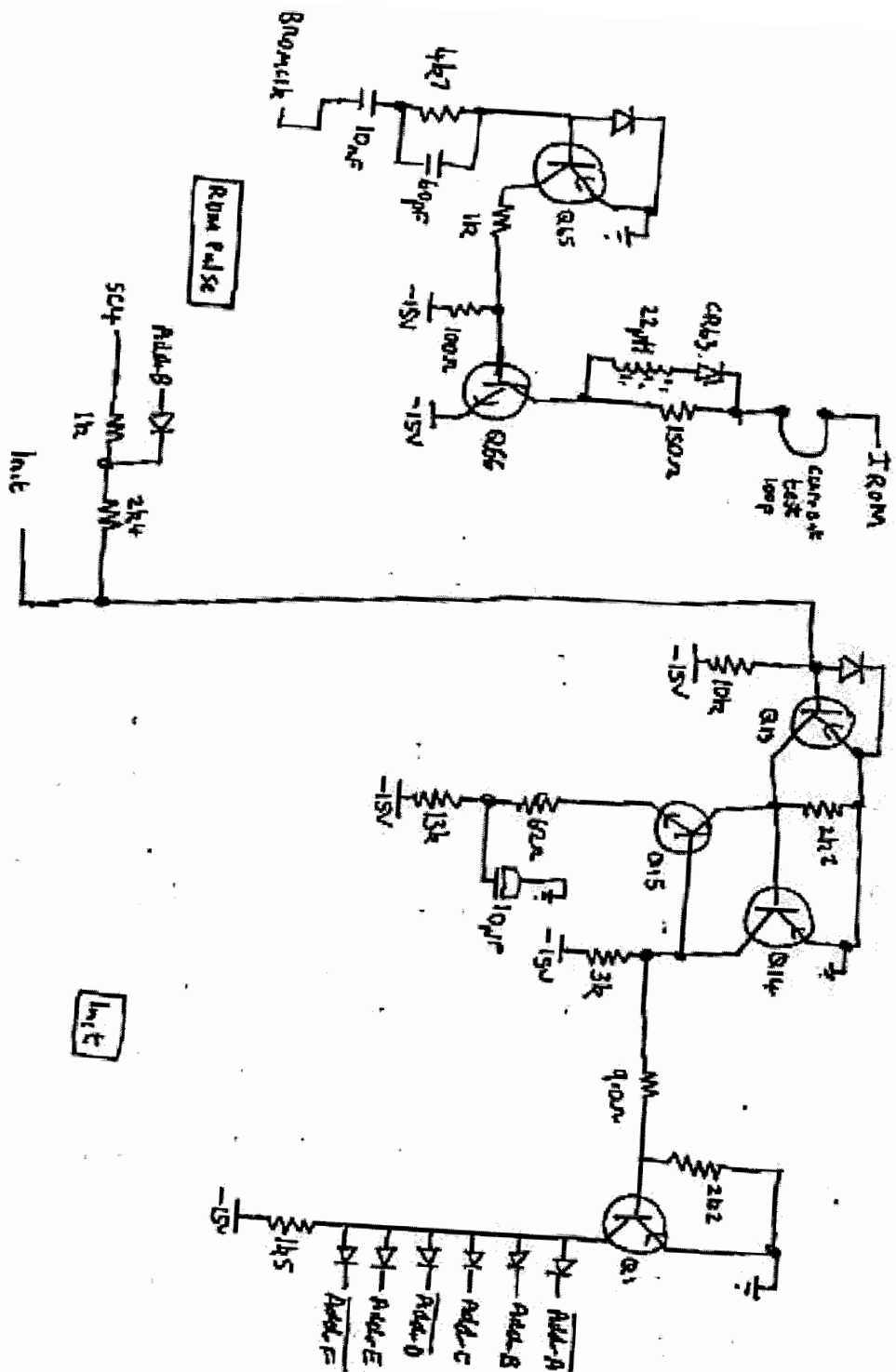


HP9100B Control Logic PCB
09100-6655

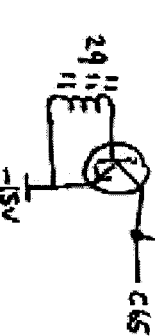
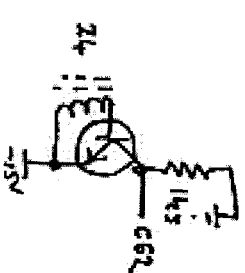
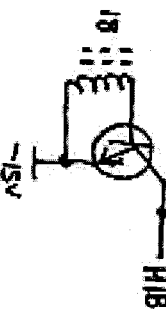
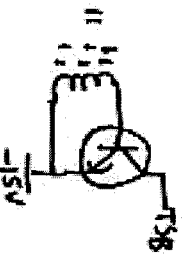
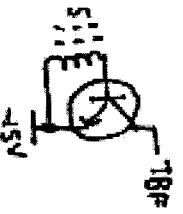
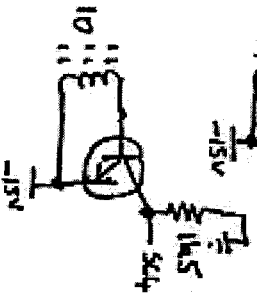
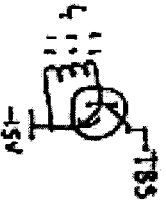
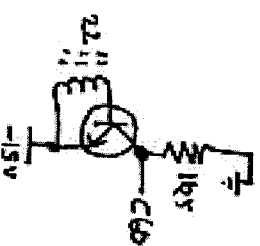
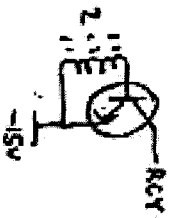
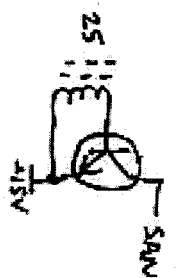
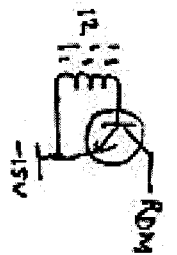
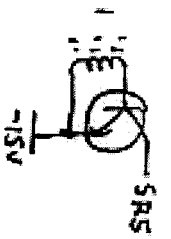




MP91000 Control Logic PCB Sheet ③ 09100-6551



HP 9100B Control Logic PCB Schematic 09100-66551

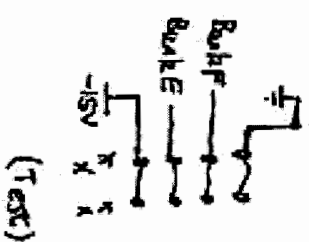
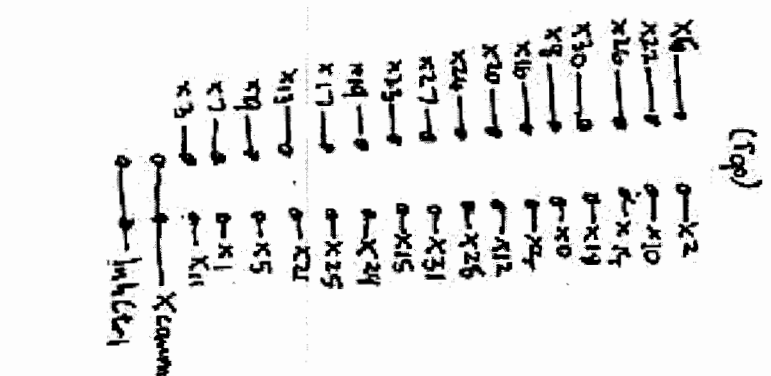
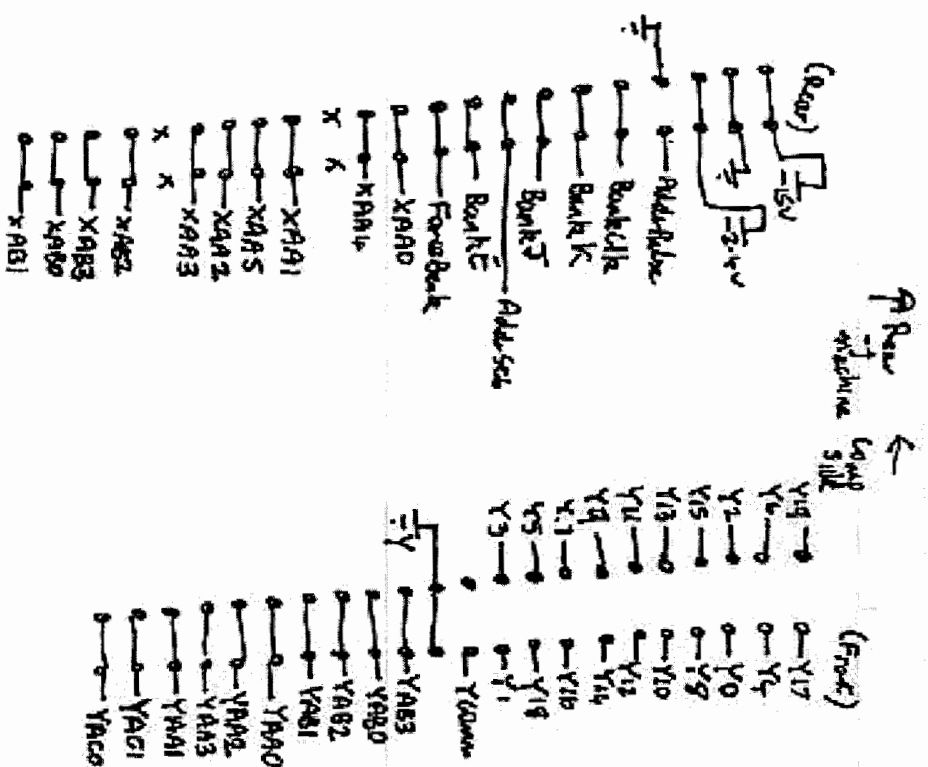


Core 1 at rear of machine.

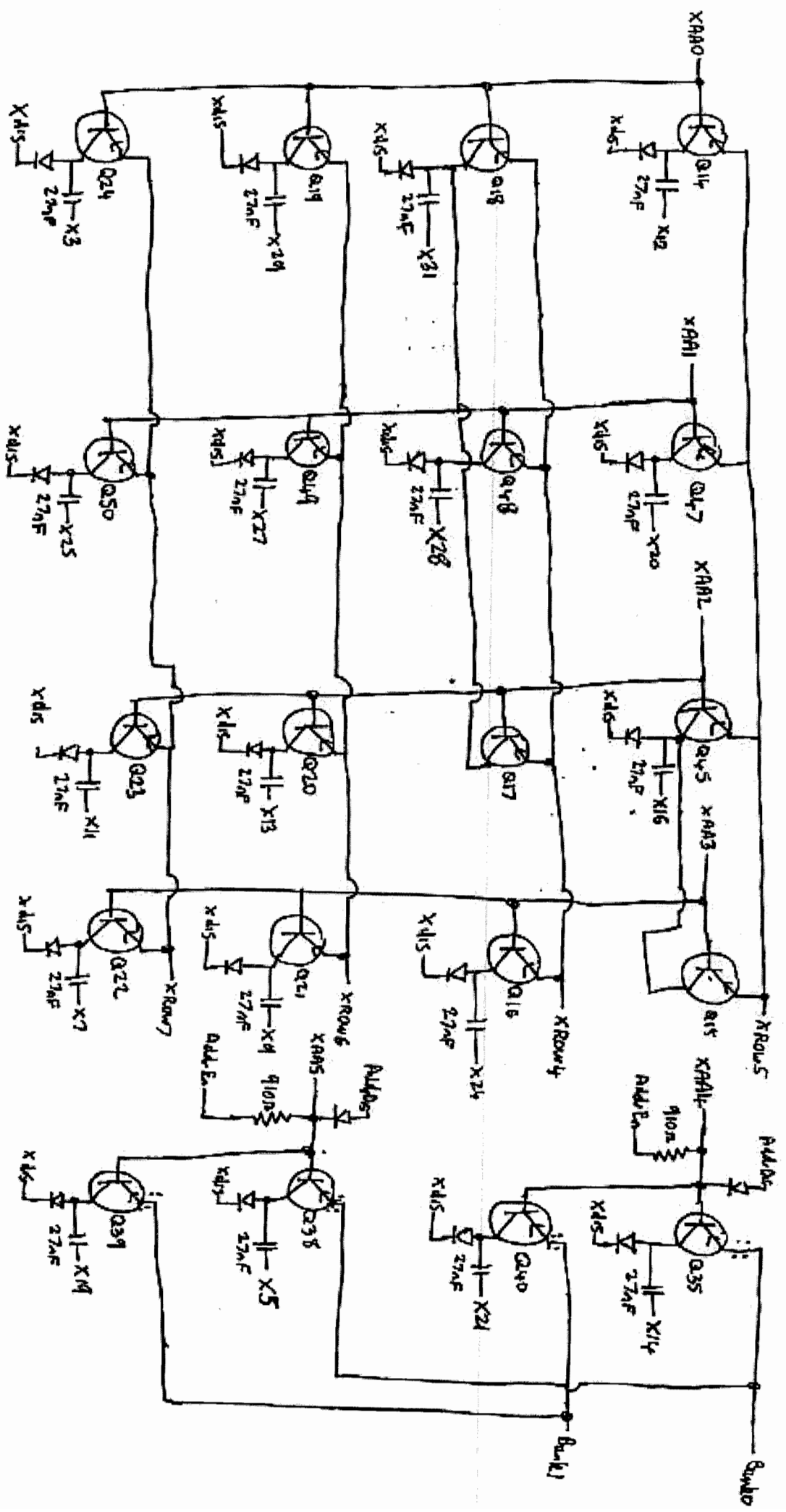
HP9100B Control Logic PCB Sheet 6

09100-66551

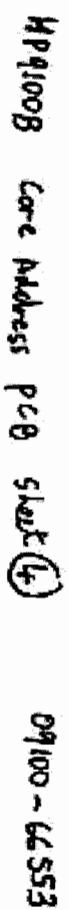
ROM P24



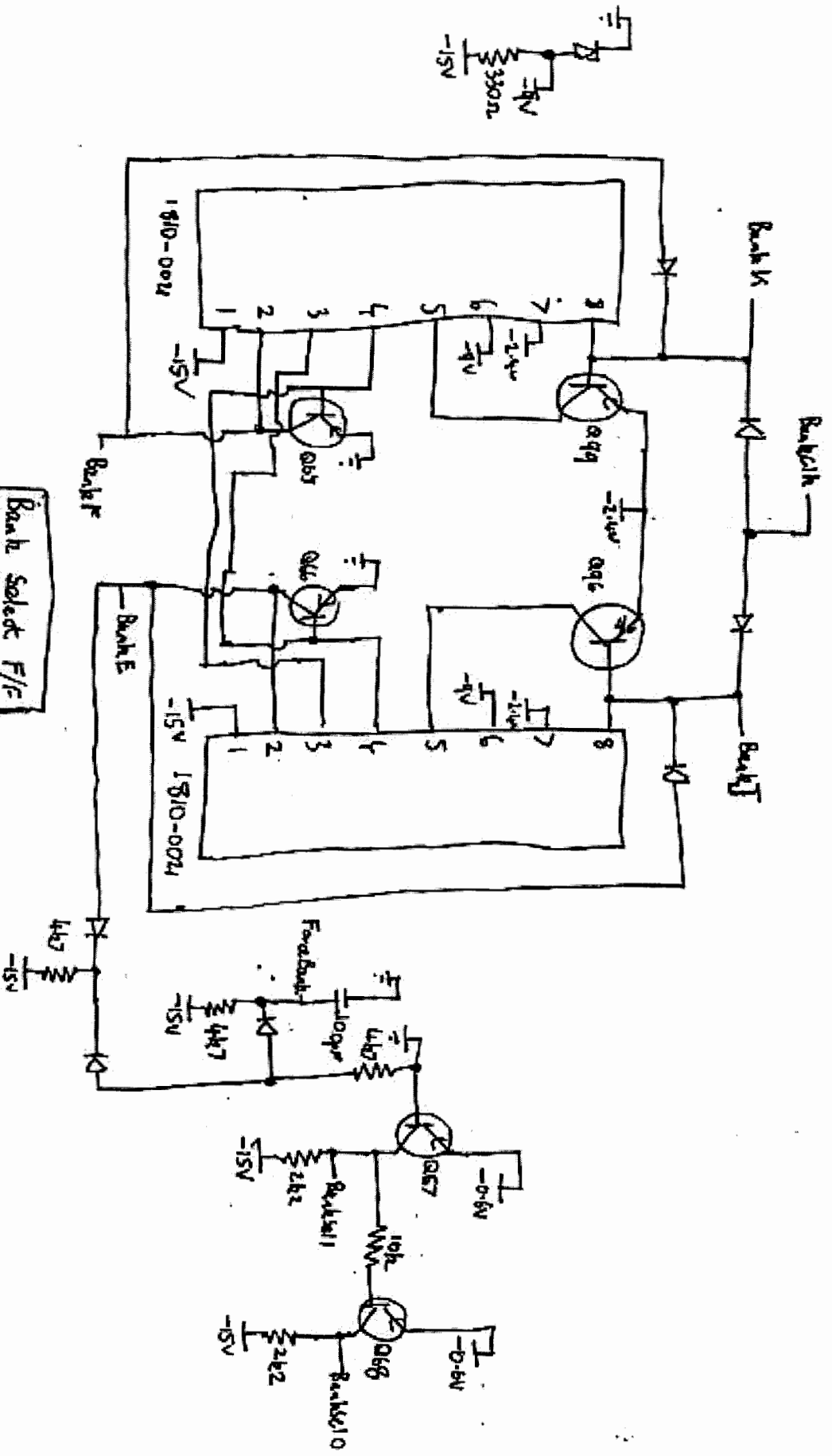
Connectors



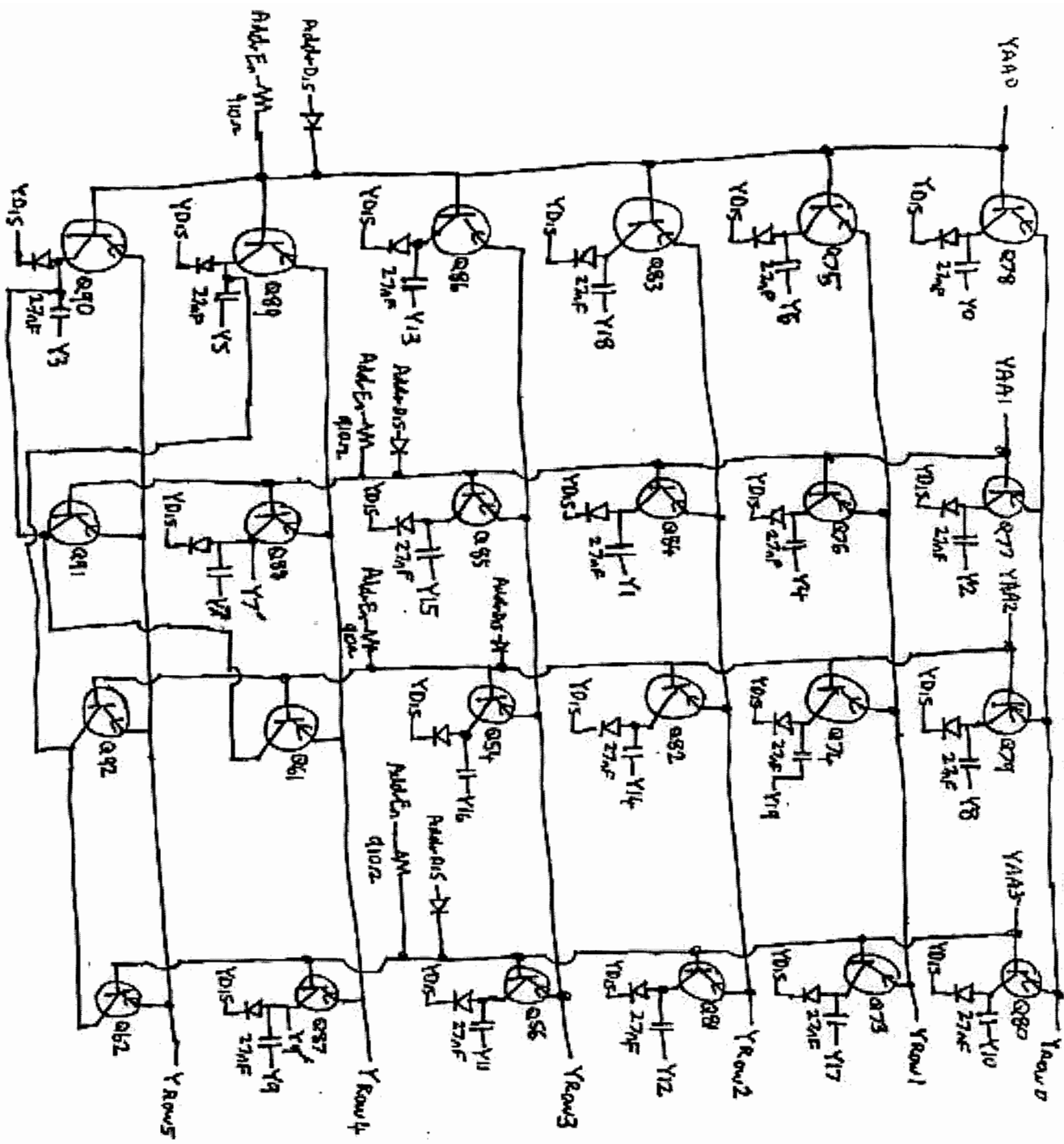
X Drivers 2



09100 - 66 553

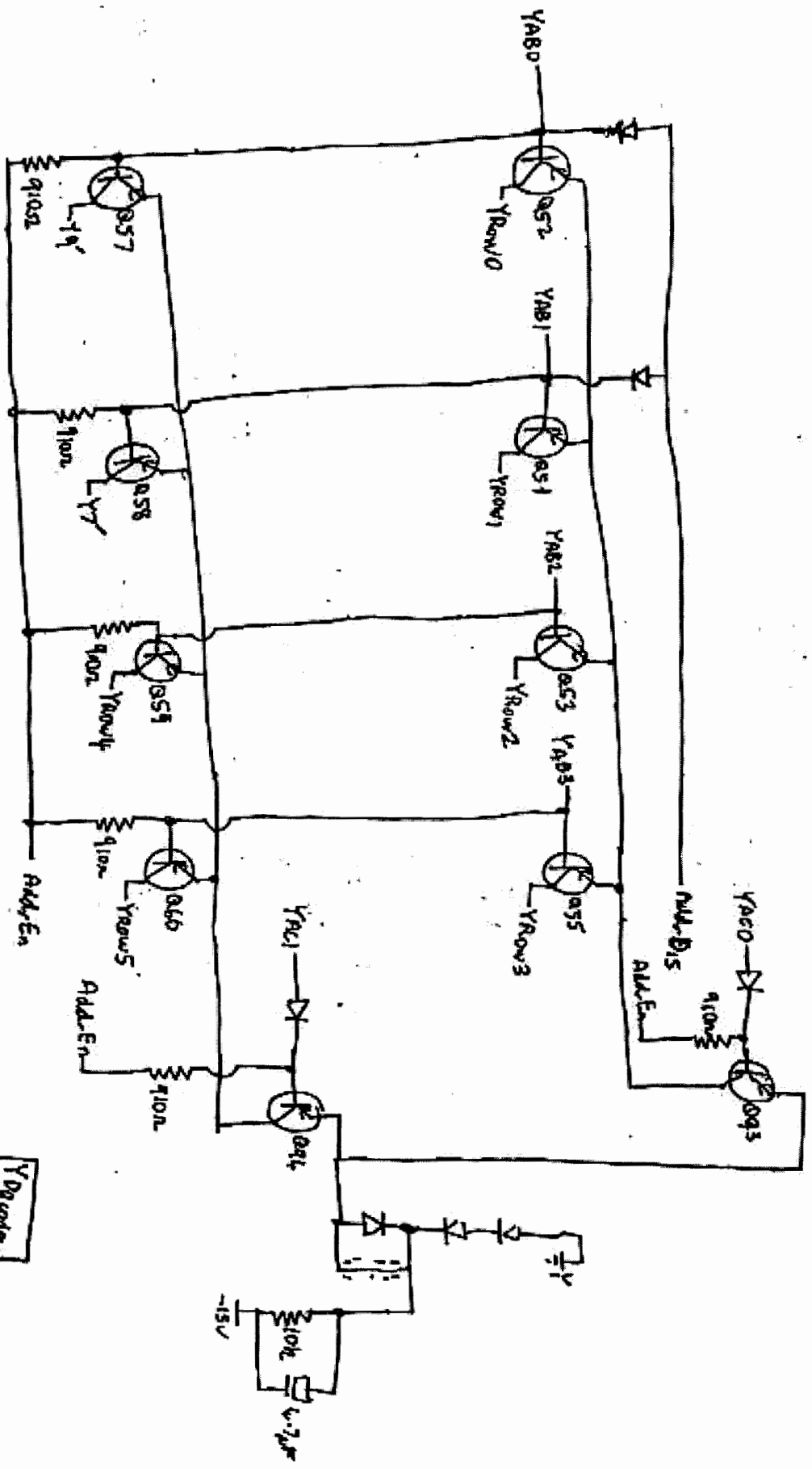


HP9100B Core Address PCB sheet ⑤ 09100-66553



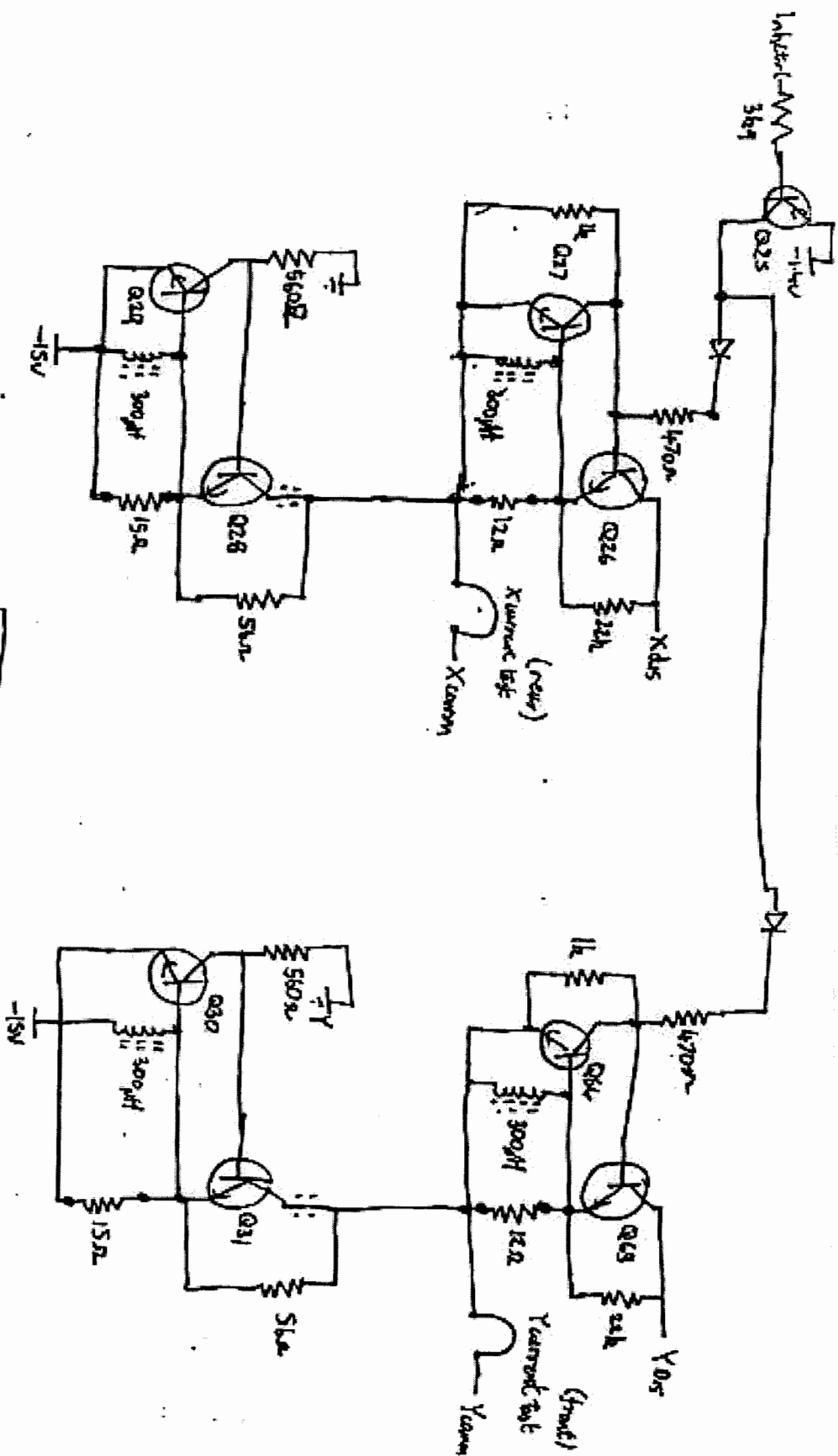
HP9100B core address PCB sheet ⑤

09100-66553

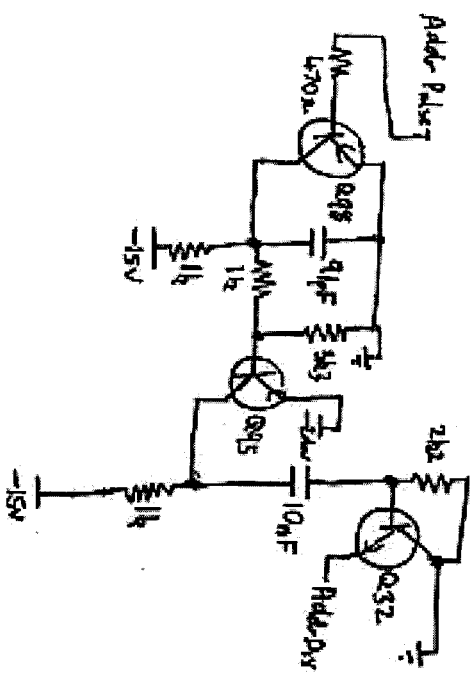
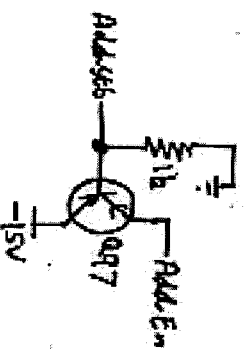


HP9100B Core Address PUB sheet ⑦

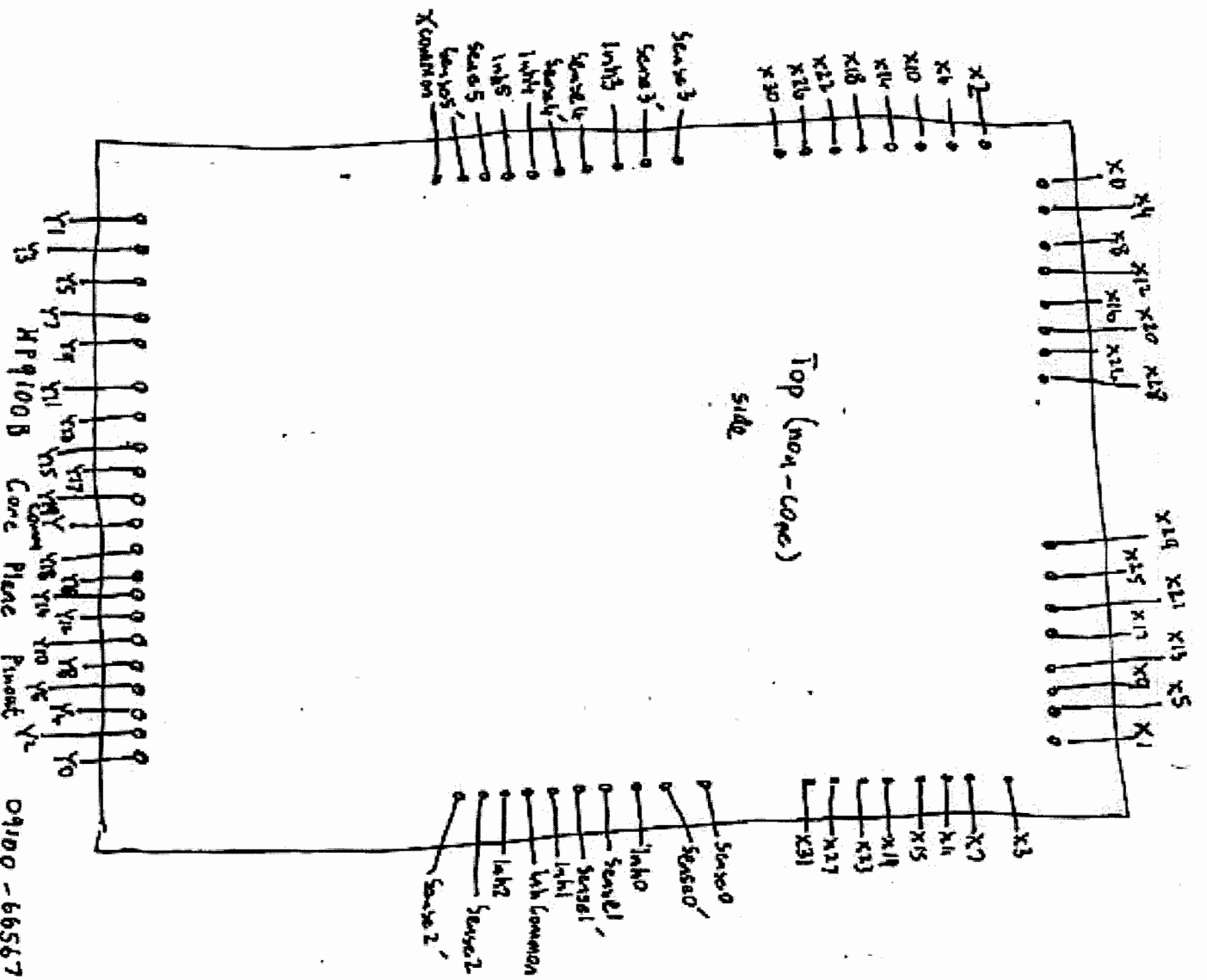
09100-66553

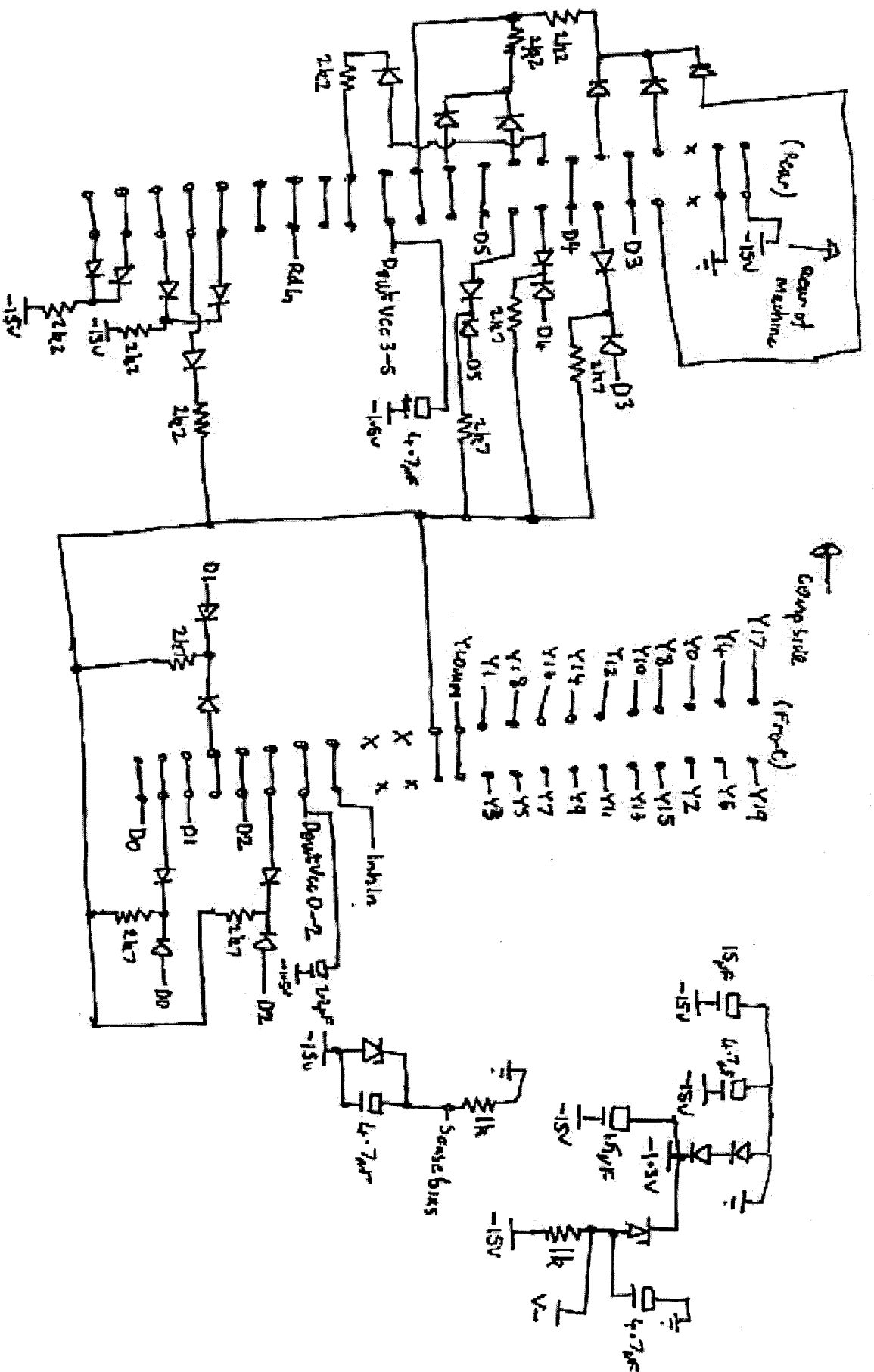


Current Source



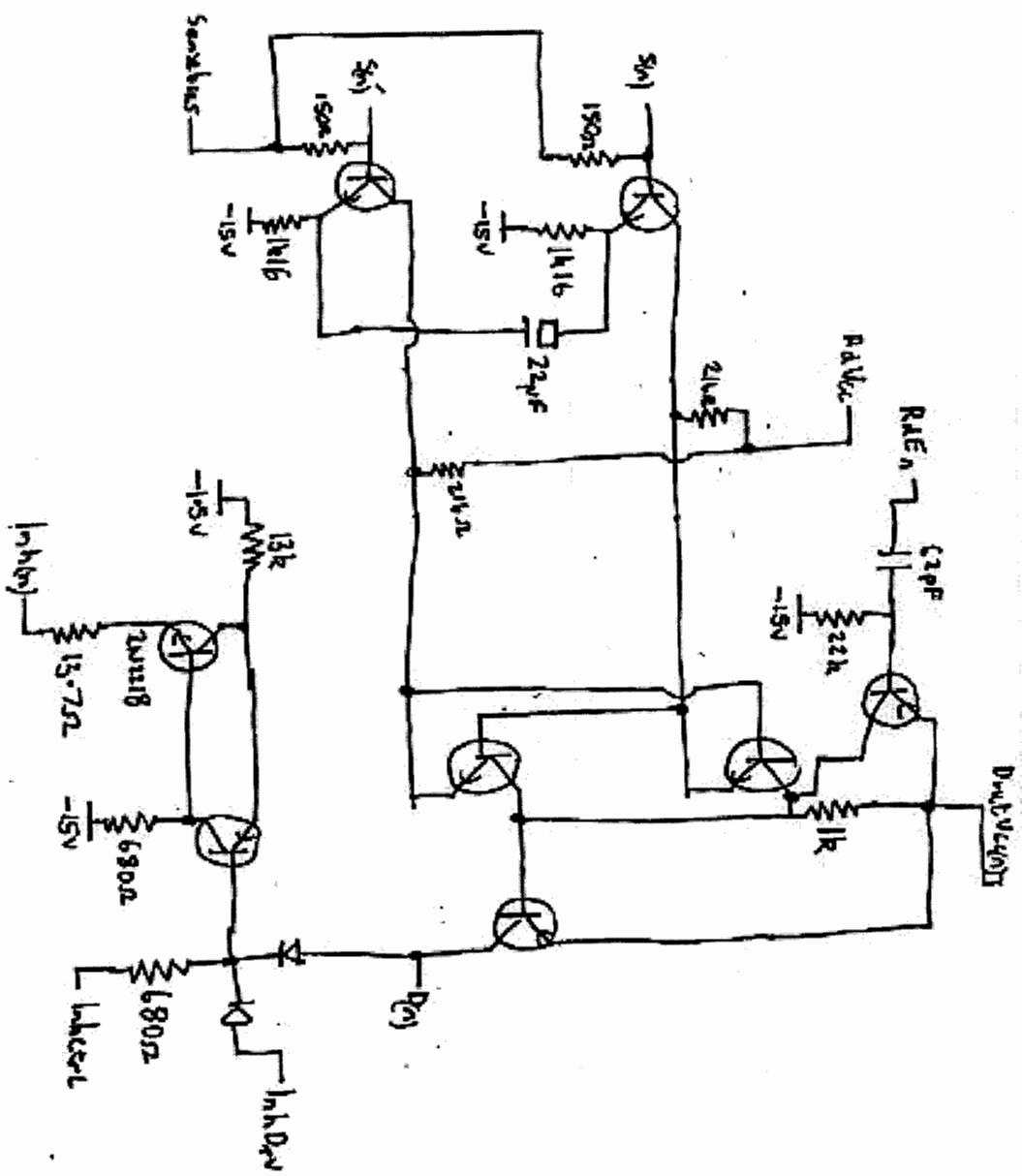
Address Control



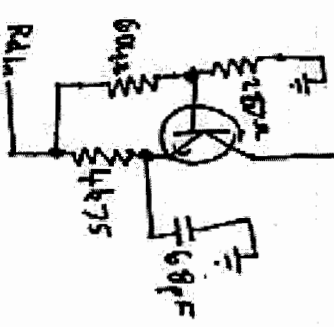
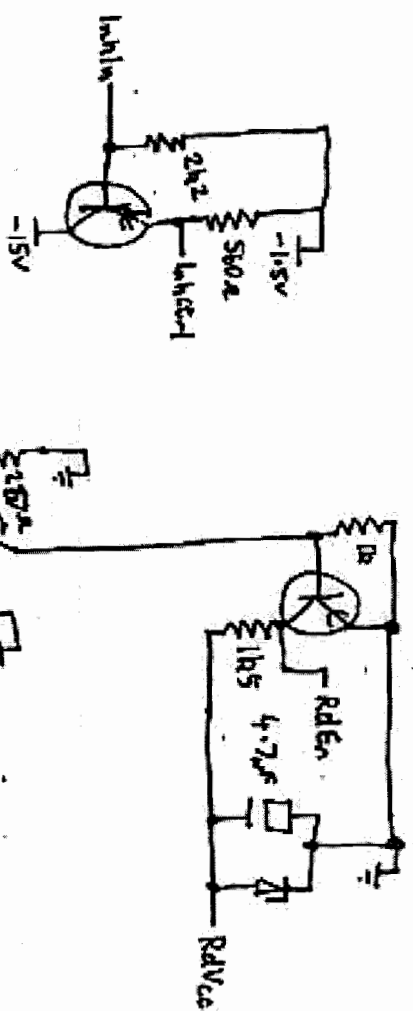


Compside, misc logic

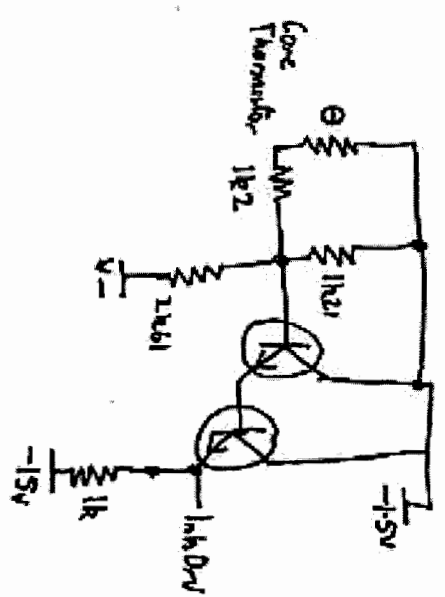
HP9100B Core/Data Reg Sht ① 09100-66554



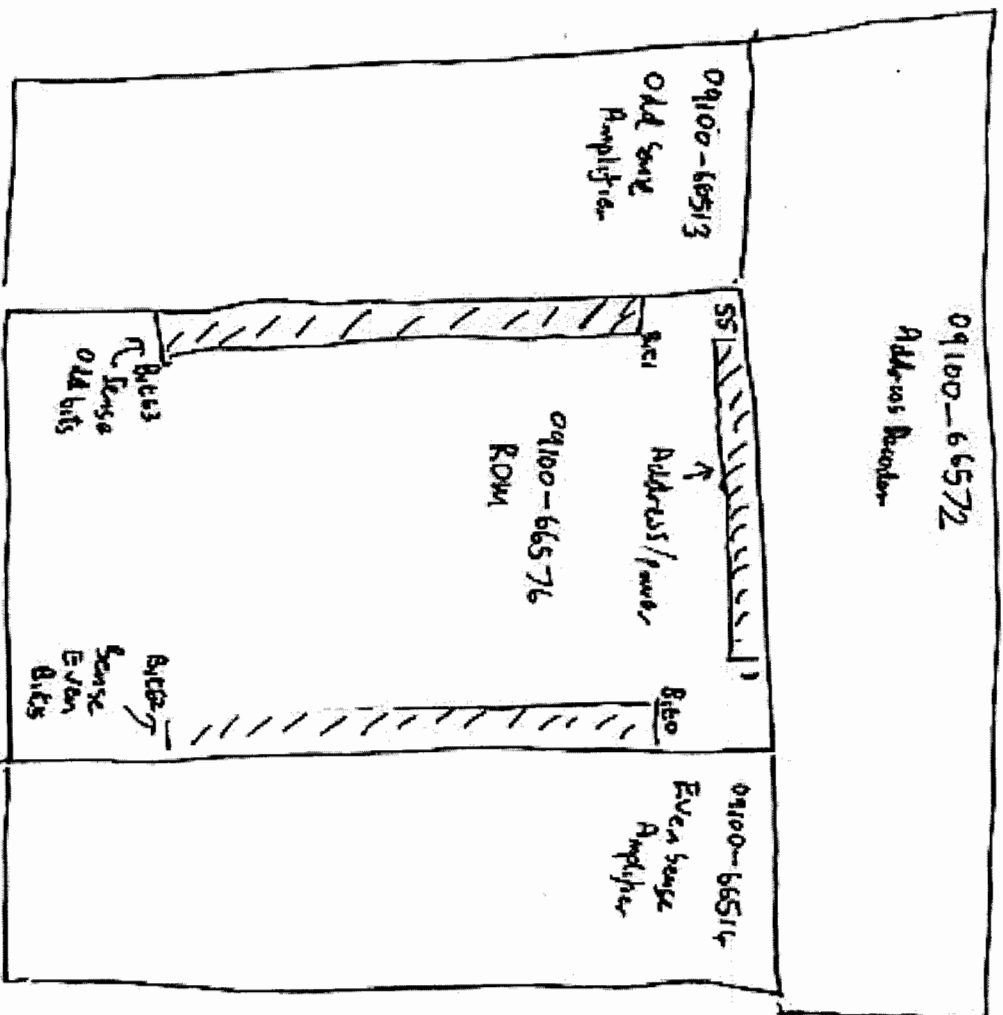
Sense/Inhibit (1of6)



Control Logic

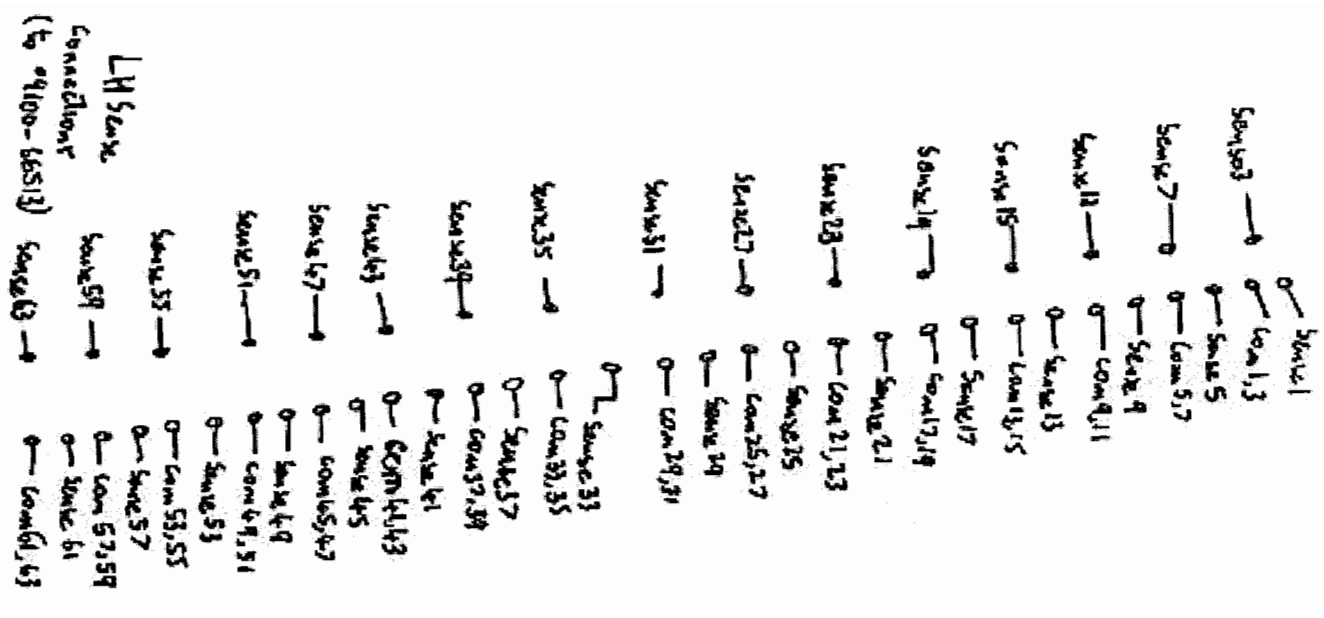


MP9100B core/Data PCB sheet (4)

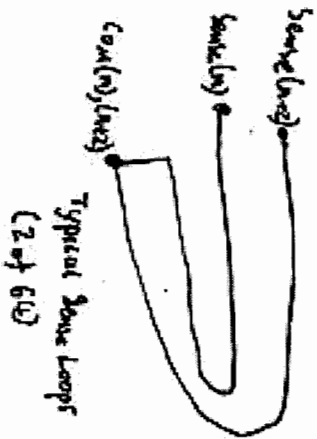


HP1100B Rom Assy layout

Review of
Pinouts



Sense Loops

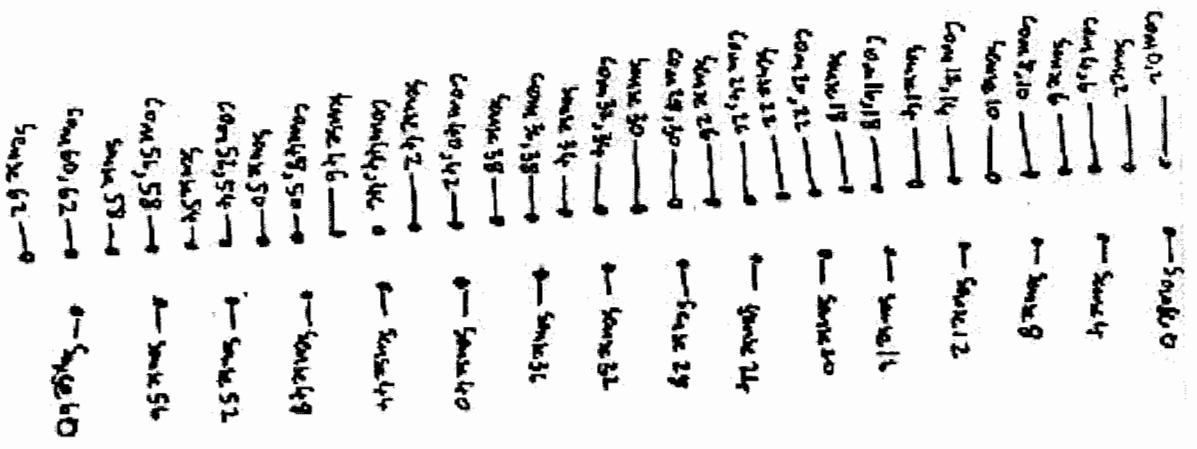


[All ROM connections shown from top
(Track side) of PCB]

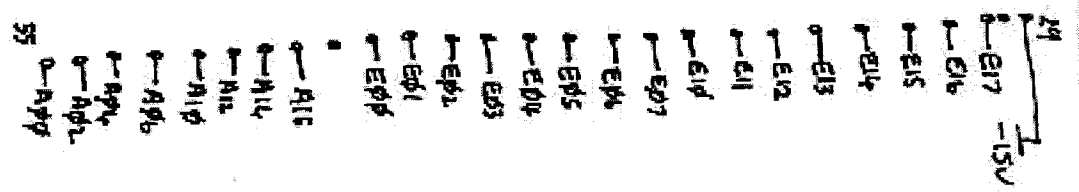
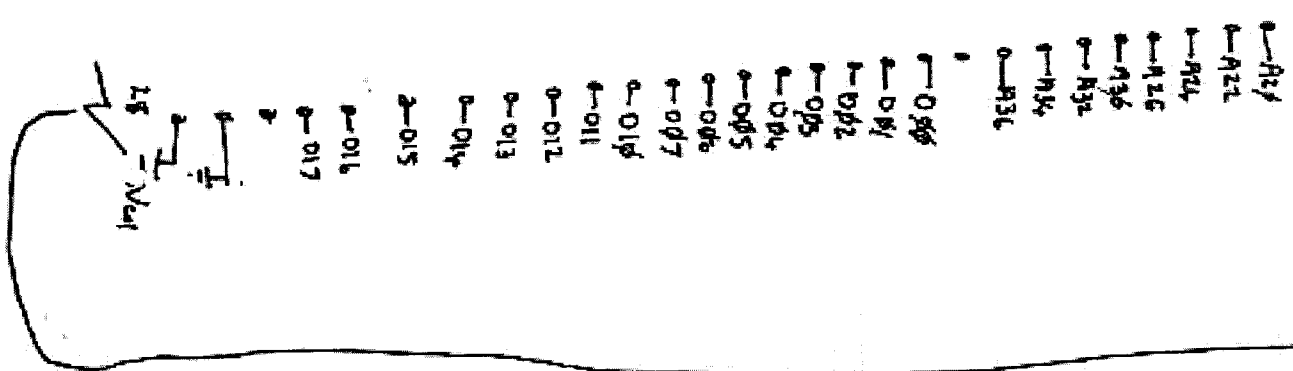
MP100B Rom Sheet ①

09100-66576

RH Sense Connections
(to 09100-66514)



RHS of Machine ↑

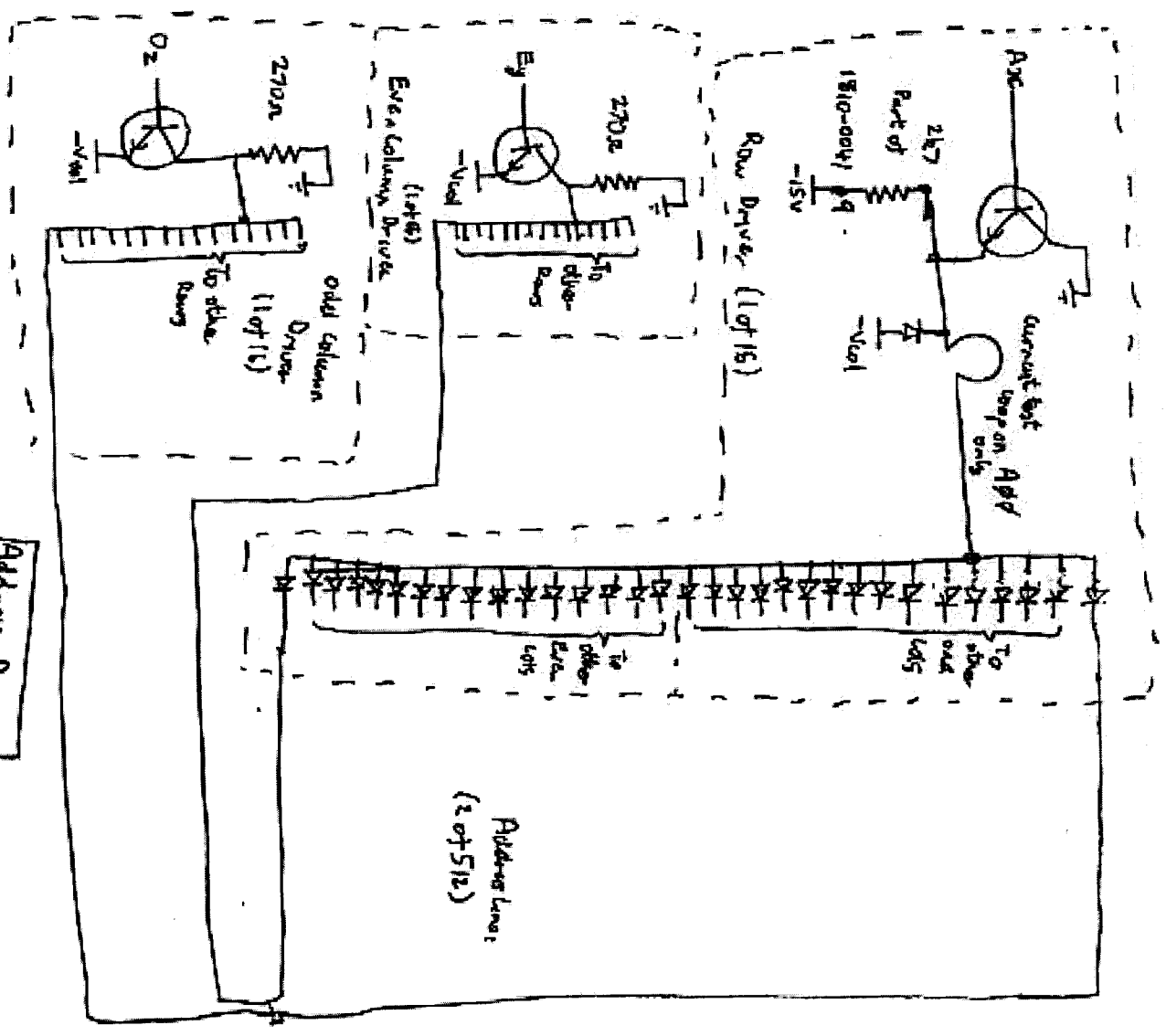


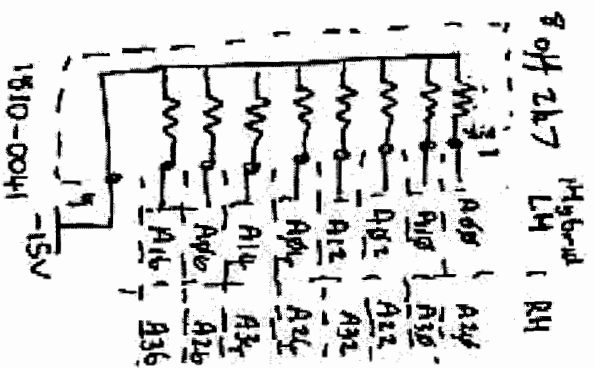
Addresses Connections
To 09100-66572

HP 9100 B Rom sheet ②

09100-66576

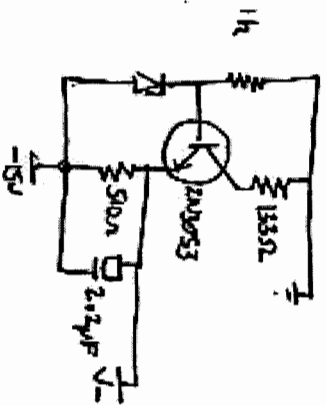
Address Drivers





Hybrid
(Part of Row Driver, Sheet ②)

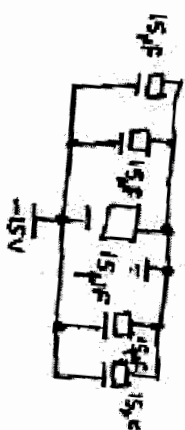
Hybrid Resistor Network
Connections



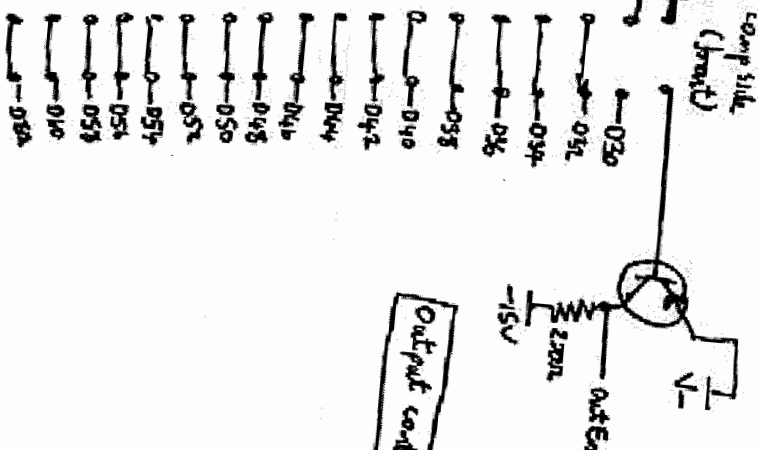
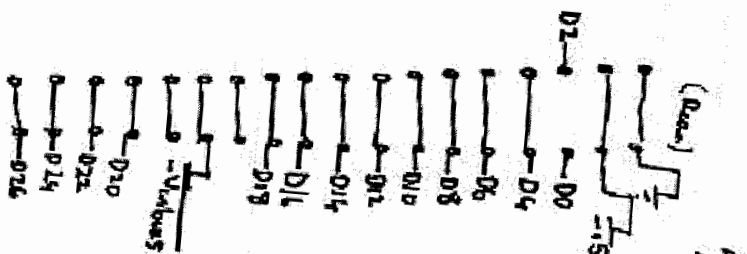
HP91008 odd sense Amplifier sheet ① 09100-66513

com0,3-0 → sense0
 sense1-0 → sense1
 com0,4-0 → sense4
 sense6-0 → sense6
 com0,10-0 → sense8
 sense10-0 → sense10
 com1,2,14-0 → sense12
 sense14-0 → sense14
 com1,8-0 → sense16
 sense18-0 → sense18
 com2,11-0 → sense20
 sense22-0 → sense22
 com2,12,26-0 → sense24
 sense28-0 → sense28
 com2,8,30-0 → sense30
 sense32-0 → sense32
 com3,34-0 → sense34
 sense36-0 → sense36
 com4,0,16,2-0 → sense40
 sense42-0 → sense42
 com4,4-0 → sense44
 sense46-0 → sense46
 com4,8,50-0 → sense48
 sense50-0 → sense50
 com5,52,34-0 → sense52
 sense54-0 → sense54
 com5,6,58-0 → sense56
 sense58-0 → sense58
 com6,0,62-0 → sense60
 sense62-0 → sense62

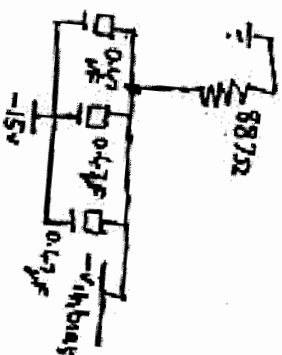
Sense Connections
 to Rom (09100-66576)



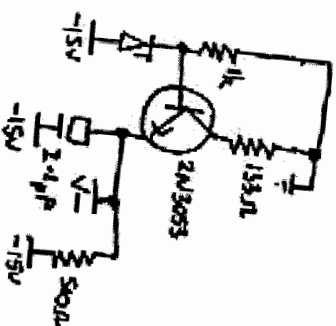
Connect 5



Output control



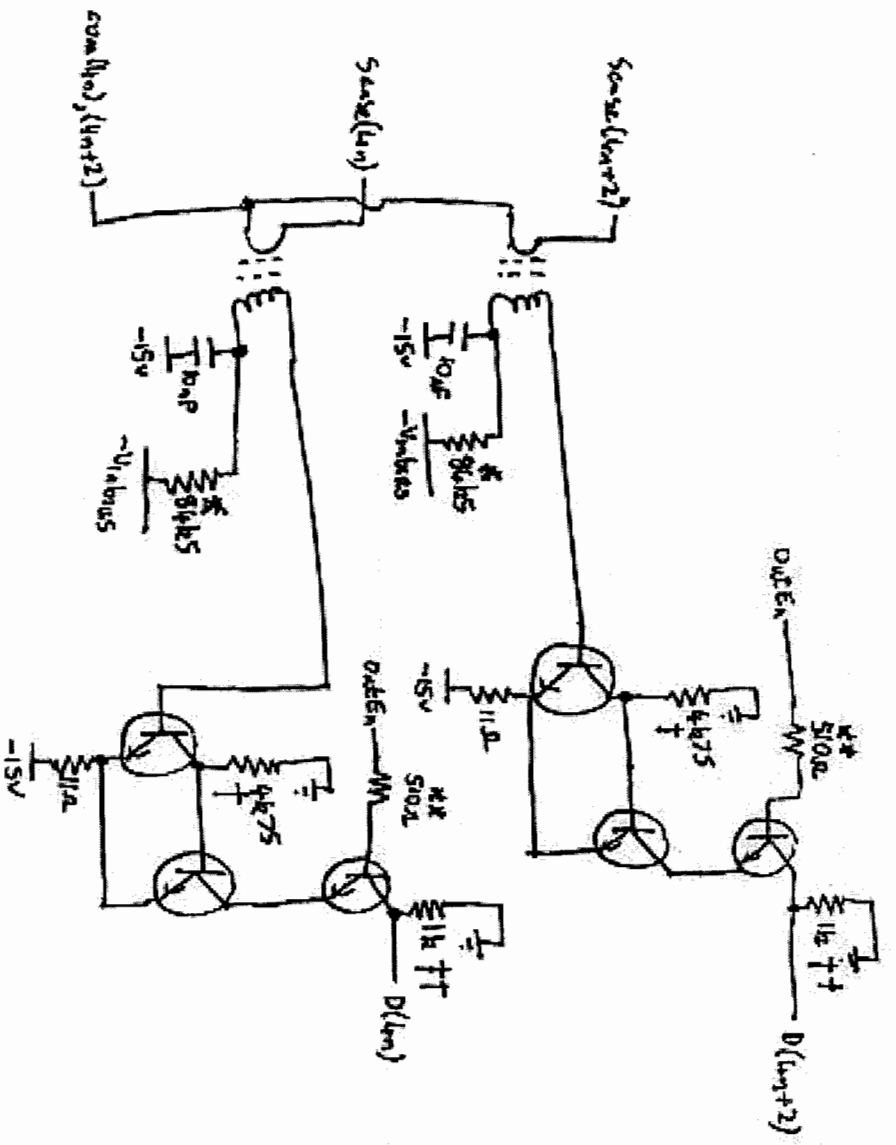
PSU



HP9100B Even Sense Amplifier

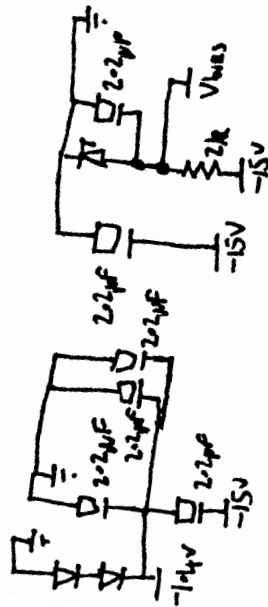
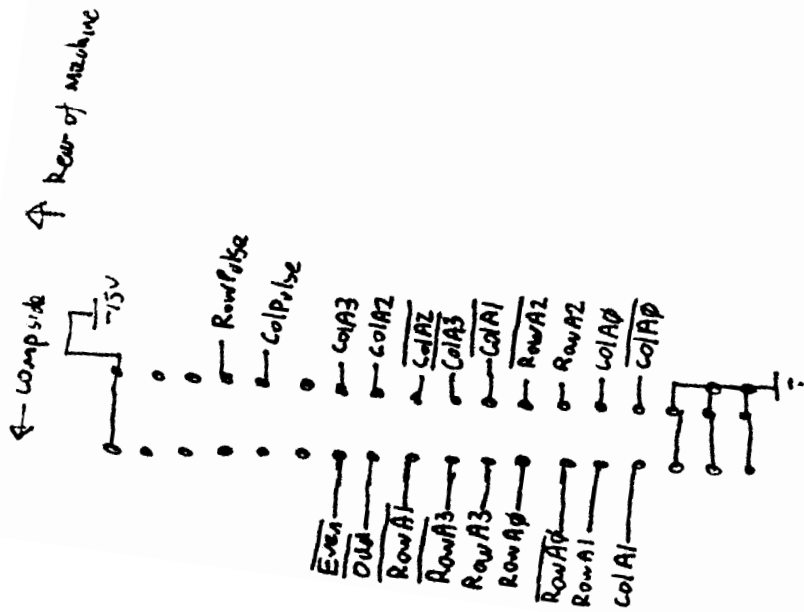
Sketch 1

09100-66576



Sense Amplifier (2 of 32)

- Component changes:
- # 110k D₄
 - 75k D₁₂
 - + 2k32 D₁₂, D₁₈
 - # 240k D₀, D₀₂, D₀₄, D₁₂, D₂₅, D₅₁
 - 150k D₁₆, D₄₈
 - ++ 240k D₀



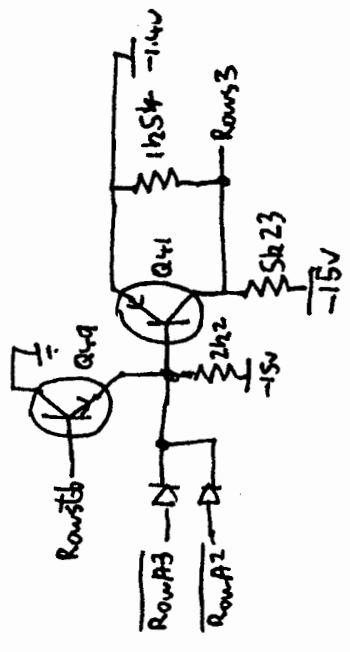
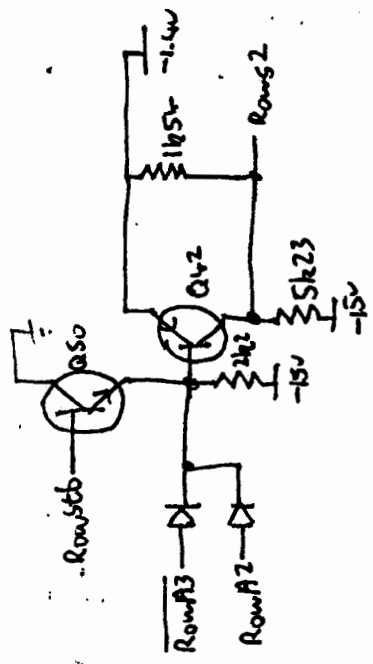
PSU



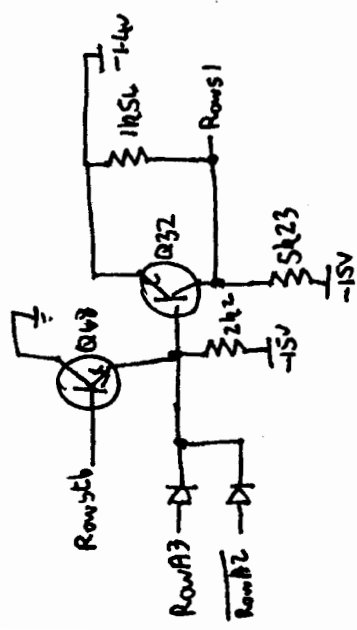
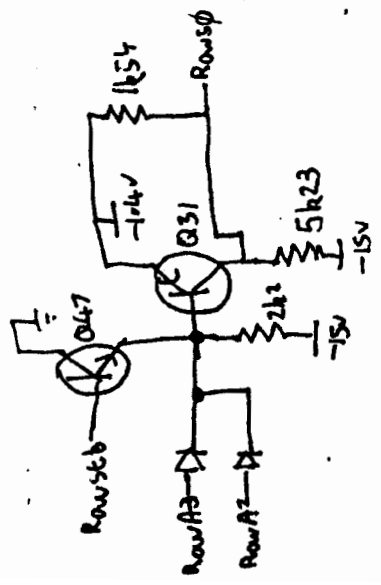
Row Address Connections
(to 09100-66576)

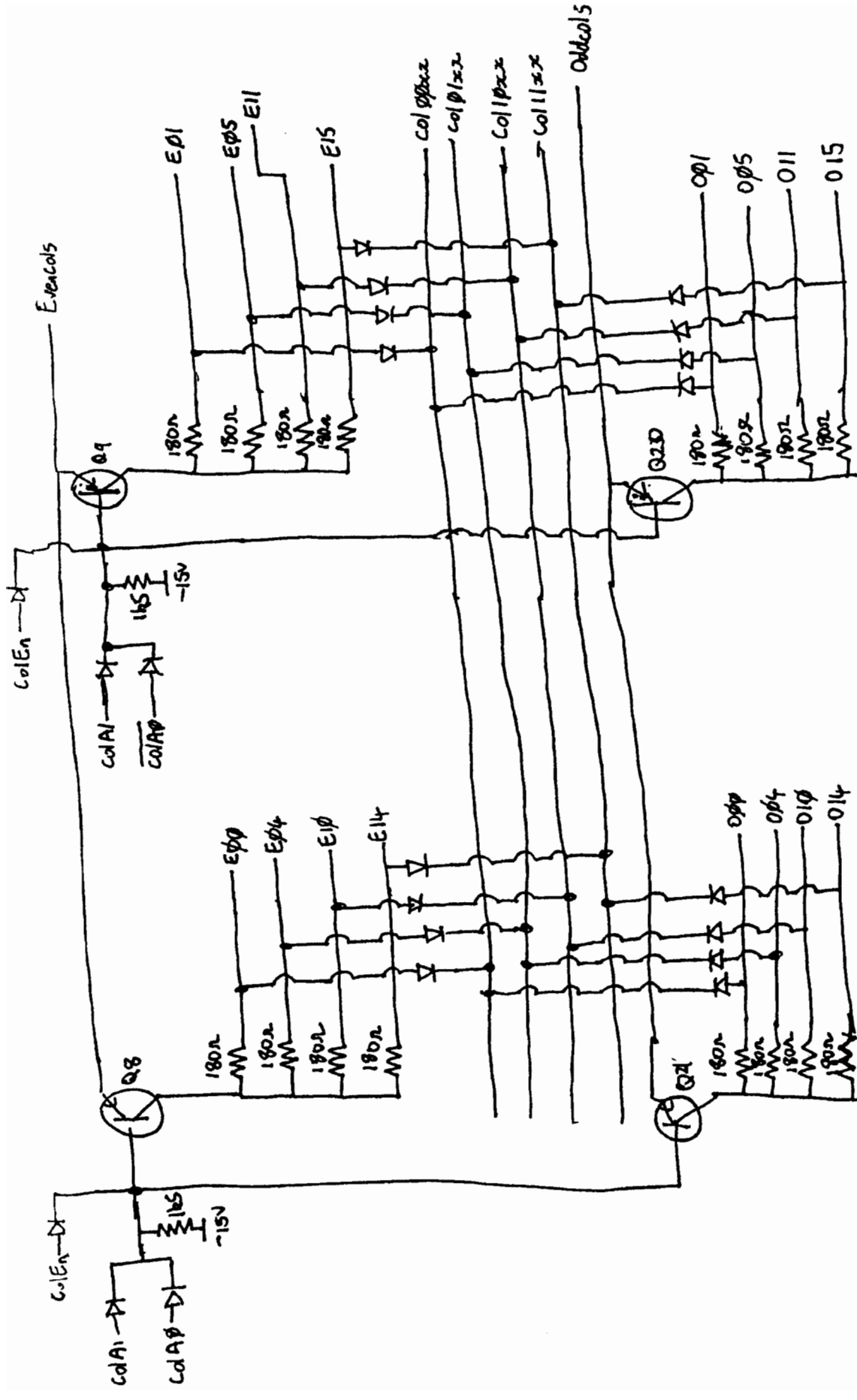
HP9100A Row Address Decoder Sheet (1)

09100-66572

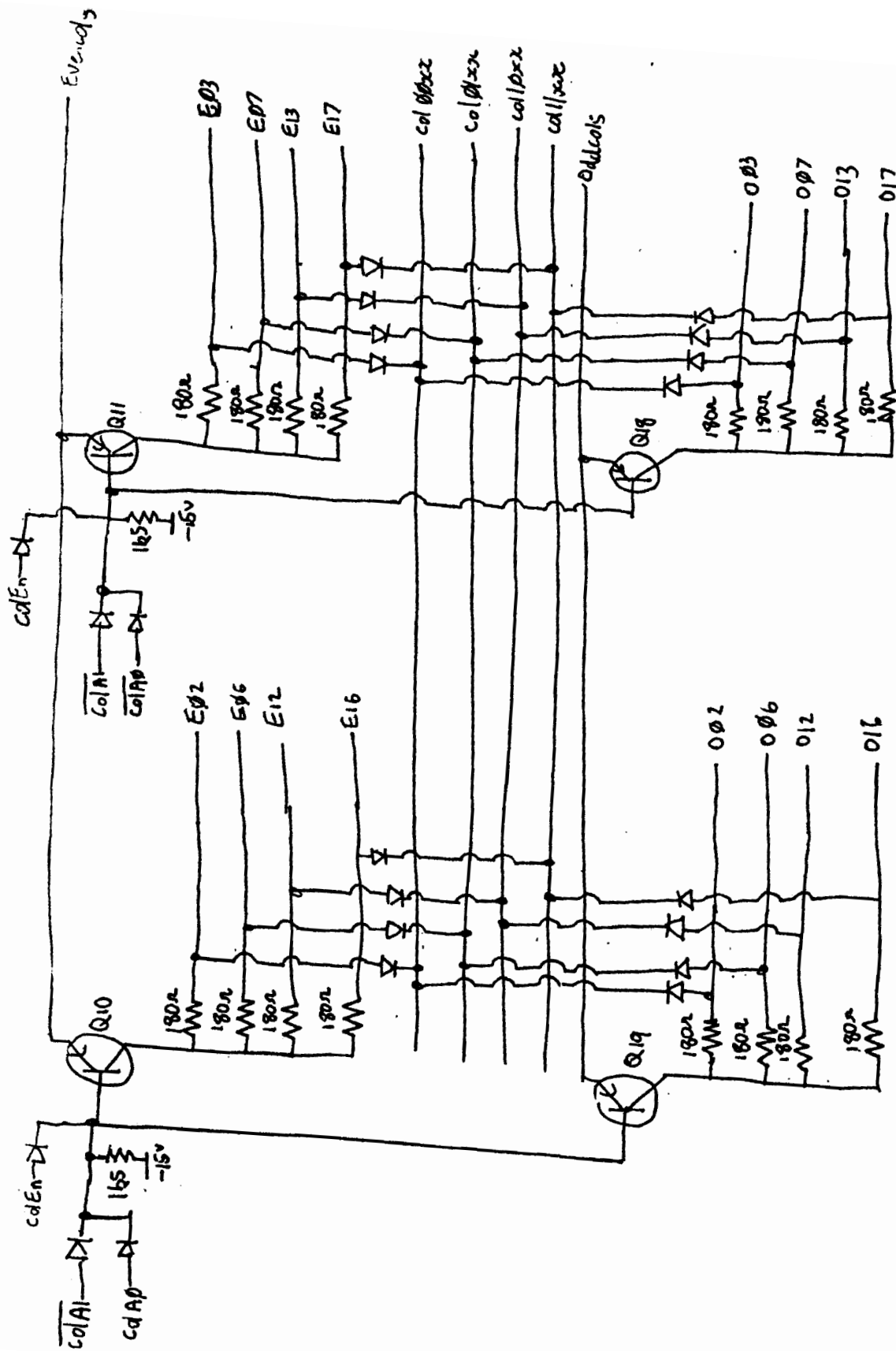


Row Decoder

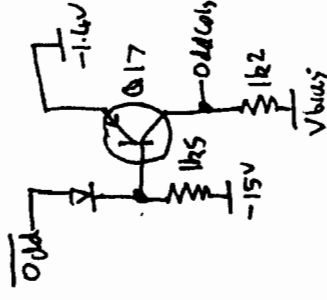
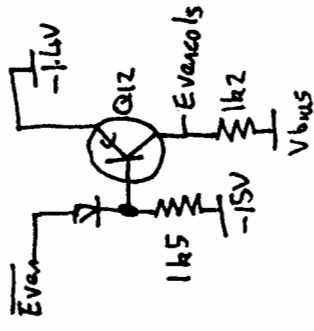
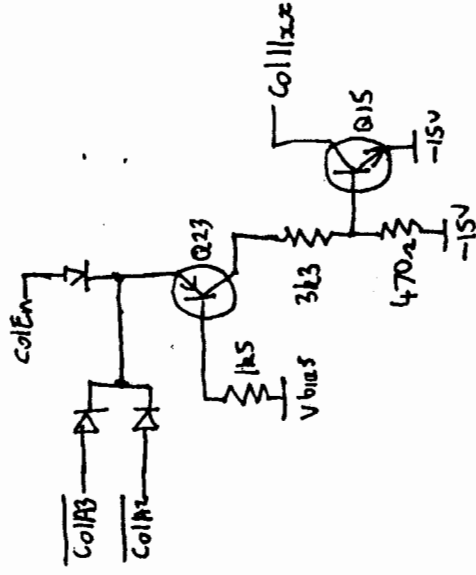
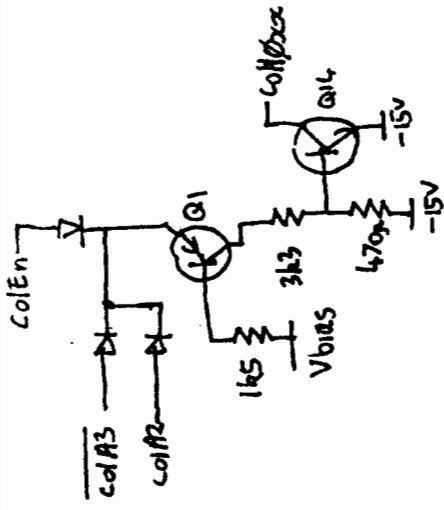
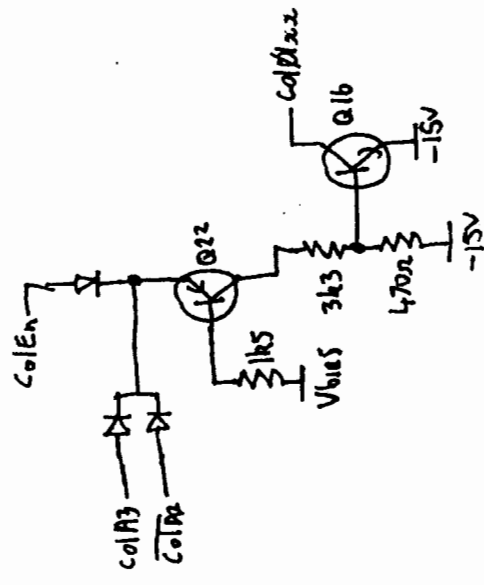
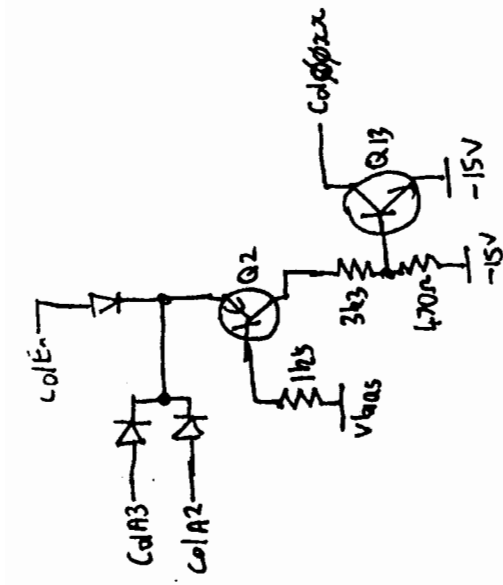




Column Decoder 0,11

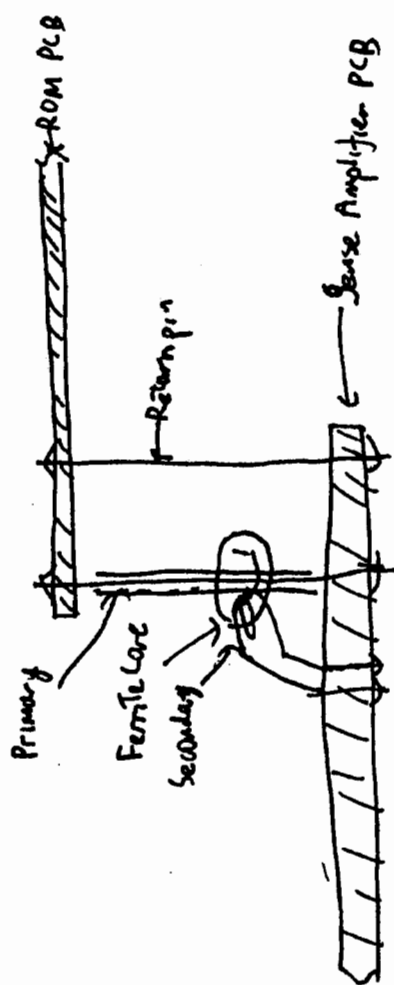


Column Decoder 2,3

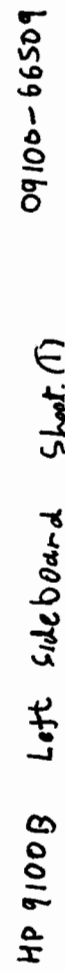


Column Decoder High Bits

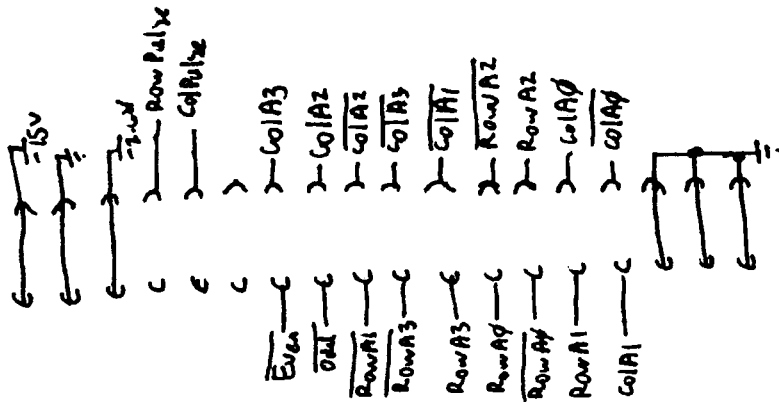




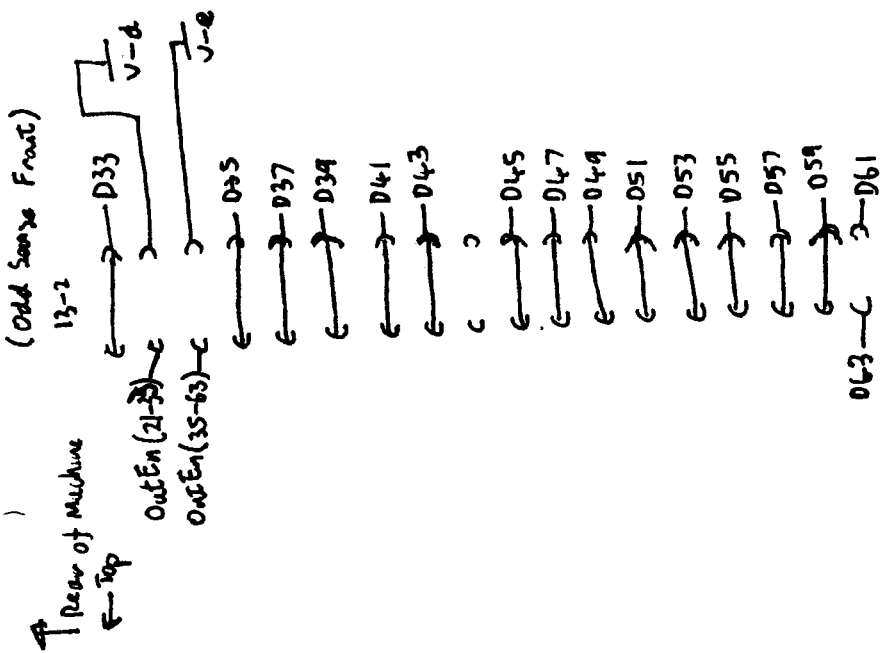
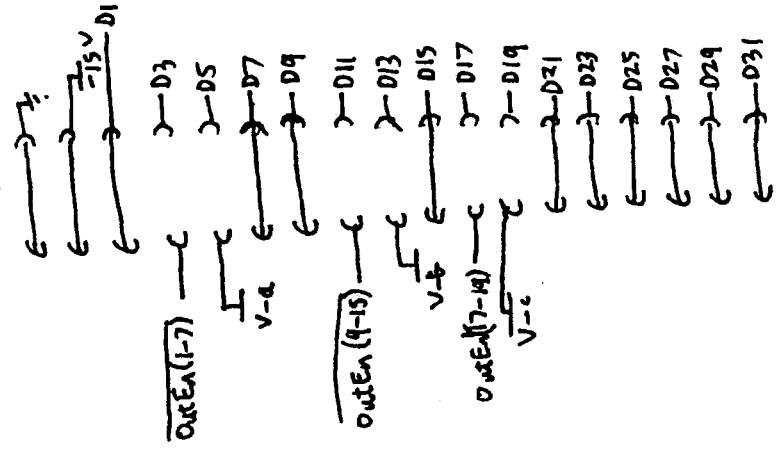
HP 9100B ROM Data Pulse Transformer



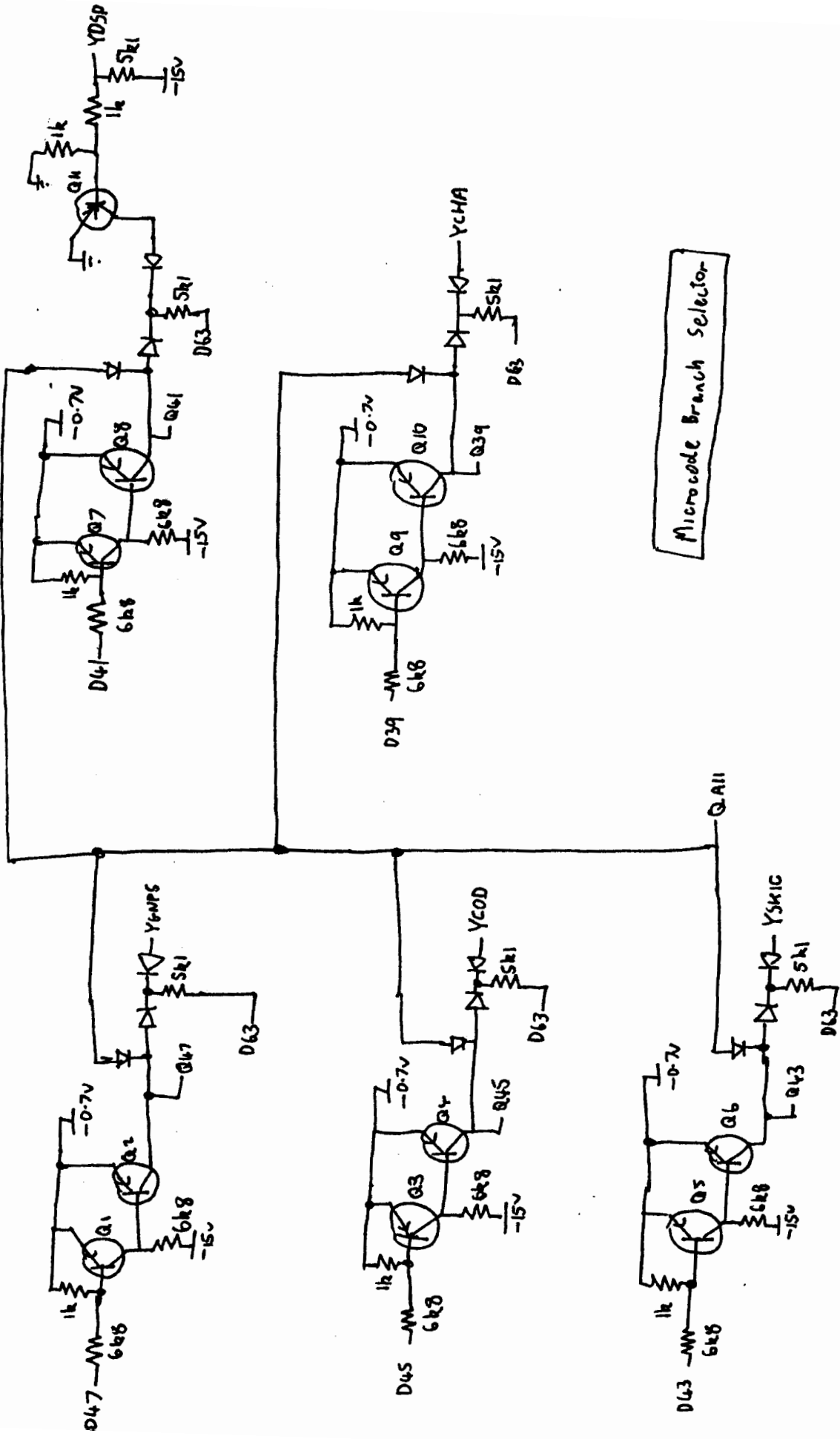
(Address)
12-1

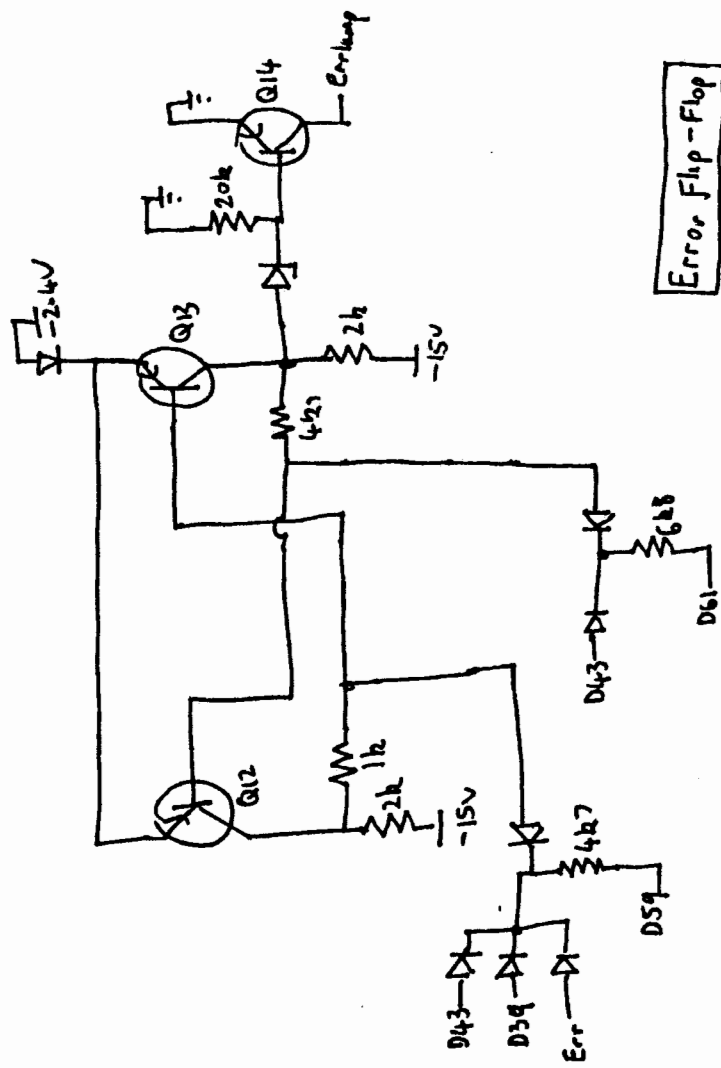


(Odd Sense Rear)
13-1

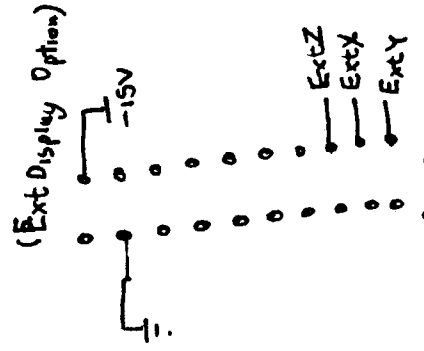
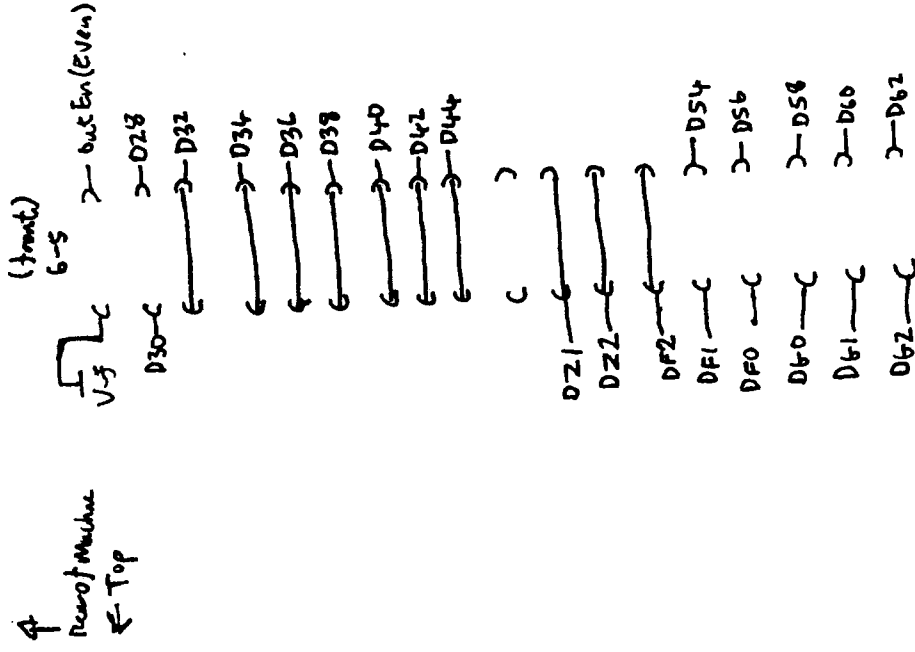
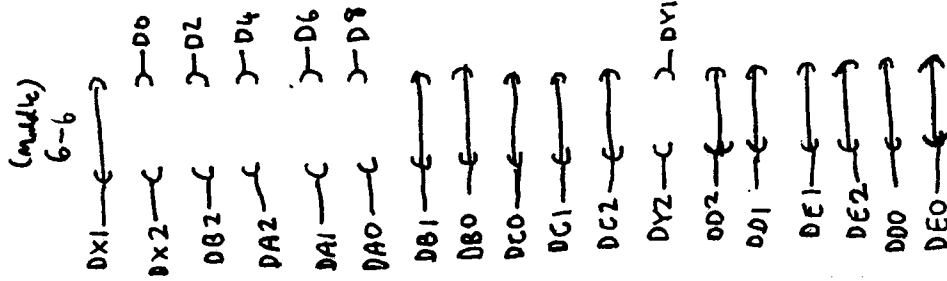
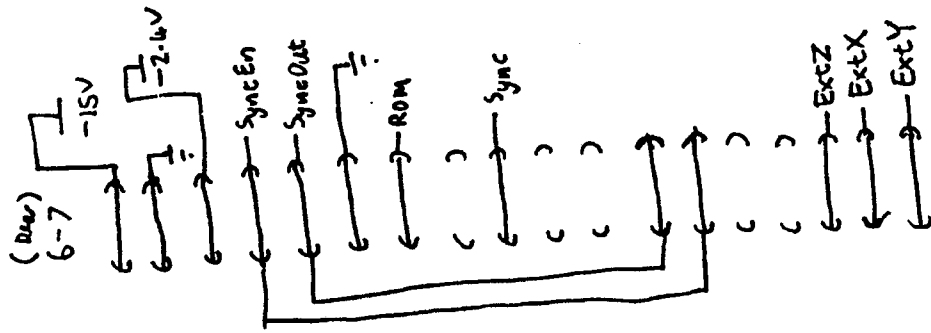


ROM Connectors



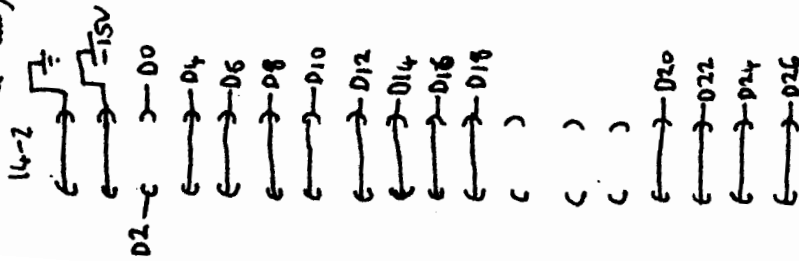


Error Flip-Flop

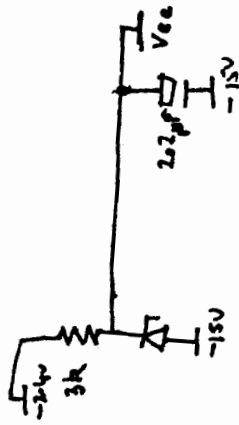
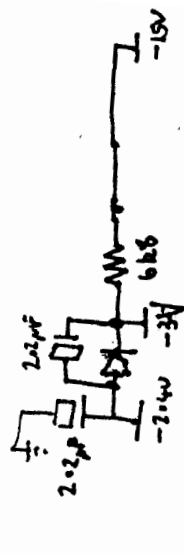
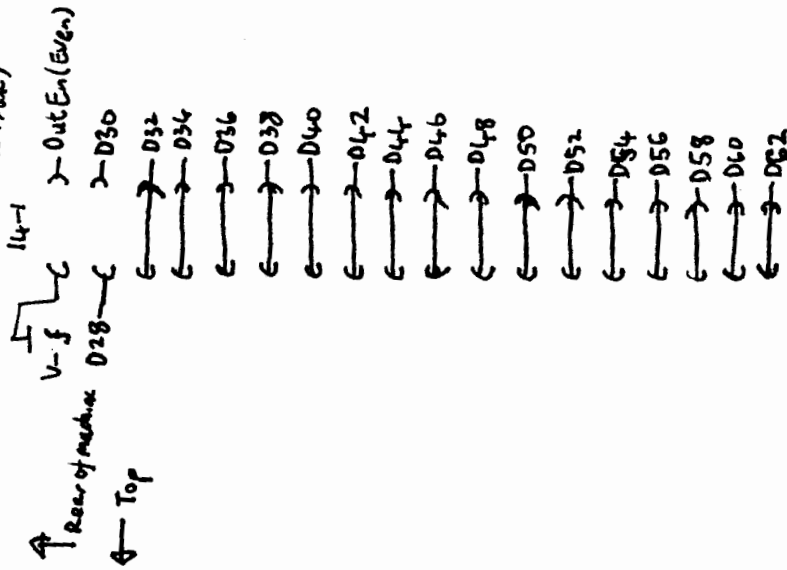


Getting PCB Connectors

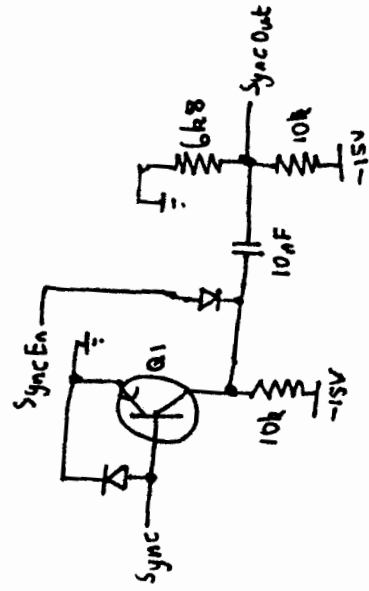
(Even Sense Rear)



(Even Sense Front)



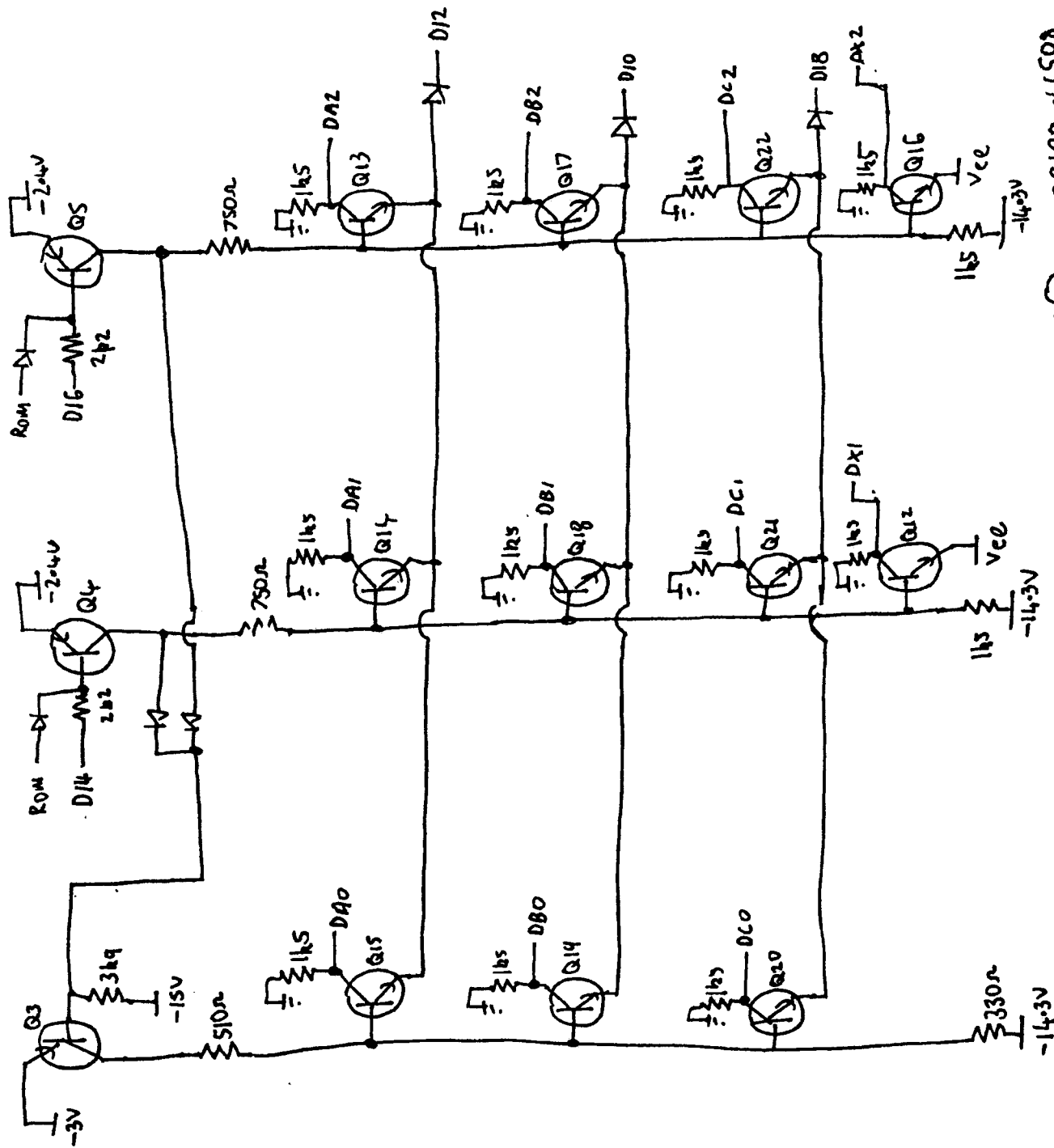
PSU



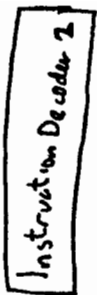
Rom Connectors

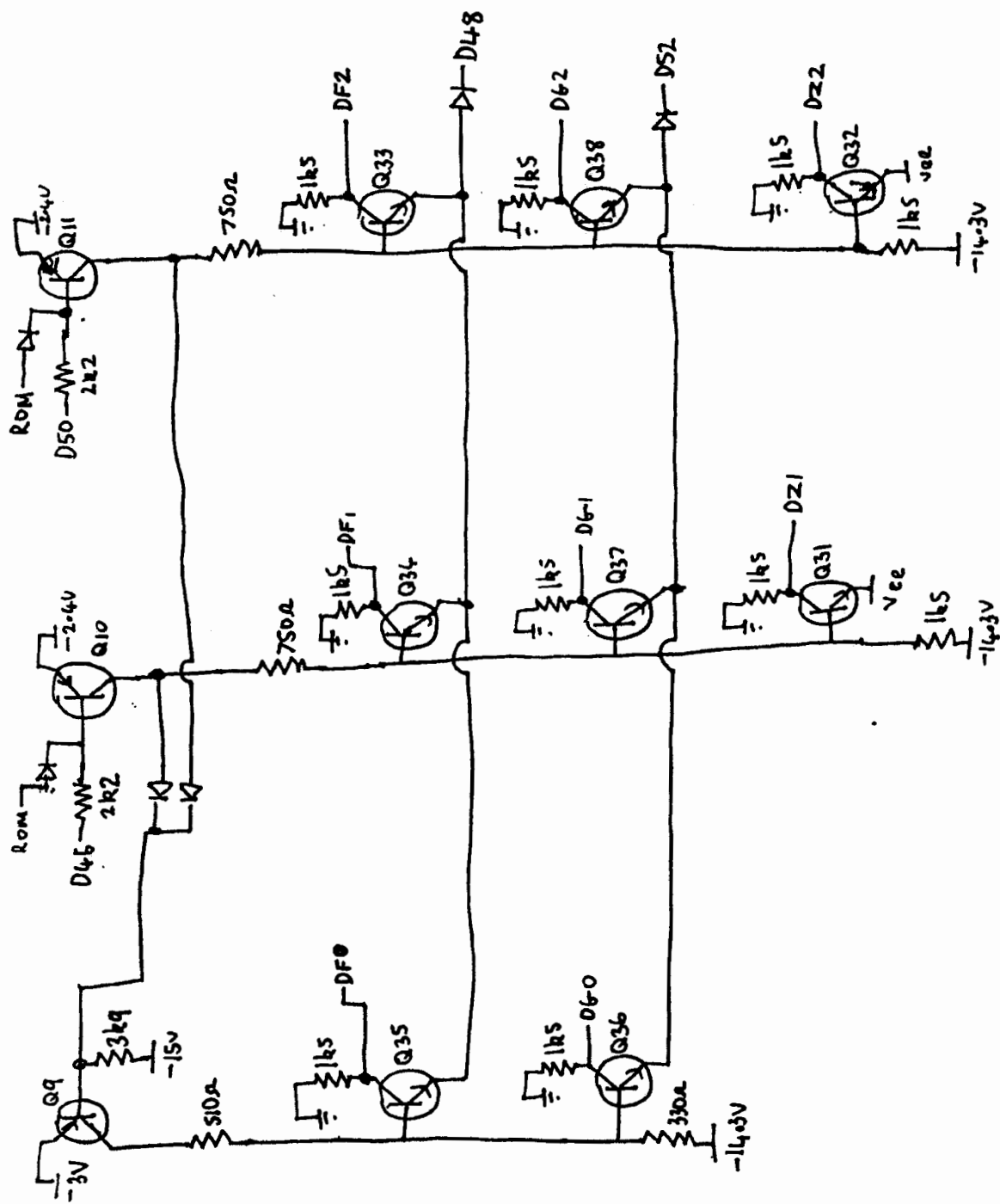
Pin 5 Sync

09100-66509

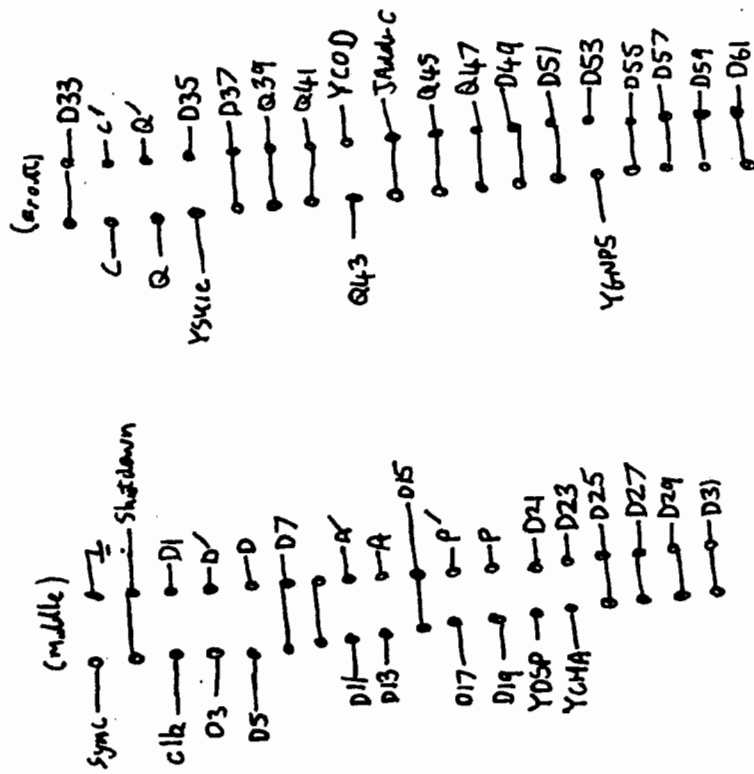


Instruction Decoder 1





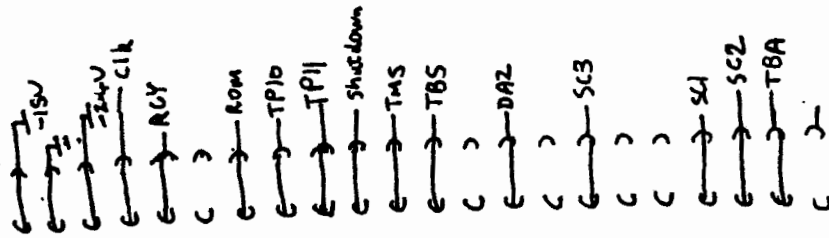
↑ Rear of machine
 ← Top



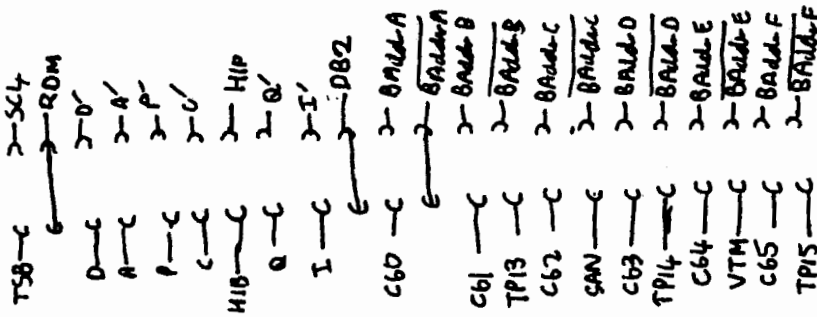
Note: All resistors 4k7 unless
 specified

Left Sideboard Connectors

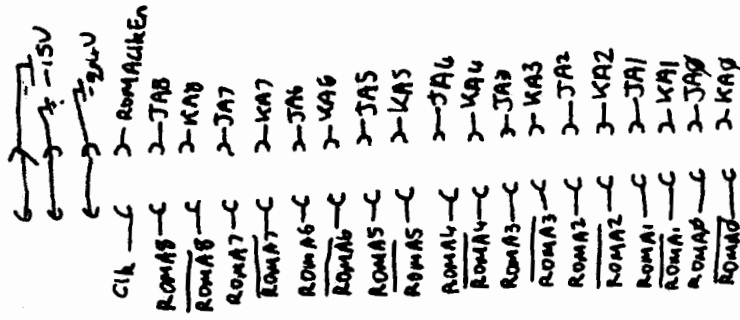
(Control Logic Rear)



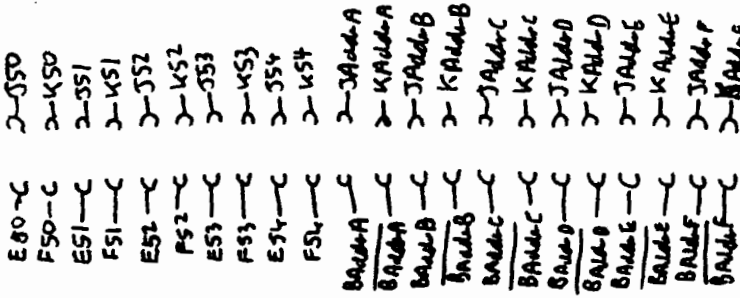
(Control Logic Front)



(LM FlipFlop Rear)



(LM FlipFlop Front)



Left Hand Modules

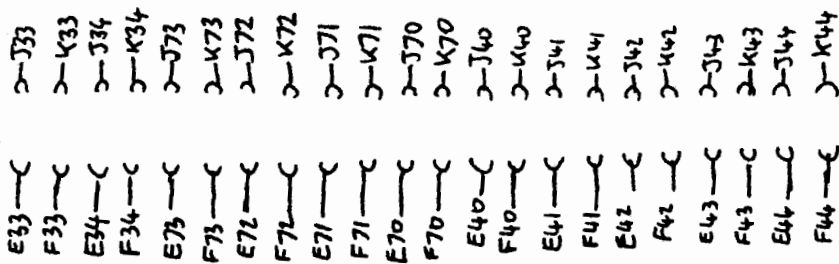
A Rear of Machine
LHS

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(Rear)

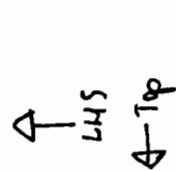


(Front)

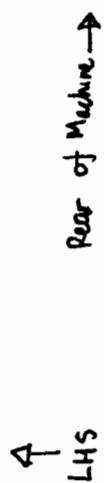


RH Flip Flop Connectors

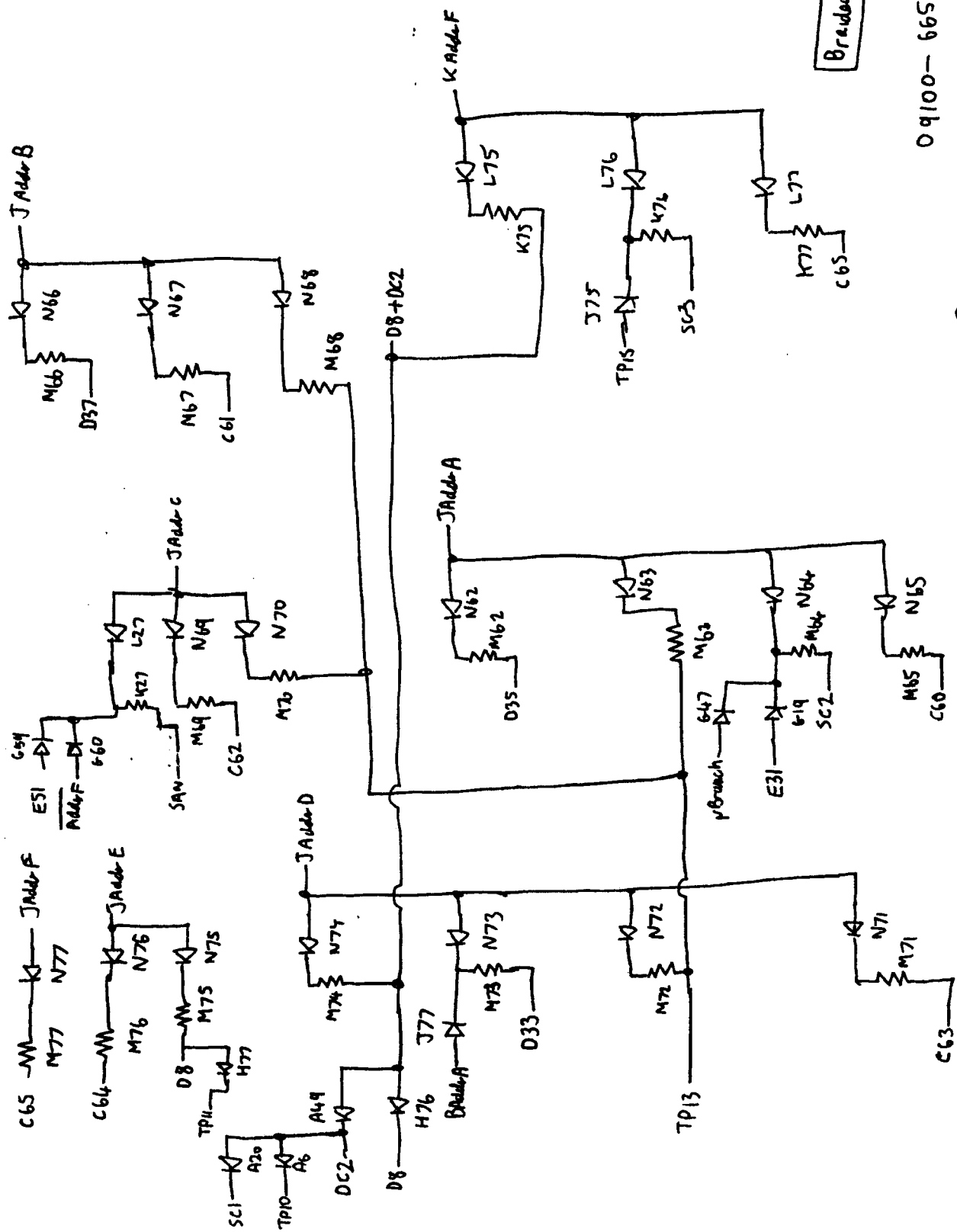
LHS Rear of machine



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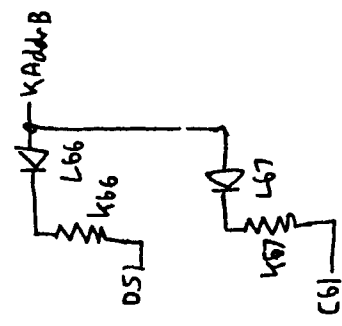
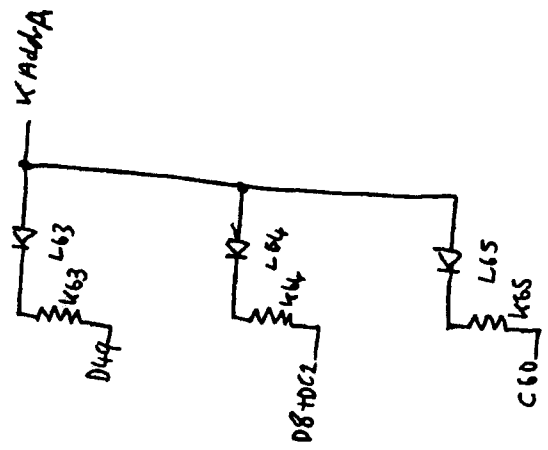
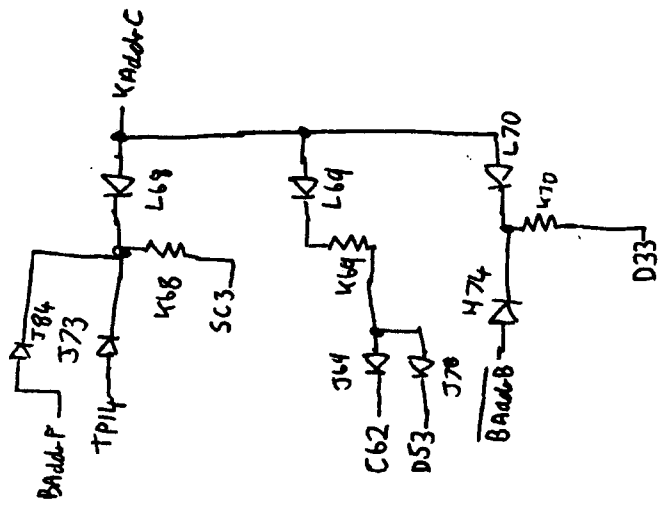
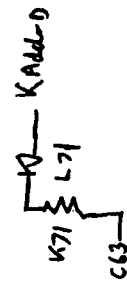
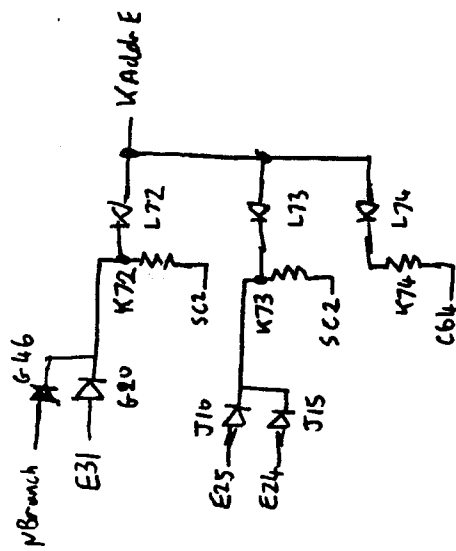


Peripheral Connectors

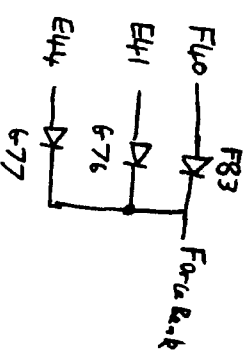
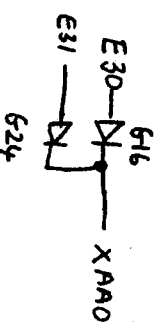
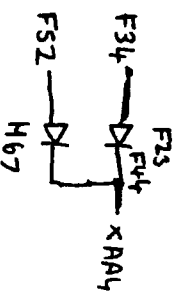
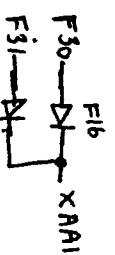
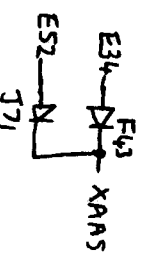
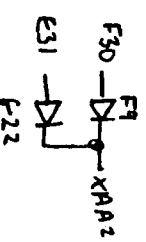
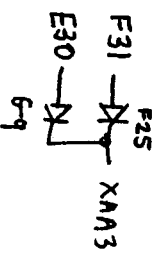
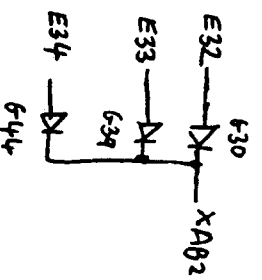
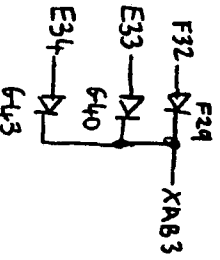
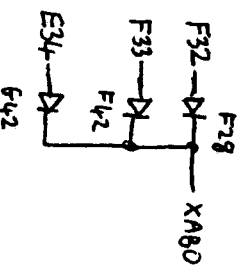
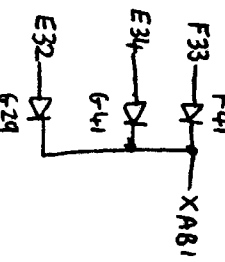
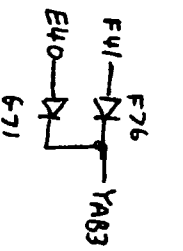
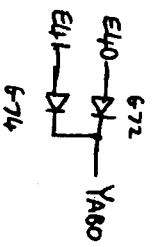
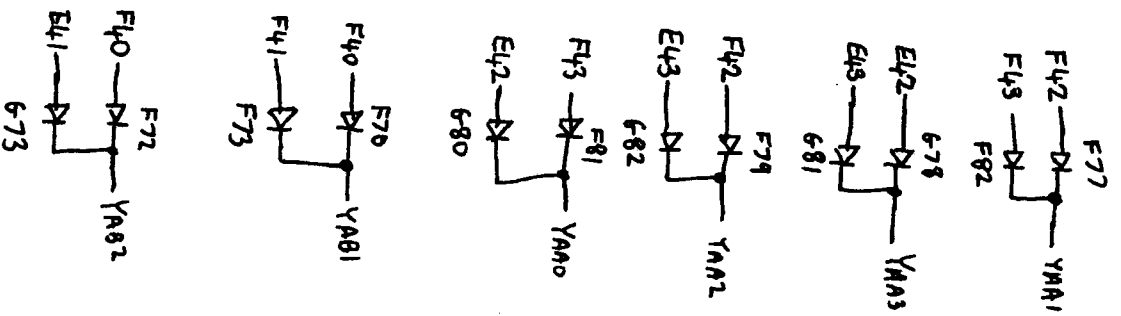


Braded ROM Address

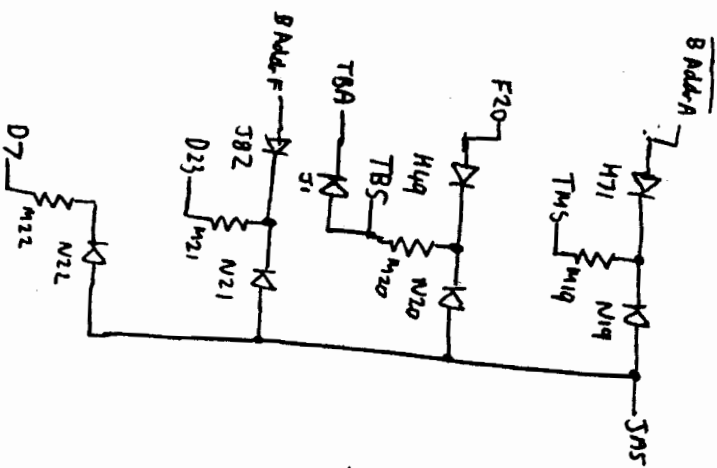
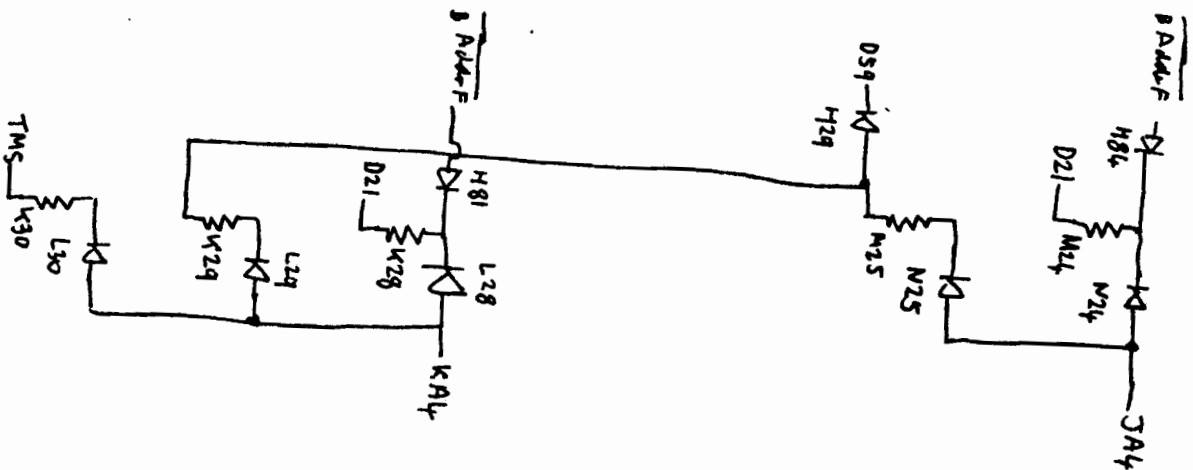
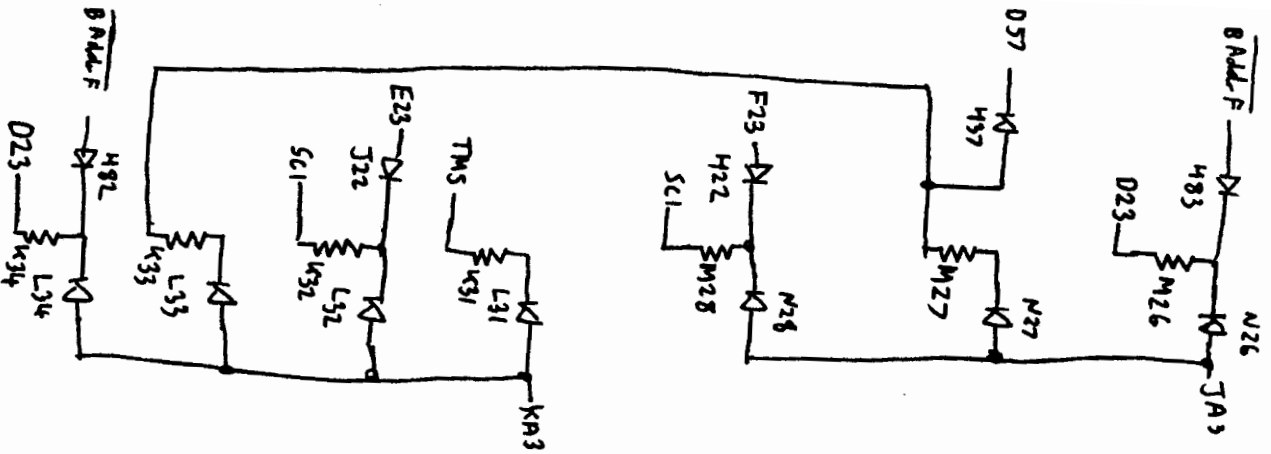
09100-66556



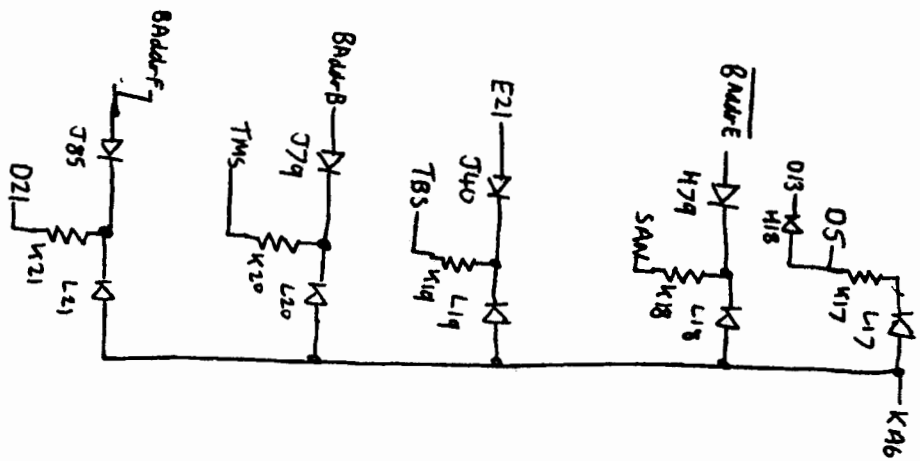
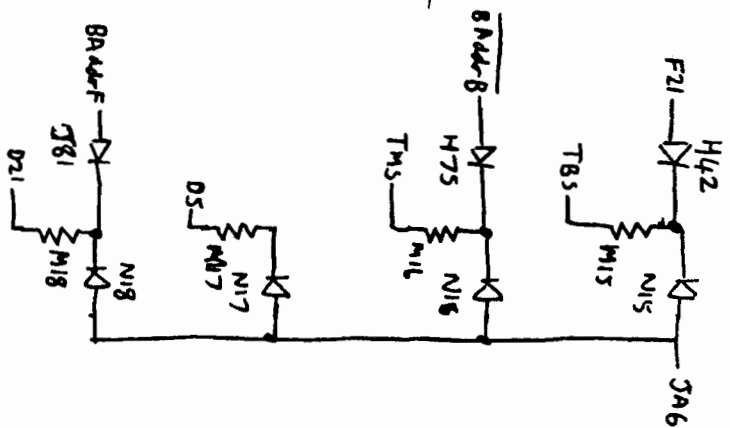
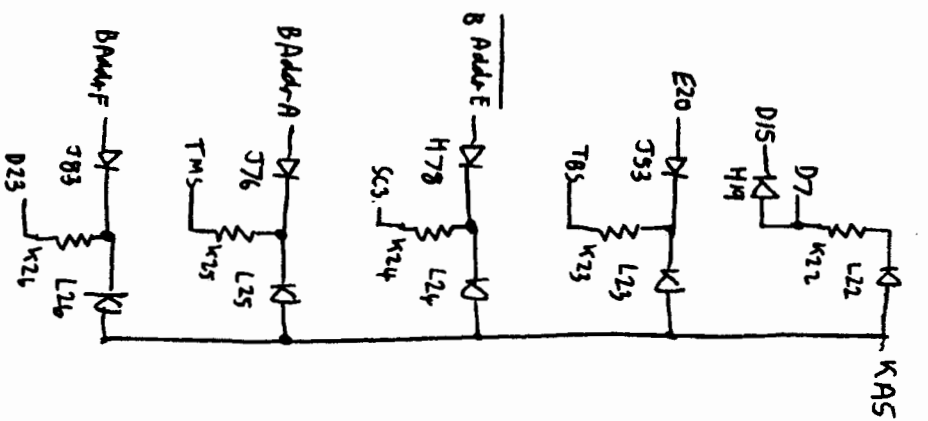
Braded Rom Address



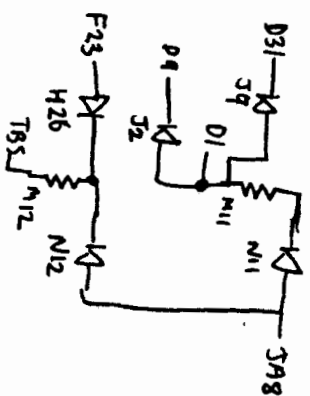
Core Address Decoder



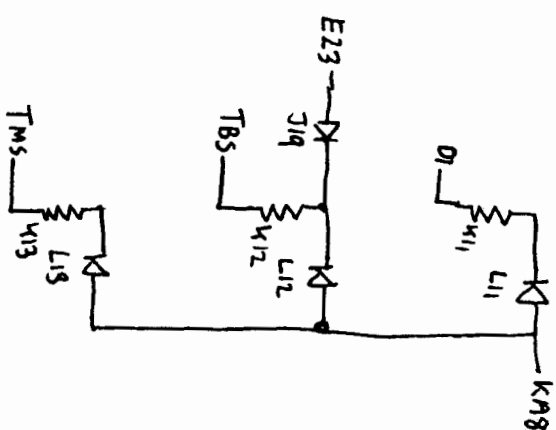
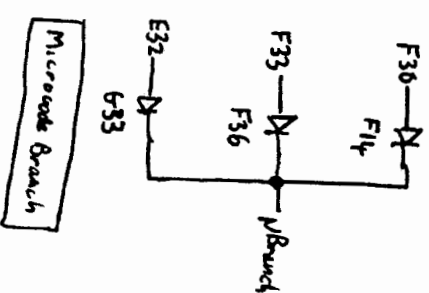
Program Sequencer



Program Sequencer

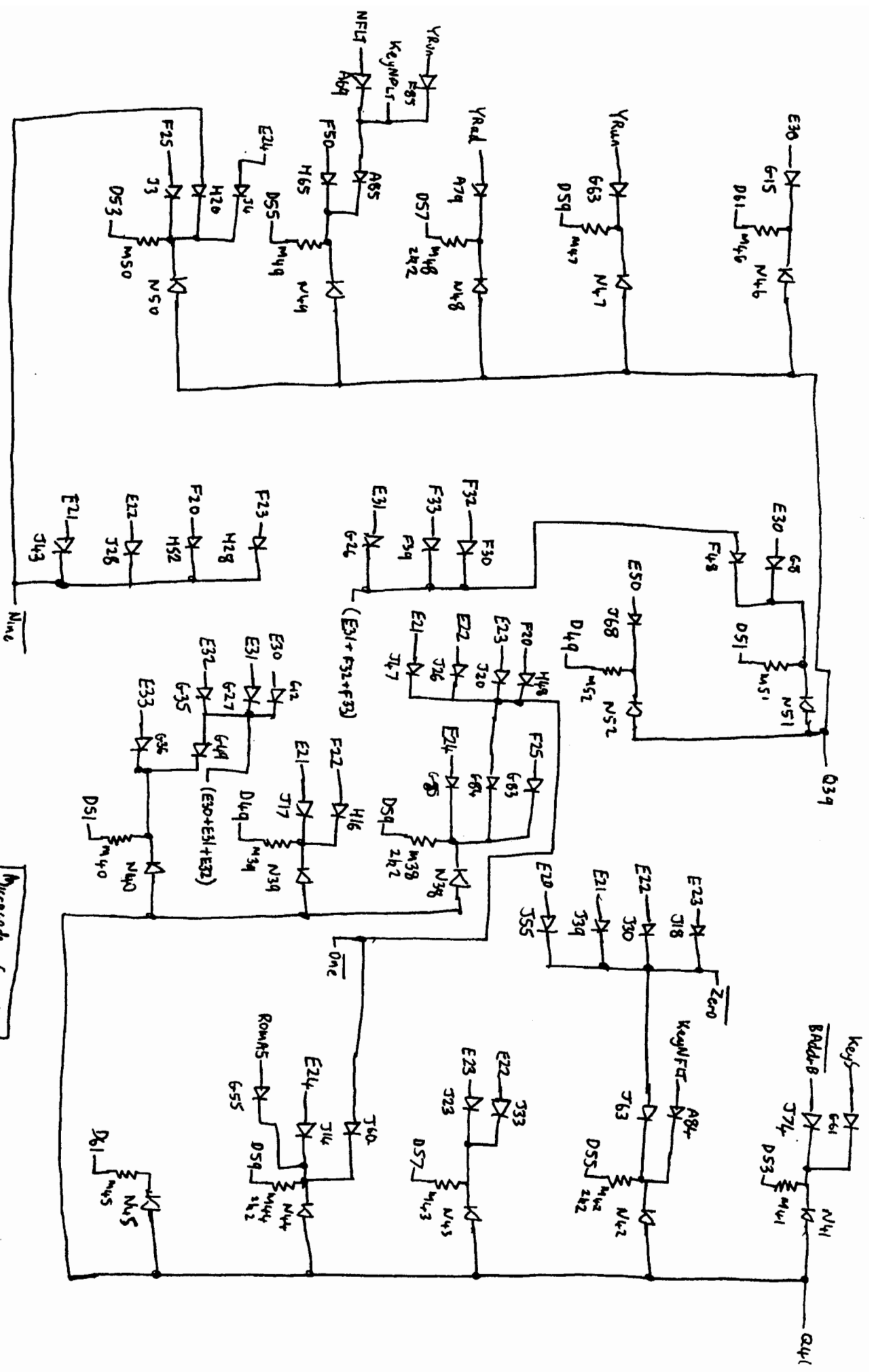


HIP — M — K — Ram ACl/E_h
 NIO NIO

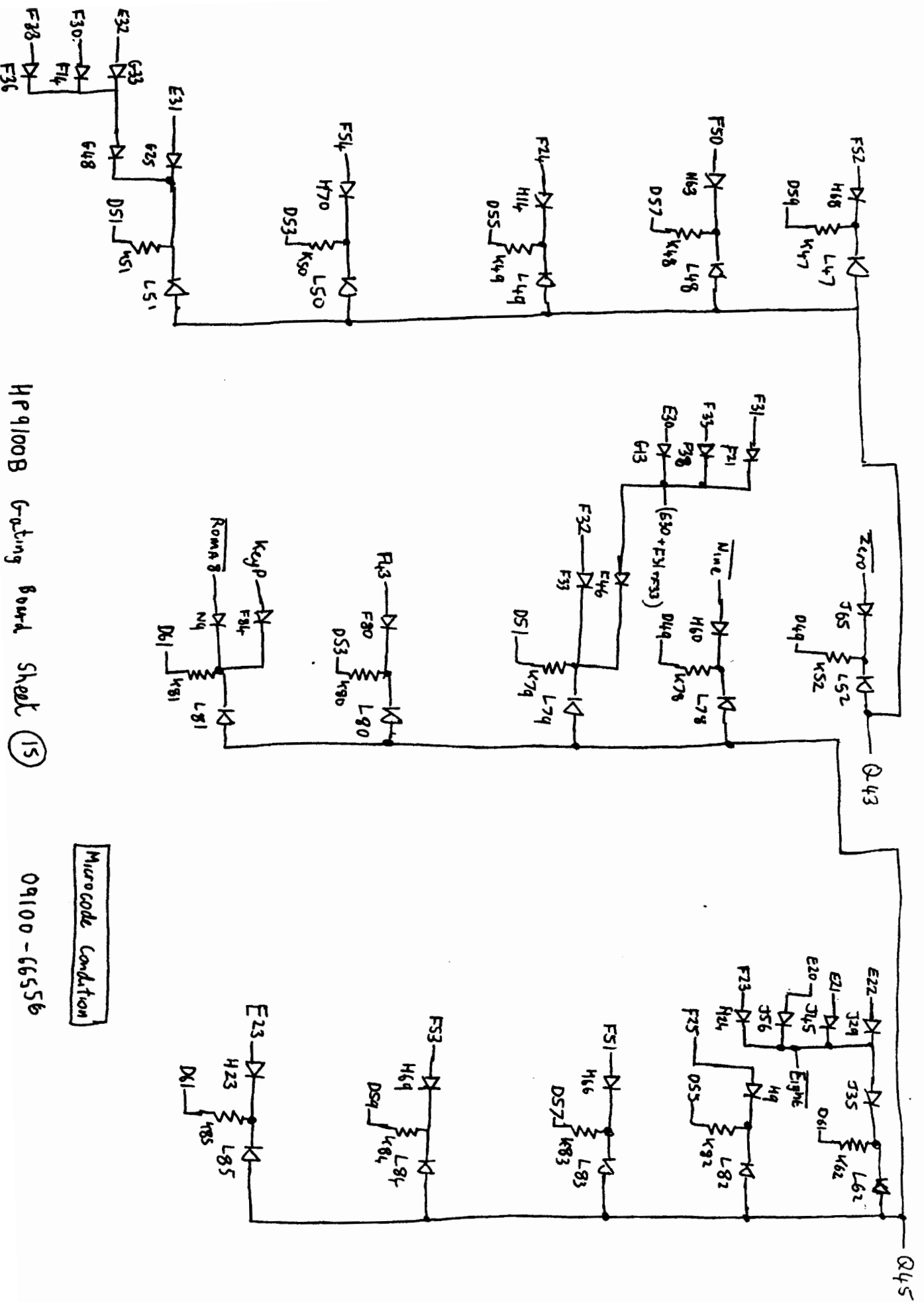


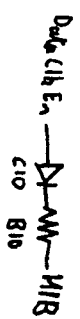
Program Sequence

Microscope Branch

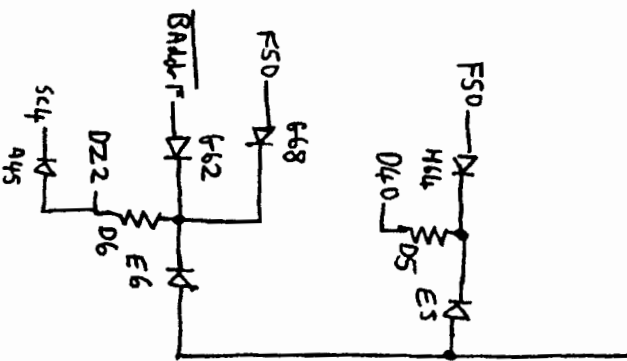
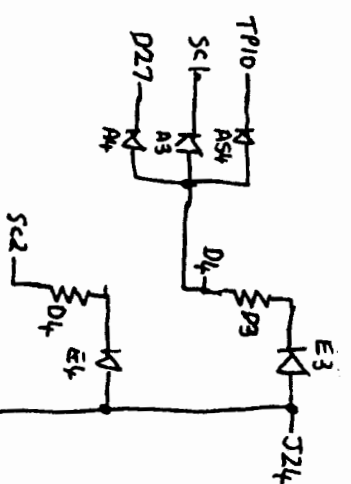
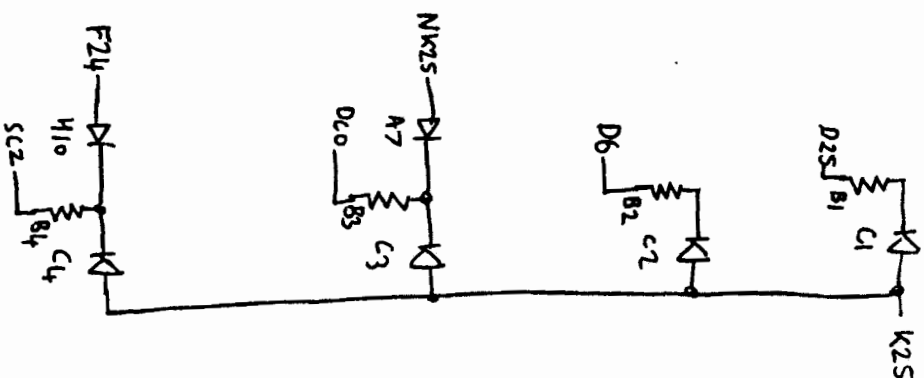
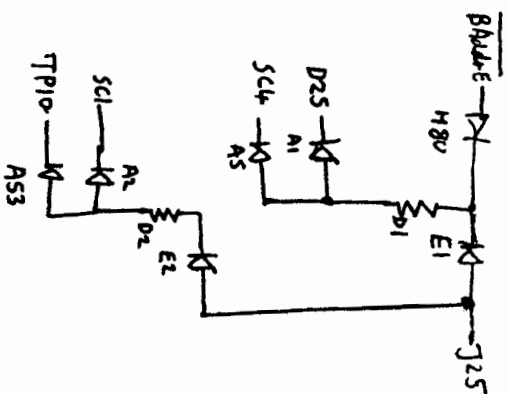


MP91008 Coding Board Sheet (14) Microcode Combinator 09100 - 66556

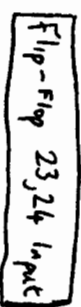




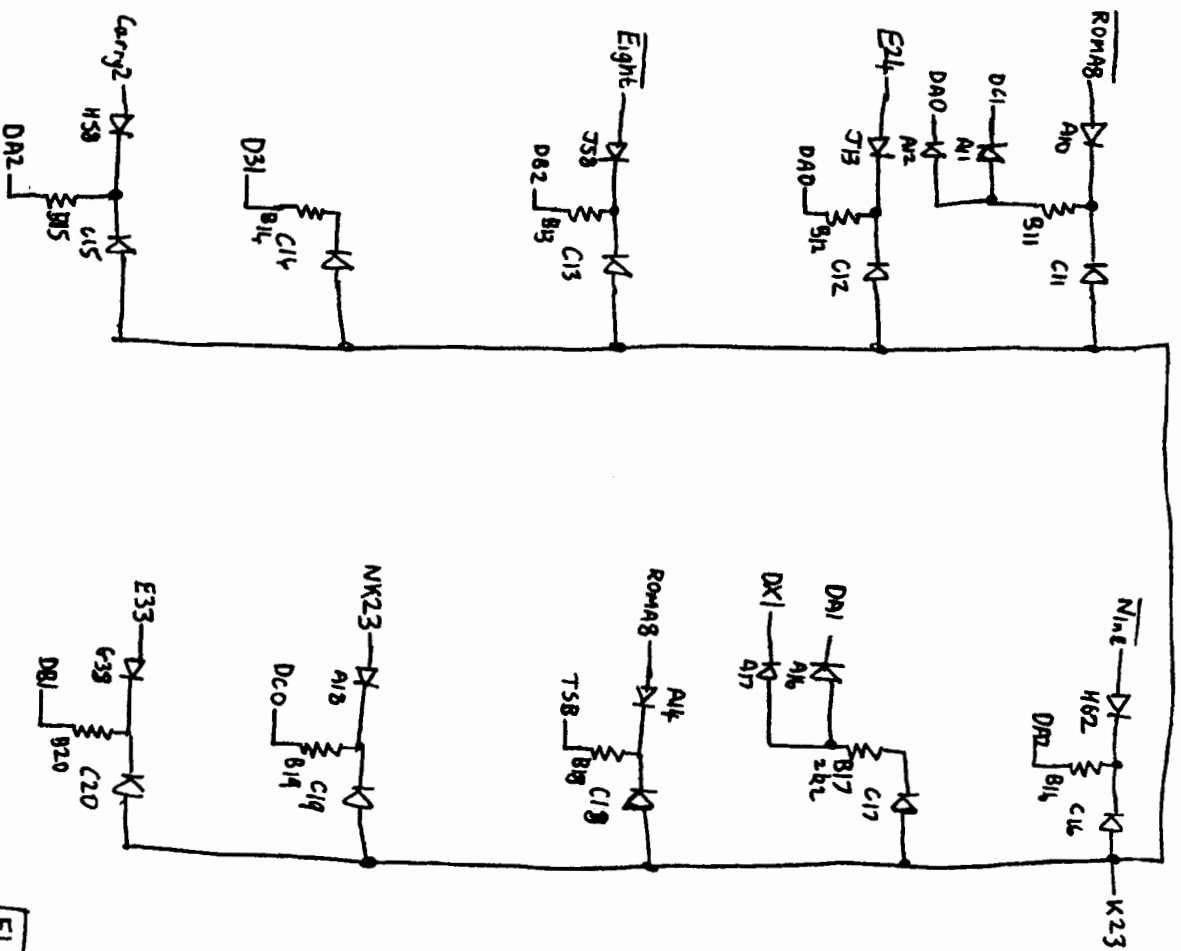
09100-66556



Flip-Flop 2425 Input



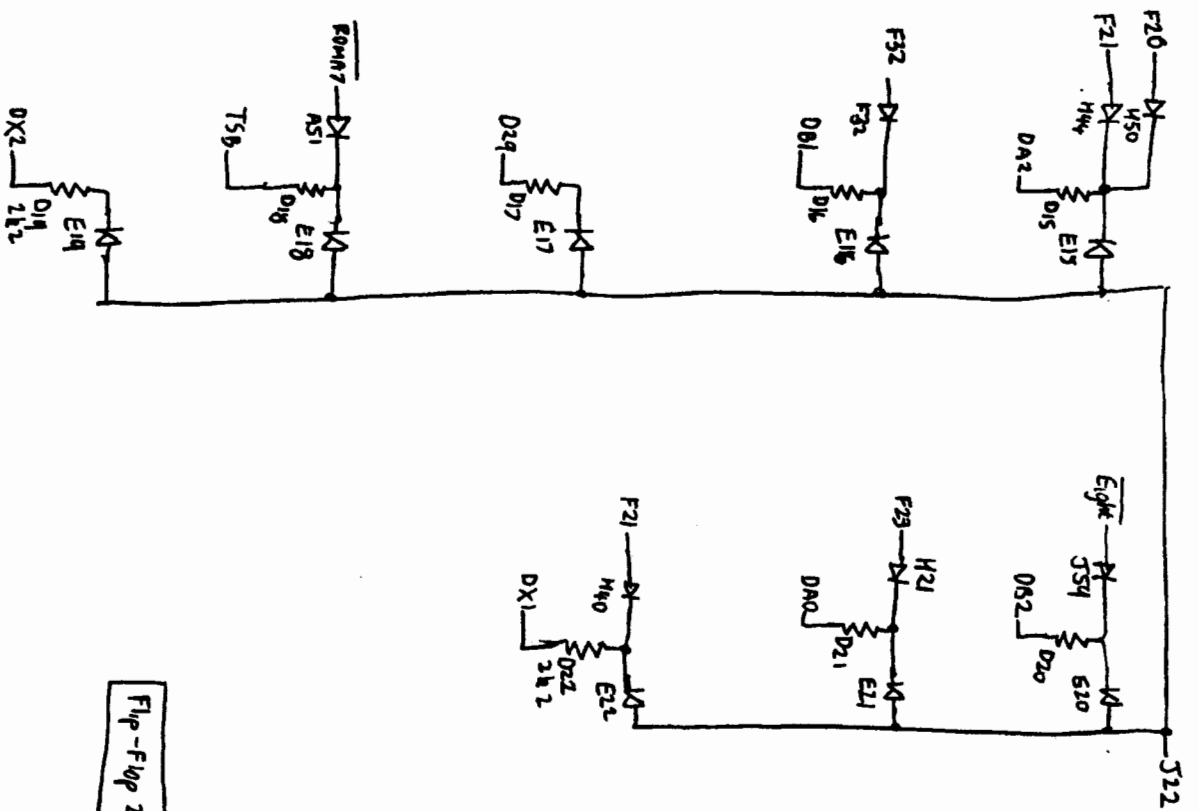
HP9100B Grating Board Sheet ⑮ 09100-86556



Flip-Flop 23 K input

HP4100B Gate Board sheet 19

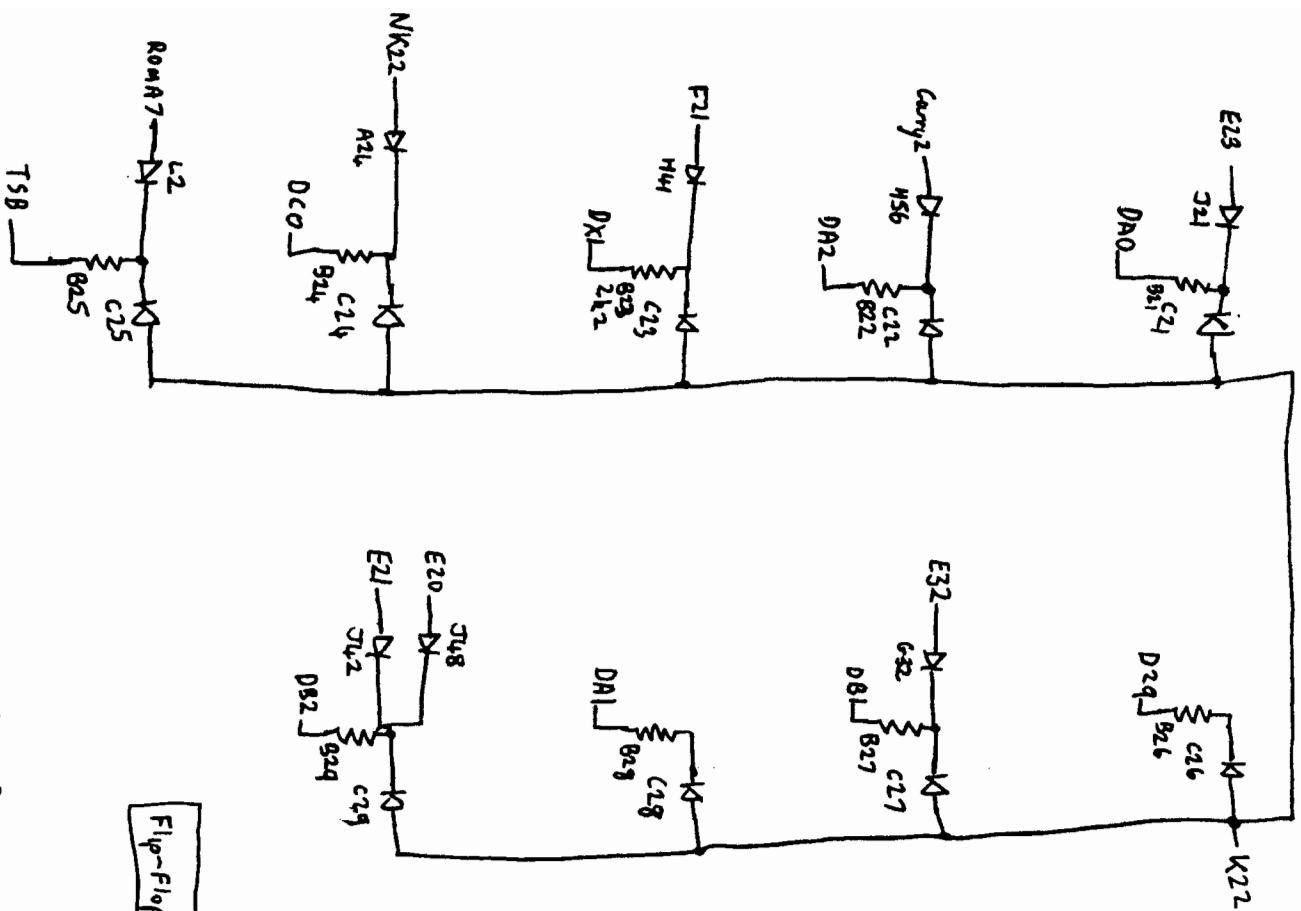
09100-66556



Flip-Flop 22 J input

HP9100B Editing Board Smt (20)

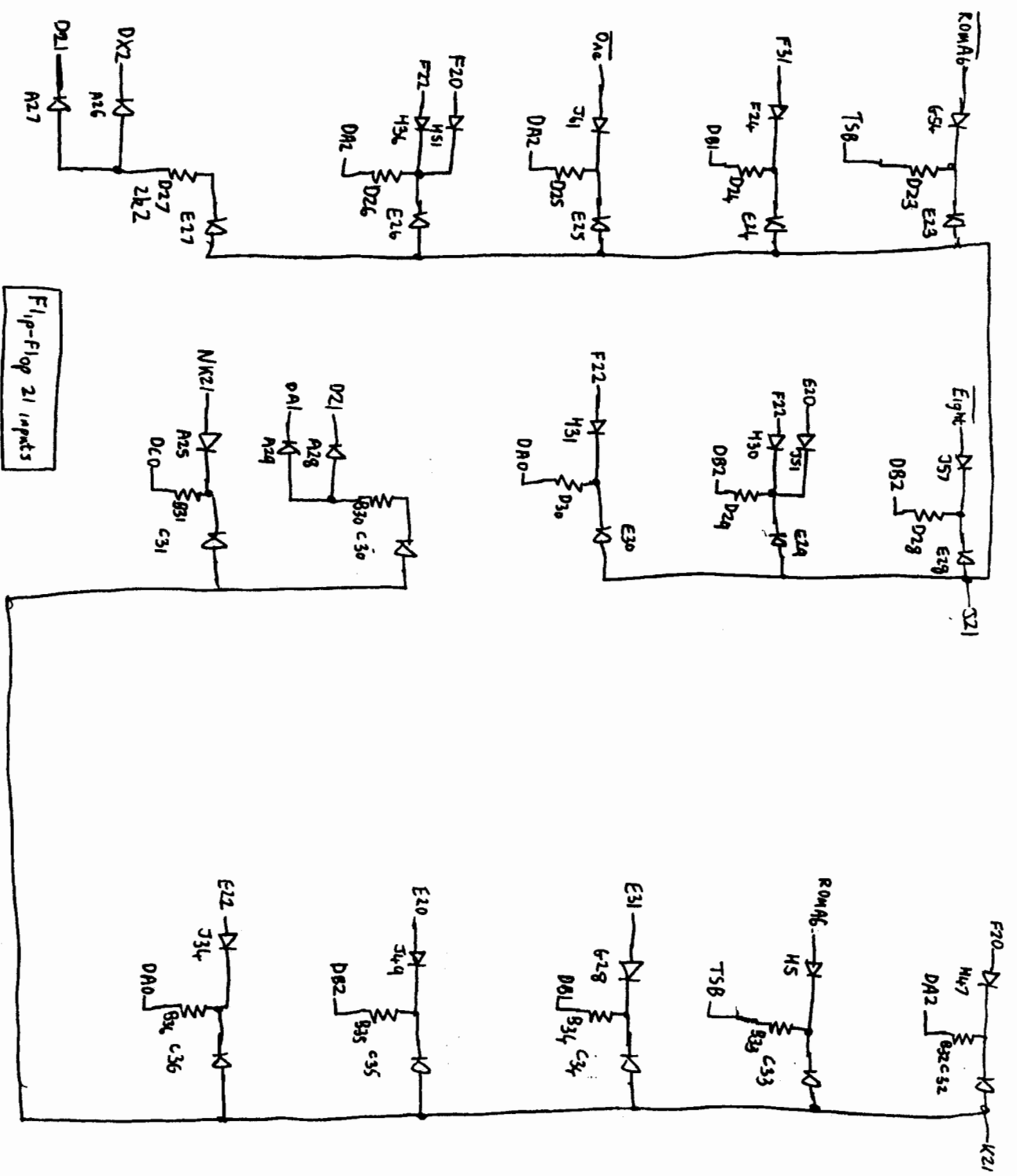
09100-66556



Flip-Flop 22 K19pk

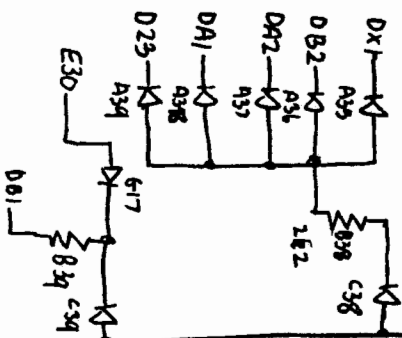
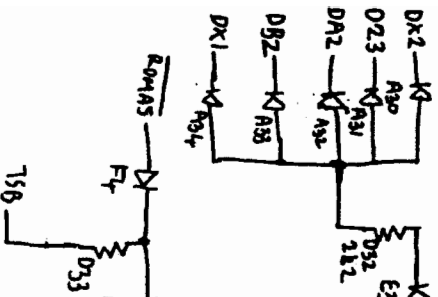
HP 9100B Gating Board Sket (21)

04100-66556



HPQ100B eating Board sheet (22)

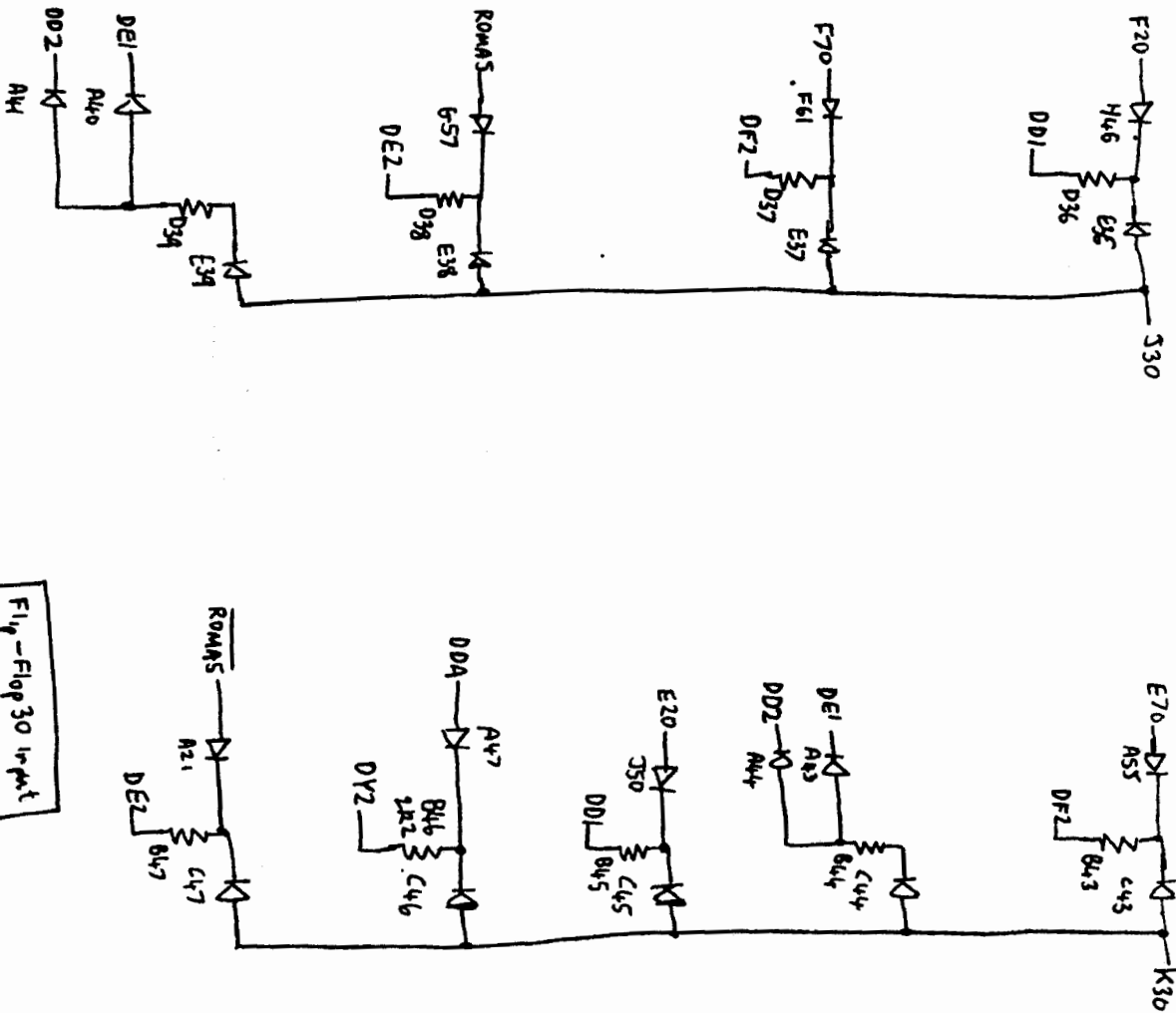
09100-66556



Flip-Flop 20 Input

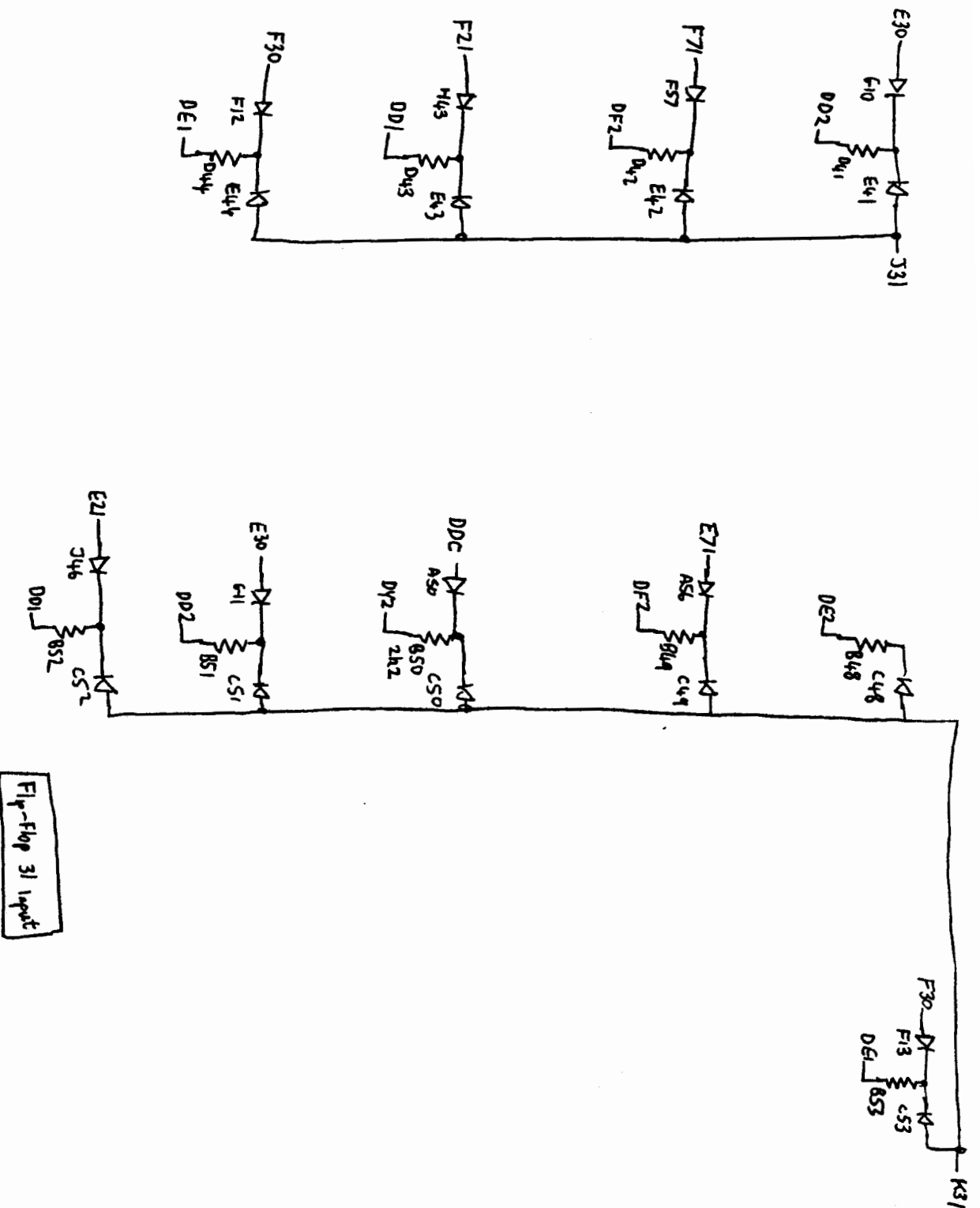
HP9100B Gating Board Slot. (23)

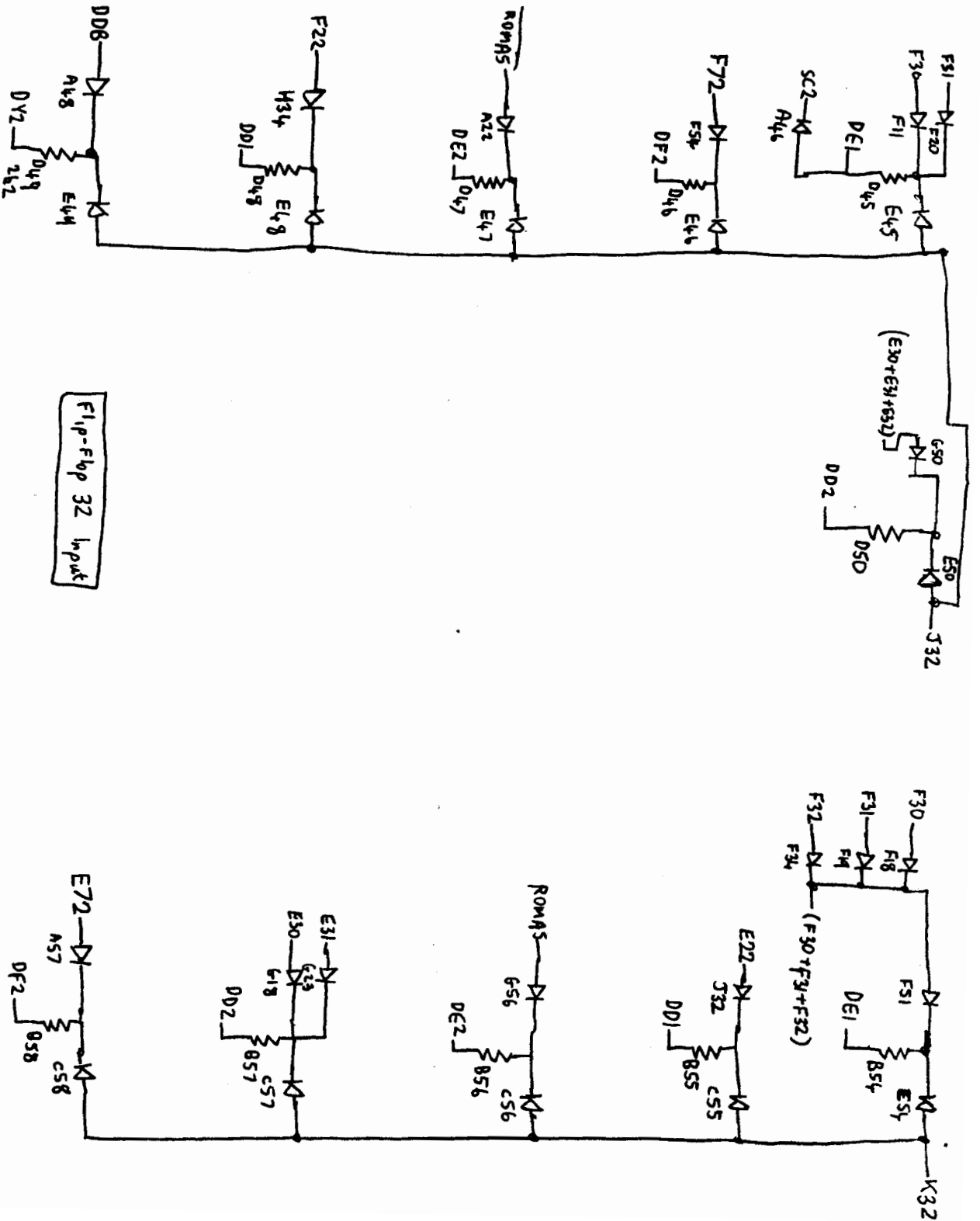
09100-66556

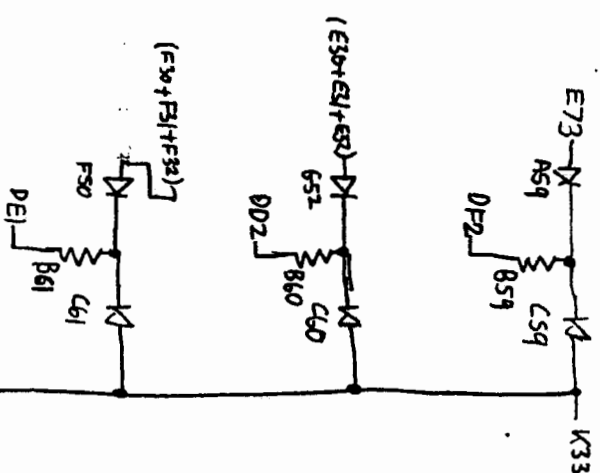
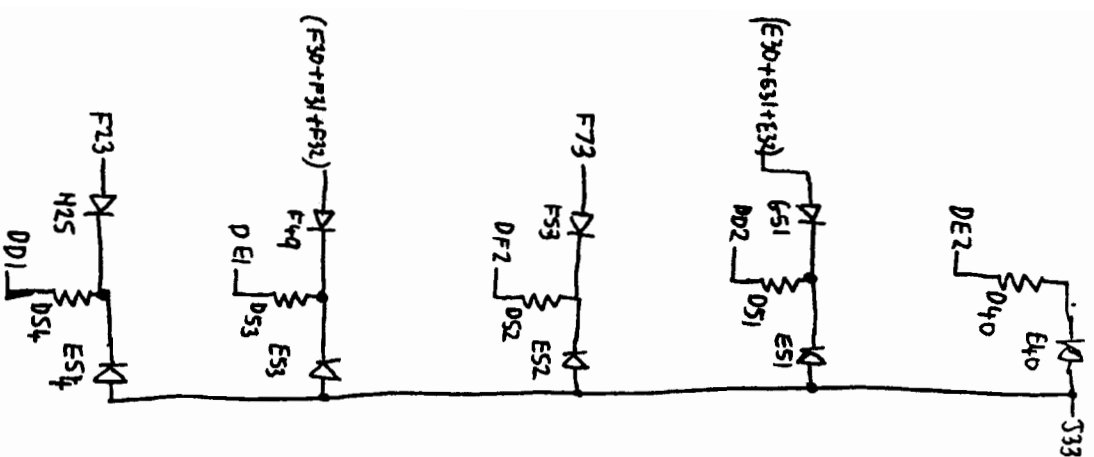


HP9100B Grounding Board Sheet (24)

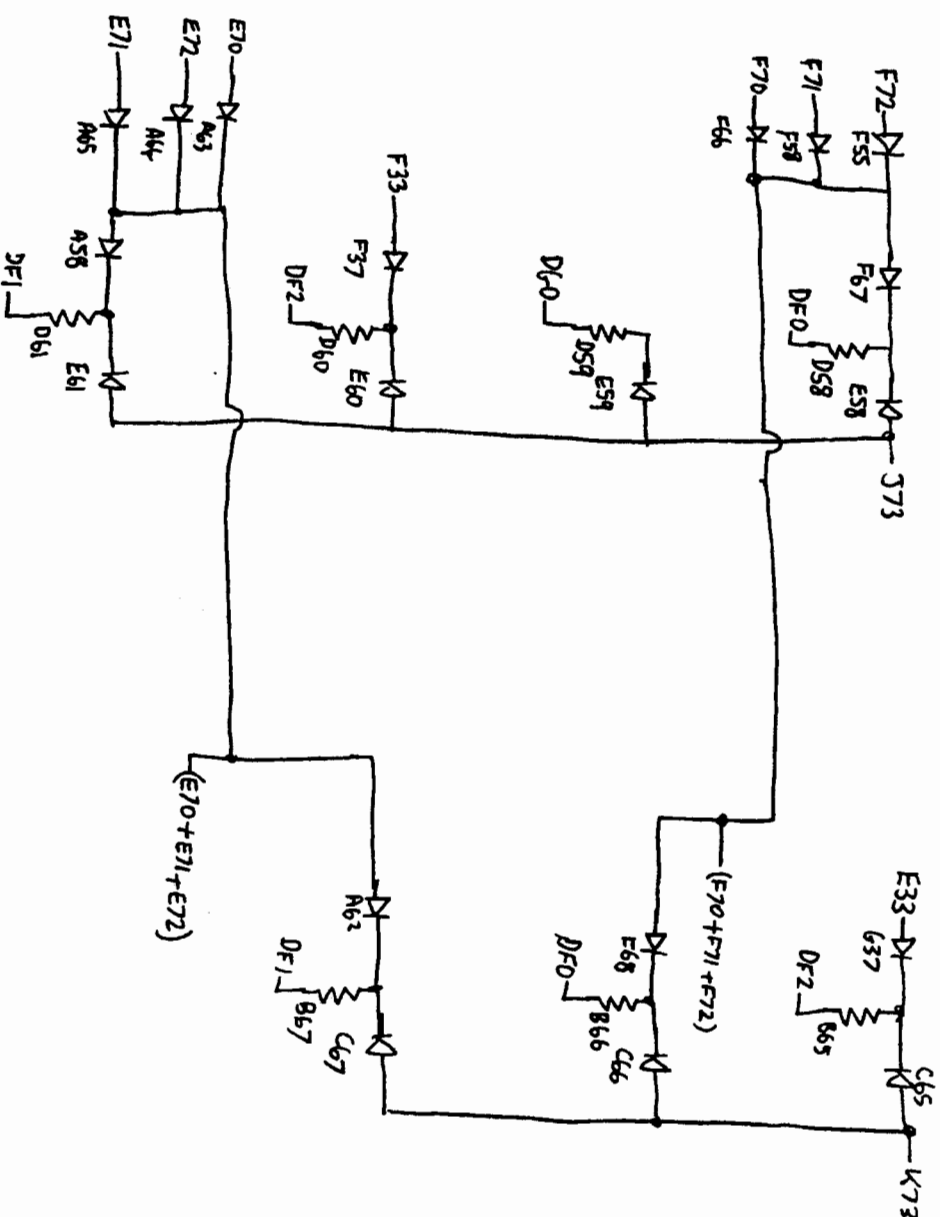
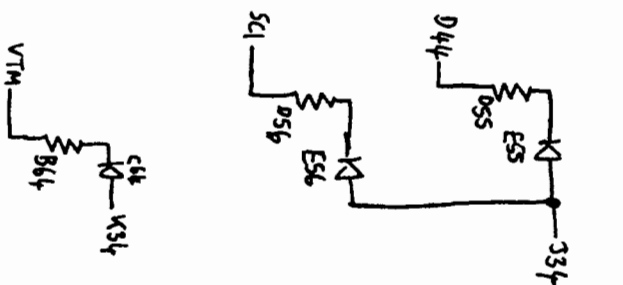
09100-66556





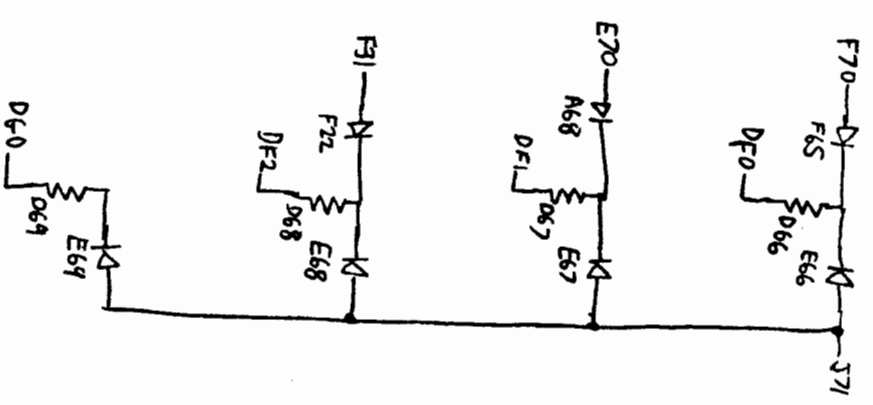
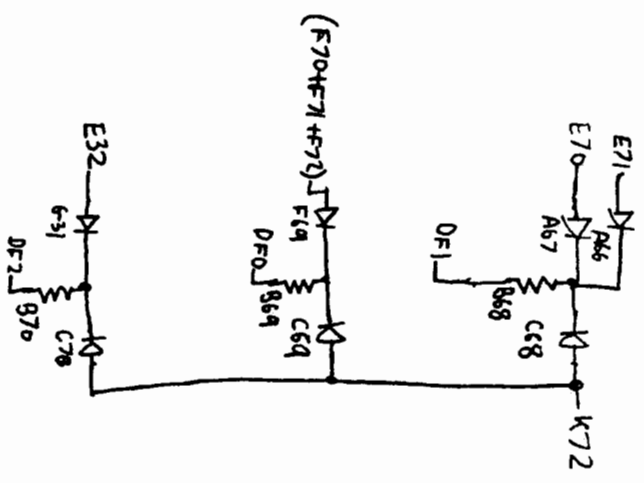
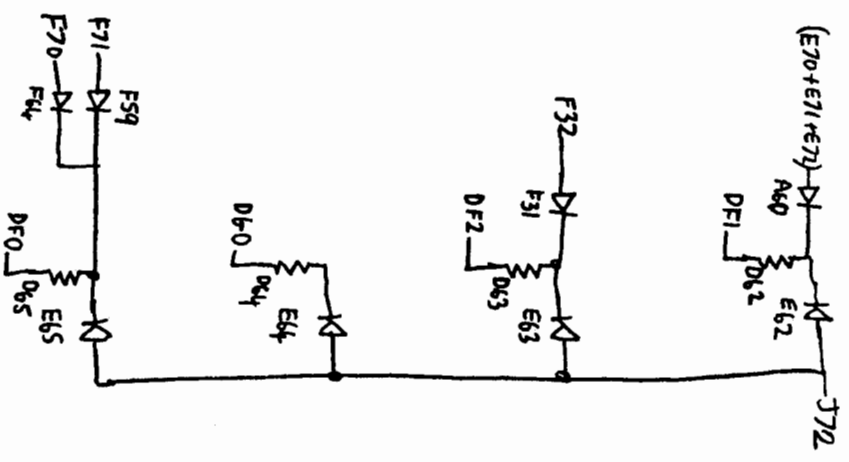


Flip-Flop 33 Input

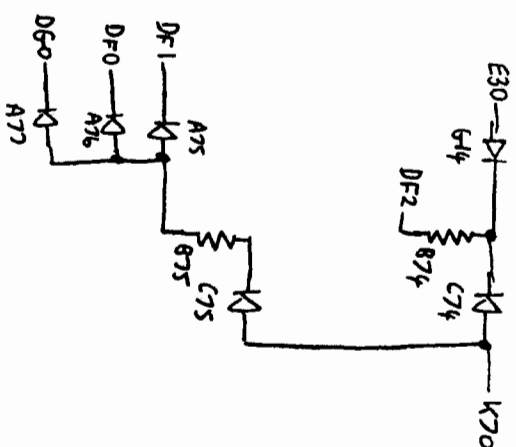
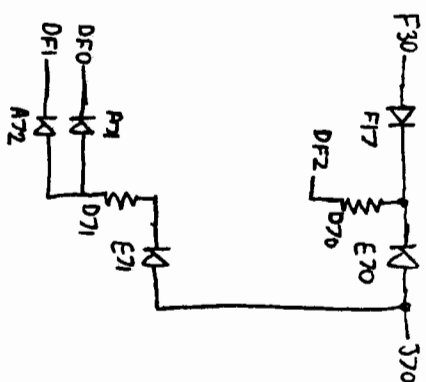
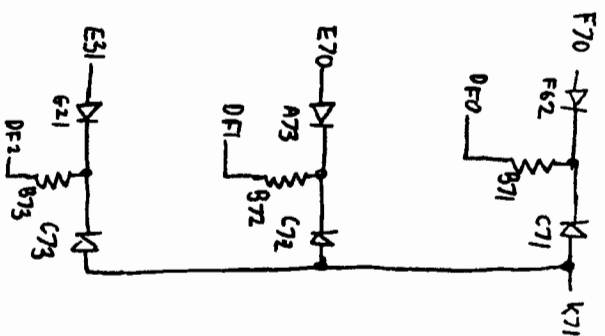


F1p-F1op 34 Inpt

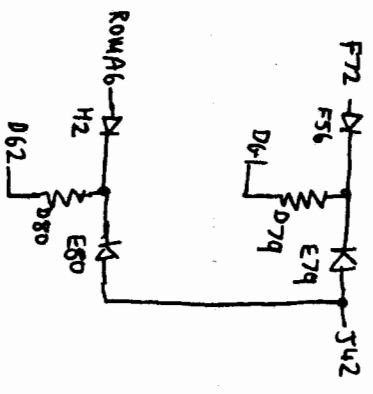
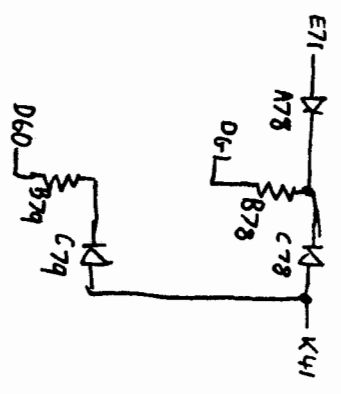
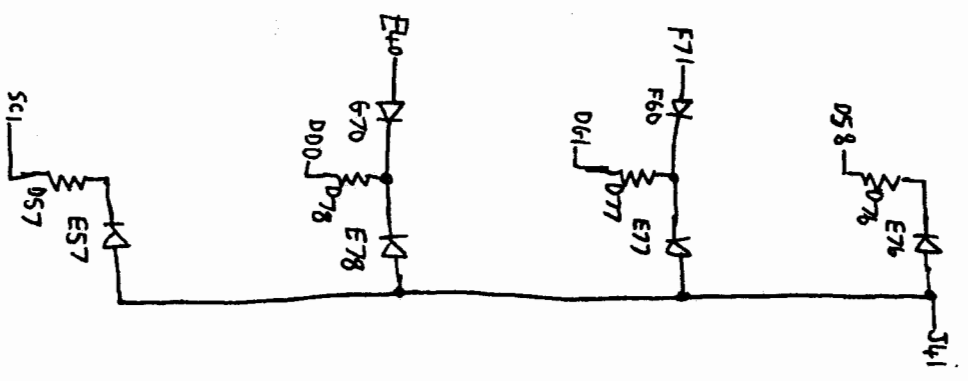
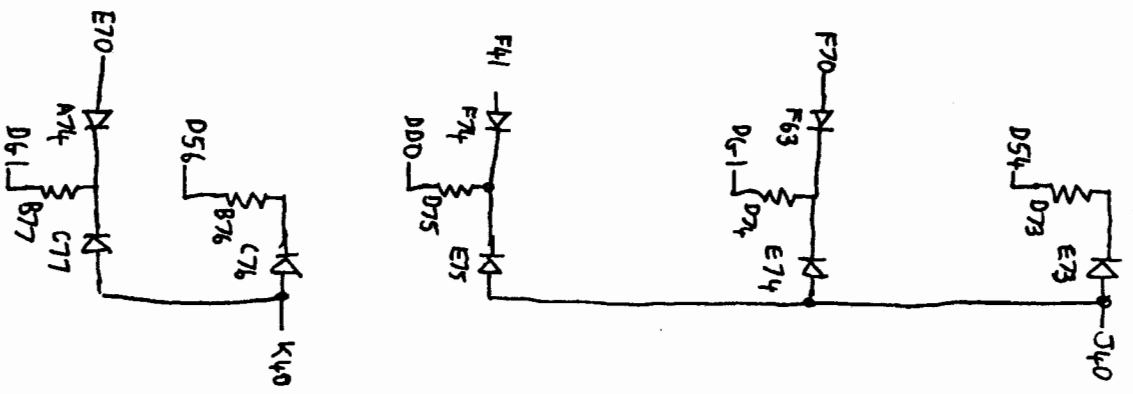
F1p-F1op 73 Inpt



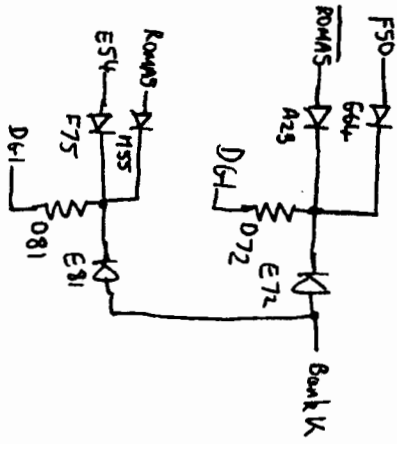
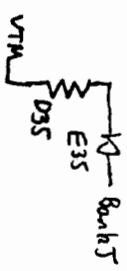
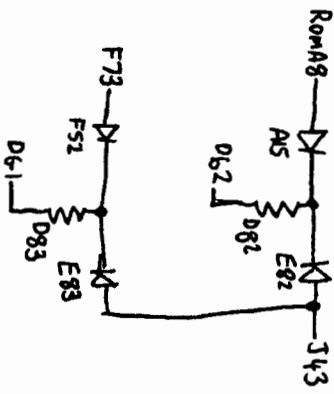
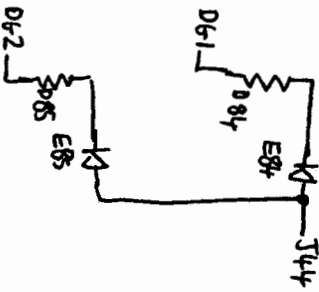
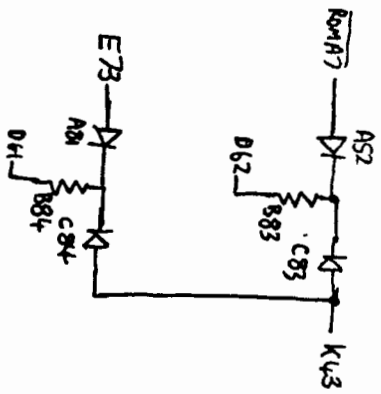
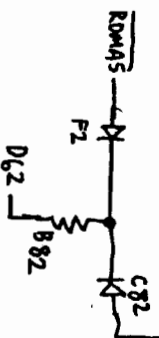
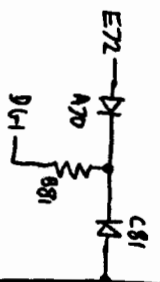
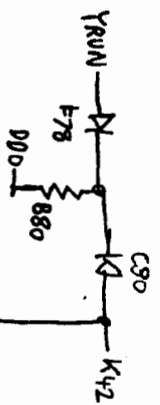
F11p-F10p 71,72 input



F_{1ip}-F_{1op} 70,71 Input

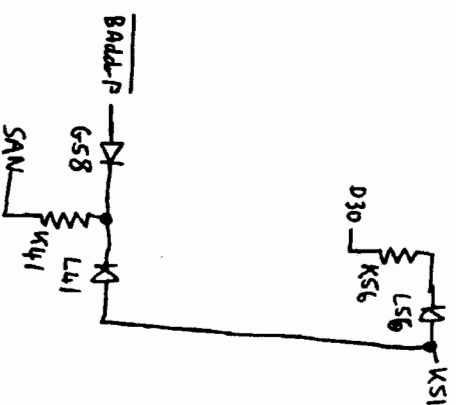
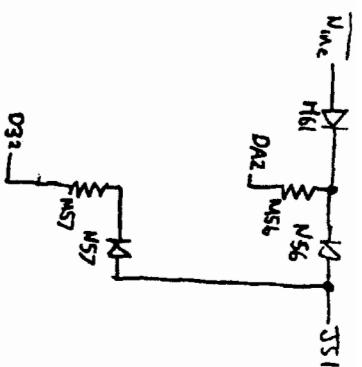
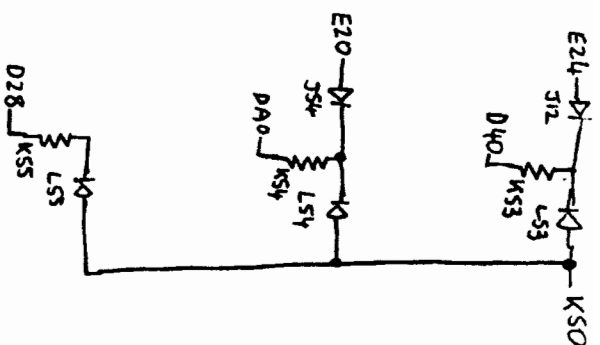
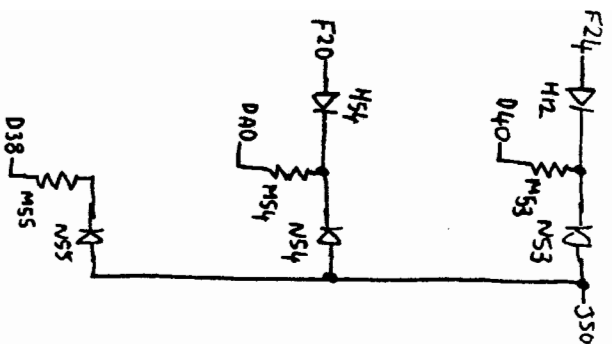


Flip-Flop 40, 41, 42 input

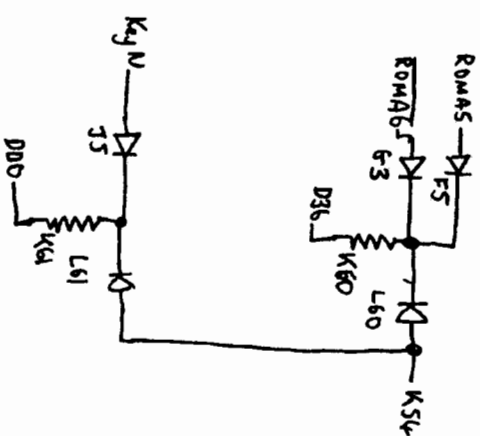
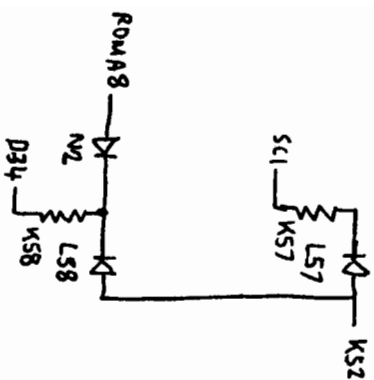
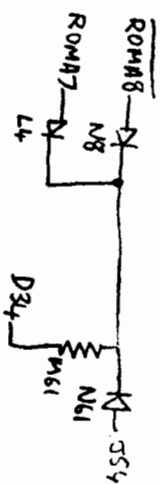
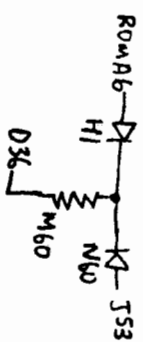
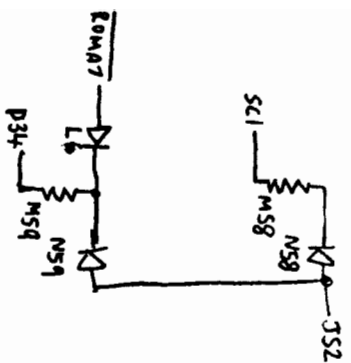


Flip-Flop 42, 43, 44 Input

Bank Select



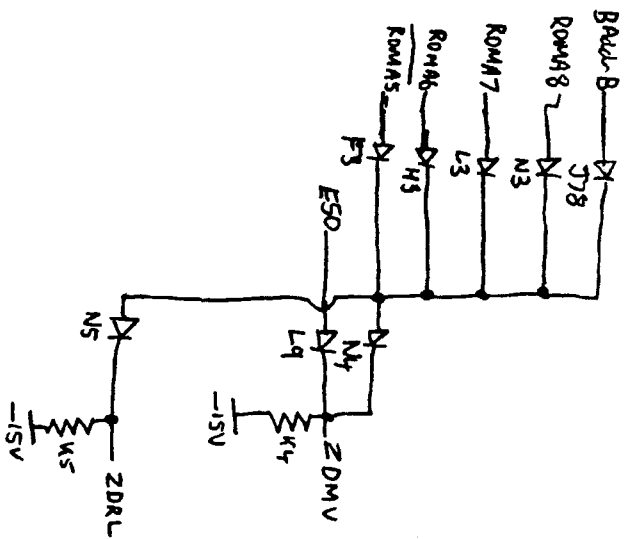
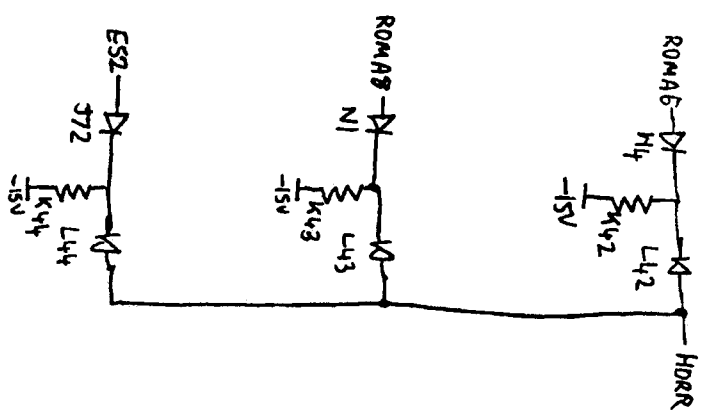
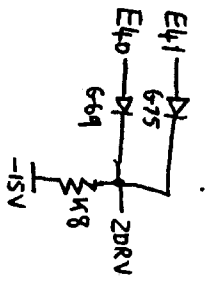
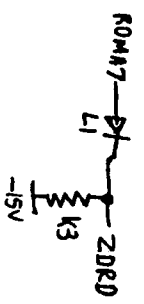
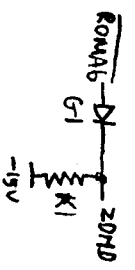
Flip-Flop 50, 51 Input



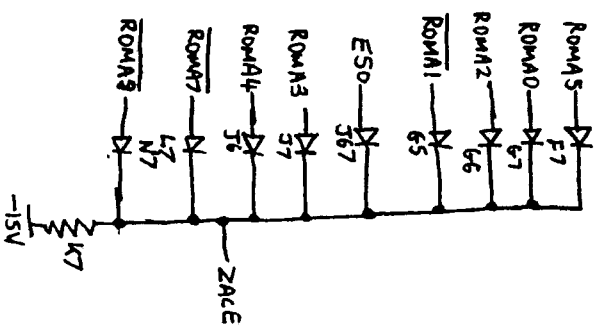
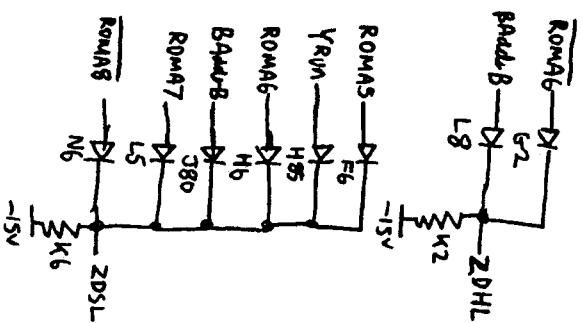
Flip-Flop 52,53,54 input

HP9100B Cutting Board Sheet 34

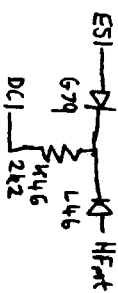
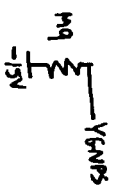
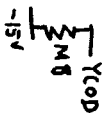
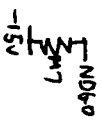
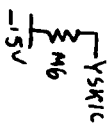
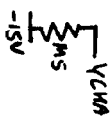
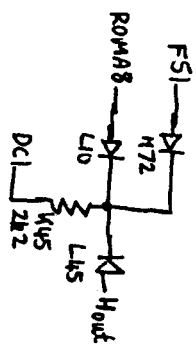
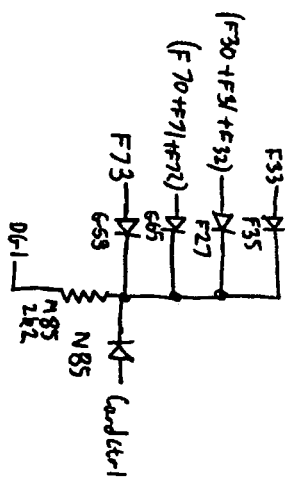
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Display Gates



Display Gates



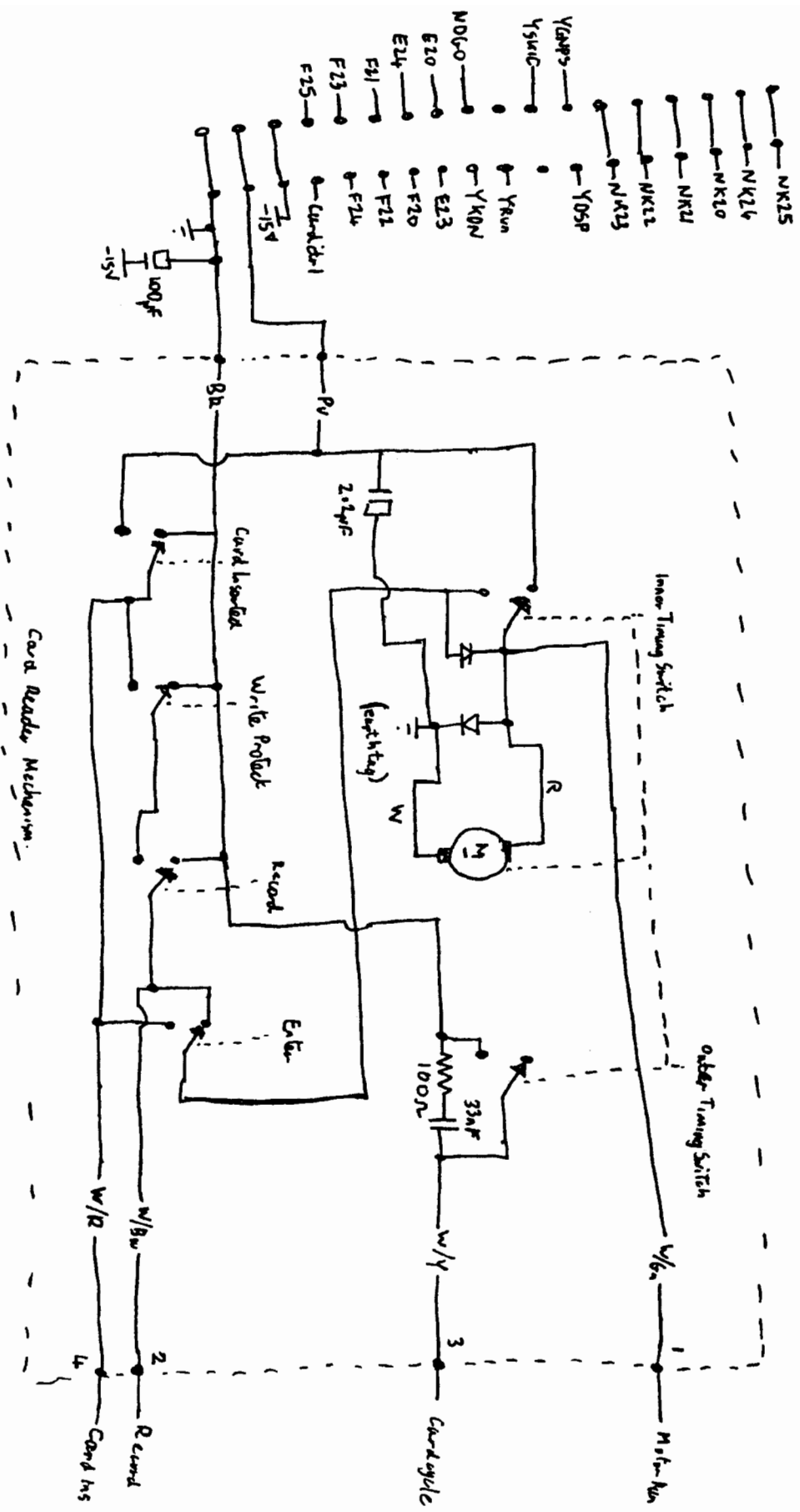
Peripheral Gates

HP9100B

Gating Board Sheet 37

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→ Component side
↑ LMS



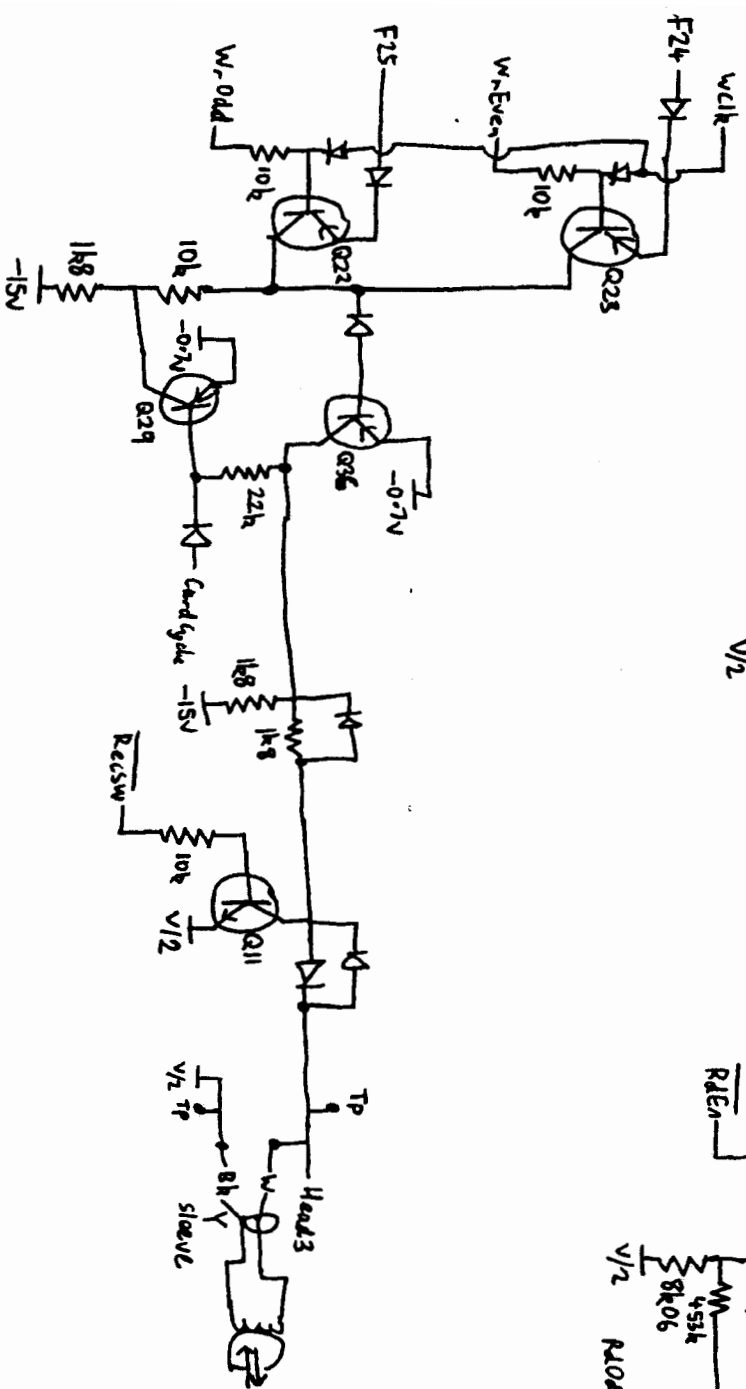
HP9100B Card Reader sheet ①

Card Reader Wiring

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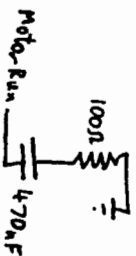


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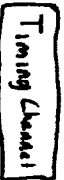
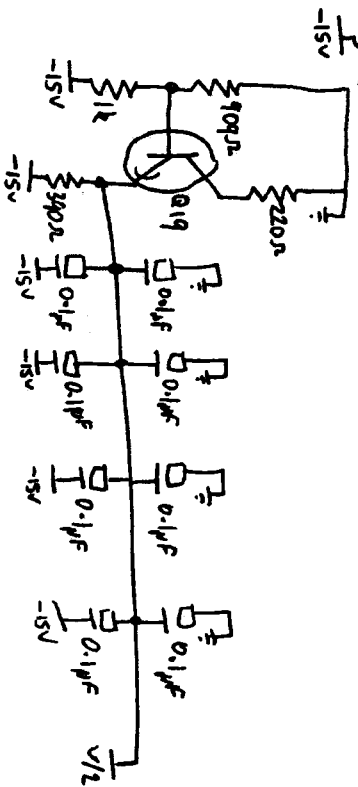


HP91008 Capt. Reader Sheet ④

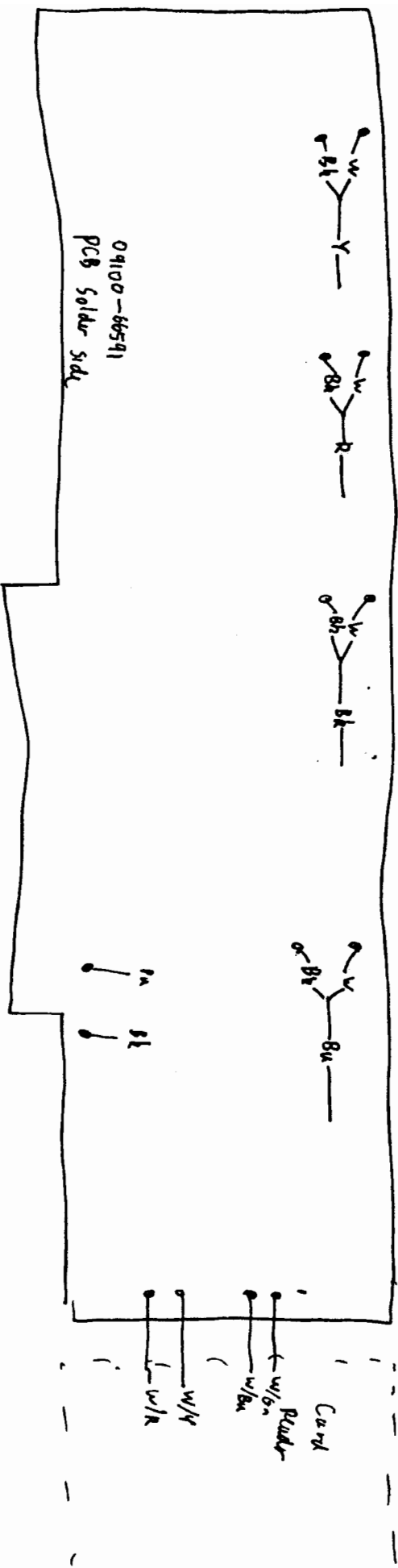
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Write Control



HP9100B Card Reader sheet ⑦



HP9100B Card Reader PCB wiring

